

	Type	Hits	Search Text	DBs	Time Stamp
1	BRS	12455	707/1-5,10.ccls.	US-PGPUB; USPAT	2004/11/05 15:46
2	BRS	9085	707/100-104.1.ccls.	US-PGPUB; USPAT	2004/11/05 15:45
3	BRS	4842	707/200-205.ccls.	US-PGPUB; USPAT	2004/11/05 15:45
4	BRS	21331	707/1-5,10.ccls. or 707/100-104.1.ccls. or 707/200-205.ccls.	US-PGPUB; USPAT	2004/11/10 08:09
5	BRS	1691	715/500,501.1.ccls.	US-PGPUB; USPAT	2004/11/10 08:06
6	BRS	21331	707/1-5,10.ccls. or 707/100-104.1.ccls. or 707/200-205.ccls.	US-PGPUB; USPAT	2004/11/06 13:12
7	BRS	1691	715/500,501.1.ccls.	US-PGPUB; USPAT	2004/11/07 09:18
8	BRS	12148	(execut\$4 with access) and (file\$2 with process\$4)	US-PGPUB; USPAT	2004/11/06 13:26
9	BRS	116	(user adj defin\$4 with process\$3) and ((hold\$4 or stor\$4) with previous\$3 with defin\$4)	US-PGPUB; USPAT	2004/11/07 10:54
10	BRS	463	(defin\$4 with process\$2 with execut\$4) and (file\$2 with access\$4) and trigger\$2	US-PGPUB; USPAT	2004/11/06 13:43
11	BRS	6	((execut\$4 with access) and (file\$2 with process\$4)) and ((user adj defin\$4 with process\$3) and ((hold\$4 or stor\$4) with previous\$3 with defin\$4))	US-PGPUB; USPAT	2004/11/06 13:17
12	BRS	1134	(execut\$4 near2 access) and (file\$2 near2 process\$4)	US-PGPUB; USPAT	2004/11/06 13:27
13	BRS	24	(execut\$4 near2 access) and (file\$2 near2 process\$4) and ((defin\$4 with process\$2 with execut\$4) and (file\$2 with access\$4) and trigger\$2)	US-PGPUB; USPAT	2004/11/06 13:42
14	BRS	2619	(access\$4 adj file) and (file\$2 near2 process\$4)	US-PGPUB; USPAT	2004/11/09 09:02
15	BRS	32	(defin\$4 adj2 process\$2 with execut\$4) and (file\$2 near2 access\$4) and trigger\$2	US-PGPUB; USPAT	2004/11/06 13:45
16	BRS	1	(defin\$4 adj2 process\$2 with execut\$4) and (file\$2 near2 access\$4) and trigger\$2 and ((access\$4 adj file) and (file\$2 near2 process\$4) )	US-PGPUB; USPAT	2004/11/06 13:44

	Type	Hits	Search Text	DBs	Time Stamp
17	BRS	114	(defin\$4 adj2 process\$2 with execut\$4) and (file\$2 near2 access\$4)	US-PGPUB; USPAT	2004/11/06 13:46
18	BRS	9	(defin\$4 adj2 process\$2 with execut\$4) and (file\$2 near2 access\$4) and ((access\$4 adj file) and (file\$2 near2 process\$4) )	US-PGPUB; USPAT	2004/11/07 10:52
19	BRS	51	(defin\$4 adj2 (process\$2 or program\$2 or application\$2) with execut\$4) and (file\$2 near2 access\$4) and ((access\$4 adj file) and (file\$2 near2 process\$4) )	US-PGPUB; USPAT	2004/11/06 14:06
20	BRS	19	(defin\$4 adj2 (process\$2 or program\$2 or application\$2) with execut\$4) and (file\$2 near2 access\$4) and ((access\$4 adj file) and (file\$2 near2 process\$4) ) and trigger\$2	US-PGPUB; USPAT	2004/11/06 14:06
21	BRS	21332	707/1-5,10.ccls. or 707/100-104.1.ccls. or 707/200-205.ccls.	US-PGPUB; USPAT	2004/11/09 08:50
22	BRS	1691	715/500,501.1.ccls.	US-PGPUB; USPAT	2004/11/09 08:51
23	BRS	114	(defin\$4 adj2 process\$2 with execut\$4) and (file\$2 near2 access\$4)	US-PGPUB; USPAT	2004/11/07 10:53
24	BRS	817	(defin\$4 adj2 process\$2) and (file\$2 near2 access\$4)	US-PGPUB; USPAT	2004/11/07 10:53
25	BRS	116	(user adj defin\$4 with process\$3) and ((hold\$4 or stor\$4) with previous\$3 with defin\$4)	US-PGPUB; USPAT	2004/11/07 10:55
26	BRS	301	file with manag\$4 with data adj area\$2	US-PGPUB; USPAT	2004/11/07 10:56
27	BRS	3	file with manag\$4 with data adj area\$2 and ((meta adj data) or meta\$2data with area) and format\$4	US-PGPUB; USPAT	2004/11/07 10:58
28	BRS	4	file with manag\$4 with data adj area\$2 and ((meta adj data) or meta\$2data) and format\$4	US-PGPUB; USPAT	2004/11/07 11:00
29	BRS	14	file with manag\$4 with data with area\$2 and ((meta adj data) or meta\$2data) and format\$4	US-PGPUB; USPAT	2004/11/07 11:00

	Type	Hits	Search Text	DBs	Time Stamp
30	BRS	21369	707/1-5,10.ccls. or 707/100-104.1.ccls. or 707/200-205.ccls.	US-PGPUB; USPAT	2004/11/09 08:51
31	BRS	1692	715/500,501.1.ccls.	US-PGPUB; USPAT	2004/11/09 08:51
32	BRS	2622	(access\$4 adj file) and (file\$2 near2 process\$4)	US-PGPUB; USPAT	2004/11/09 09:03
33	BRS	37	(determin\$4 with file adj type\$2) and creat\$4 and (tree adj2 structur\$2)	US-PGPUB; USPAT	2004/11/09 09:05
34	BRS	12	(determin\$4 with file adj type\$2) and creat\$4 and (tree adj2 structur\$2) and ((access\$4 adj file) and (file\$2 near2 process\$4) )	US-PGPUB; USPAT	2004/11/09 09:09
35	BRS	2	(determin\$4 with file adj type\$2) and creat\$4 and (tree adj2 structur\$2) and ((access\$4 adj file) and (file\$2 near2 process\$4) ) and (parent with director\$4)	US-PGPUB; USPAT	2004/11/09 09:39
36	BRS	667	set\$4 with change adj flag\$4	US-PGPUB; USPAT	2004/11/09 09:39
37	BRS	136	(set\$4 with change adj flag\$4) and (high adj2 level)	US-PGPUB; USPAT	2004/11/09 09:41
38	BRS	1	(set\$4 with change adj flag\$4) and (high adj2 level) and ((access\$4 adj file) and (file\$2 near2 process\$4) )	US-PGPUB; USPAT	2004/11/09 09:40
39	BRS	17	((707/1-5,10.ccls. or 707/100-104.1.ccls. or 707/200-205.ccls. ) or 715/500,501.1.ccls. ) and ((set\$4 with change adj flag\$4))	US-PGPUB; USPAT	2004/11/09 09:44
40	BRS	5658	(application adj program adj interface)	US-PGPUB; USPAT	2004/11/09 09:46
41	BRS	33	(application adj program adj interface) and kernal	US-PGPUB; USPAT	2004/11/09 09:46
42	BRS	29	(application adj program adj interface) and kernal and user and area\$2	US-PGPUB; USPAT	2004/11/09 09:47
43	BRS	25	(application adj program adj interface) and kernal and user and area\$2 and layer	US-PGPUB; USPAT	2004/11/09 09:47

	Type	Hits	Search Text	DBs	Time Stamp
44	BRS	1	(application adj program adj interface) and kernal and user and area\$2 and (layer with structure)	US-PGPUB; USPAT	2004/11/09 09:48
45	BRS	23	(application adj program adj interface) and kernal and user and area\$2 and layer\$4 and structure\$2	US-PGPUB; USPAT	2004/11/09 10:05
46	BRS	265	(application adj program adj interface) and user and area\$2 and layer\$4 with structure\$2	US-PGPUB; USPAT	2004/11/09 10:00
47	BRS	103	(application adj program adj interface) and user with area\$2 and layer\$4 with structure\$2	US-PGPUB; USPAT	2004/11/09 10:00
48	BRS	16	((707/1-5,10.ccls. or 707/100-104.1.ccls. or 707/200-205.ccls. ) or 715/500,501.1.ccls. ) and ((application adj program adj interface) and (user with area\$2) and (layer\$4 with structure\$2))	US-PGPUB; USPAT	2004/11/09 10:01
49	BRS	29	(application adj program adj interface) and kernal and user and area\$2	US-PGPUB; USPAT	2004/11/09 10:06
50	BRS	2	((707/1-5,10.ccls. or 707/100-104.1.ccls. or 707/200-205.ccls. ) or 715/500,501.1.ccls. ) and ((application adj program adj interface) and kernal and user and area\$2 )	US-PGPUB; USPAT	2004/11/09 10:07
51	BRS	1692	715/500,501.1.ccls.	US-PGPUB; USPAT	2004/11/10 08:06
52	BRS	11859	709/200,201,202,203,217,218,219.ccls.	US-PGPUB; USPAT	2004/11/10 08:07
53	BRS	4786	711/100,111-115.ccls.	US-PGPUB; USPAT	2004/11/10 08:08
54	BRS	1395	714/1,2,15,16,100.ccls.	US-PGPUB; USPAT	2004/11/10 08:09
55	BRS	19292	S52 or S53 or S54 or S51	US-PGPUB; USPAT	2004/11/10 08:09
56	BRS	21369	707/1-5,10.ccls. or 707/100-104.1.ccls. or 707/200-205.ccls.	US-PGPUB; USPAT	2004/11/10 08:09

File 344:Chinese Patents Abs Aug 1985-2004/May  
 (c) 2004 European Patent Office  
 File 347:JAPIO Nov 1976-2004/May(Updated 040903)  
 (c) 2004 JPO & JAPIO

**\*File 347: JAPIO data problems with year 2000 records are now fixed.**

Alerts have been run. See HELP NEWS 347 for details.

File 348:EUROPEAN PATENTS 1978-2004/Sep W02

(c) 2004 European Patent Office

File 349:PCT FULLTEXT 1979-2002/UB=20040923,UT=20040916

(c) 2004 WIPO/Univentio

File 350:Derwent WPIX 1963-2004/UD,UM &UP=200461

(c) 2004 Thomson Derwent

**\*File 350: For more current information, include File 331 in your search.**

Enter HELP NEWS 331 for details.

Set	Items	Description
S1	10047	AU=(TAMURA M? OR TAMURA, M? OR TAKE R? OR TAKE, R? OR NOGU-
		CHI Y? OR NOGUCHI, Y? OR OGIHARA K? OR OGIHARA, K?)
S2	3397	S1 AND (READ??? OR WRIT??? OR EXECUT??? OR OPERATING??
		OR -
		PROCESS???? OR LAUNCH???)
S3	20673	(S2 OR OPEN???) (3N) (FILE?? OR DATA?? OR SOFTWARE?? OR
		CODE?
		? OR CODING OR FOLDER?? OR FILE??()SYSTEM?? OR
		FILESYSTEM??)
S4	99	S1 AND (USER?? OR PROGRAMMER?? OR OPERATOR?? OR
		PROGRAMER?-
		?) (3N) (PROCESS??? OR OPERATION?? OR ALGORITHM?? OR
		PROGRAM???)
		OR APPLICATION?? OR PROCEDURE?? OR STEPS?? OR MODULE?? OR
		MAC-
		RO?? )
S5	25	S4 AND (HOLD??? OR RESERV??? OR STORING?? OR
		STOR??) (3N) S3
S6	77	S2 AND (EXECUT?? OR EXECUTING??) (3N) S3
S7	0	S2 AND (TRIGGER??? OR INITIAT??? OR ACTIVAT??? OR
		START???)
		) (7N) S1
S8	21	S5 AND S6
S9	21	S8 AND S3
S10	0	S9 AND IC=G06F?
S11	2	S9 AND (FILE(2N)SYSTEM??)
S12	392	S2(3N)S3
S13	12	S12(5N)S4
S14	9	S13(S) (S5 OR S6)

11/3,K/1 (Item 1 from file: 348)  
DIALOG(R)File 348:EUROPEAN PATENTS  
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01713385

Method, apparatus and program for data management  
Verfahren, Gerat und Programm zum Behandeln von Daten  
Procede, dispositif et logiciel pour de gestion de donnees  
PATENT ASSIGNEE:

FUJI PHOTO FILM CO., LTD., (202400), 210 Nakanuma Minami-Ashigara-shi,  
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Noguchi, Yukinori , Fuji Photo Film Co.Ltd. 798 Miyanodai Kaisei-machi,  
Ashigarakmi-gun, Kanagawa-ken, (JP)

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PATENT (CC, No, Kind, Date): EP 1404110 A2 040331 (Basic)

APPLICATION (CC, No, Date): EP 2003021758 030925;

PRIORITY (CC, No, Date): JP 2002281513 020926

DESIGNATED STATES: AT; BE; BG; CH; CY; CZ; DE; DK; EE; ES; FI; FR; GB; GR;  
HU; IE; IT; LI; LU; MC; NL; PT; RO; SE; SI; SK; TR

EXTENDED DESIGNATED STATES: AL; LT; LV; MK

INTERNATIONAL PATENT CLASS: H04N-001/21

ABSTRACT WORD COUNT: 105

NOTE:

Figure number on first page: NONE

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200414	351
SPEC A	(English)	200414	4031
Total word count - document A			4382
Total word count - document B			0
Total word count - documents A + B			4382

INVENTOR:

Noguchi, Yukinori ...

...ABSTRACT A2

Original data and derivative data are managed securely and  
conveniently. A **processing** unit of a computer 1 whose IP address is J1  
sends a file A as...

...A file management unit generates accompanying information comprising  
link information "file A1, URL-100" and **processing** content information  
"copying" for the file A, and generates accompanying information  
comprising link information "file A, IP address J1" and **processing**  
content information "copied from file A" for the file A1. An attachment  
unit attaches the...

...SPECIFICATION a hard disc or the like of a computer are subjected to  
various kinds of **processing** carried out by a user, such as copying,  
manipulation, and image **processing** in the case of image data.  
Therefore, data sets generated from an original data set...

...many cases in one computer. When a user wishes to carry out some kind of **processing** on one of the data sets, the user needs to know a relationship between the...

...be deleted is stored. Furthermore, in the case where the user wishes to carry out **processing** that requires time and specialized knowledge (such as image **processing** on an image **data** set), if the user knows that a data set having been subjected to the same **processing** exists, the **user** simply uses the data set to save his/her time. In other words, clarifying the...

...if one of image data sets as the master content is subjected to color conversion **processing**, image **data** sets as the subordinate content can also be subjected to the color conversion **processing**, and the original **data** set is changed. Therefore, if another type of image **processing** such as gradation conversion **processing** or density conversion **processing** needs to be carried out on the original data set, the desired data set cannot be obtained, since the original data set has been lost due to the color conversion **processing**. In order to solve this problem, in the case where a user needs to carry out **processing** on master content, the user needs to copy an original data set to be stored...

11/3,K/2 (Item 2 from file: 348)  
DIALOG(R) File 348:EUROPEAN PATENTS  
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00798637

**Tone generating method and device**

**Verfahren und Vorrichtung zur Tonerzeugung**

**Methode et dispositif pour la generation de notes**

**PATENT ASSIGNEE:**

YAMAHA CORPORATION, (404962), 10-1, Nakazawa-cho, Hamamatsu-shi,  
Shizuoka-ken 430, (JP), (Proprietor designated states: all)

**INVENTOR:**

Tamura, Motoichi , c/o Yamaha Corp., 10-1, Nakazawa-cho, Hamamatsu-shi,  
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**LEGAL REPRESENTATIVE:**

Kehl, Gunther, Dipl.-Phys. et al (48354), Patentanwaltskanzlei Gunther  
Kehl Friedrich-Herschel-Strasse 9, 81679 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 743631 A2 961120 (Basic)  
EP 743631 A3 970709  
EP 743631 B1 020306

APPLICATION (CC, No, Date): EP 96107770 960515;

PRIORITY (CC, No, Date): JP 95144159 950519; JP 95264629 950920; JP 9623323  
960117

DESIGNATED STATES: DE; GB; IT

INTERNATIONAL PATENT CLASS: G10H-001/00; G10H-001/18; G10H-007/00

ABSTRACT WORD COUNT: 169

**NOTE:**

Figure number on first page: 13

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB96	6113
CLAIMS B	(English)	200210	4076
CLAIMS B	(German)	200210	3582
CLAIMS B	(French)	200210	4658
SPEC A	(English)	EPAB96	21309
SPEC B	(English)	200210	21422
Total word count - document A			27428
Total word count - document B			33738
Total word count - documents A + B			61166

**INVENTOR:**

Tamura, Motoichi ...

...SPECIFICATION generating method and device which permit a tone to be formed by a general-purpose **processor** having a computing unit.

The present invention also relates to a tone generating method which forms a tone waveform by **executing** a tone generating program on a programmable computing unit such as a CPU or DSP...

...sequencer, a tone generator section for forming a tone waveform, and a microprocessor or central **processing** unit (CPU) for controlling the tone generator section in accordance with the input performance information. The CPU performs tone generator driver **processing** (performance **processing**) such as assignment of each tone to a selected channel (channel assignment) and parameter conversion...



...to the problem, an improved tone generating device has been proposed, in which the CPU **executes** a specific application program to generate tones on the basis of the application program. This...

...by a general-purpose computing unit which can perform tone generating and other functions by **executing** not only the tone generating application program but also other application programs. A general-purpose...

...personal computer can be employed as the hardware of such a tone generating device, which **executes** a tone generator program for tone generating operations. In this way, a so-call "software..."

...generator" is provided by the general-purpose computer.

In cases where tone are generated by **executing** an application program on a general-purpose device having a computing unit (CPU), it has...

...cycle (i.e., conversion timing of a D/A converter employed). Thus, when the CPU **executes** necessary operations for each of the tone generating channels, some preparatory operations are performed by the CPU, such as **reading**, into the CPU registers, of various registers' values used in the last calculations for the channel. Also, after termination of tone generating **processing** for the channel, it is necessary to **write** the CPU registers' values into a memory for next **execution** of the **processing**.

However, because the tone waveform sample forming or calculating **process** is performed sample by sample in each of the tone generating channels, the conventional software...

...CPU's calculating time is spent on the preparatory operations rather than the tone generating **process** (resulting in increased overheads), so that the calculating efficiency and response and tone forming speed ...

...tone generator would be undesirably lowered. That is, in performing the tone waveform sample forming **process** for the individual tone generating channels every sampling cycle, it is necessary for the CPU...

...forming calculations, cause the registers' stored contents to be saved into the memory for next **execution** of the **process**. Thus, much **processing** time is required for operations other than the primary waveform forming calculations.

Further, in the known tone generating methods where the CPU performs both the tone generator **processing** and the performance **processing**, the "performance **processing**" is one for creating control information for controlling tones to be generated on the basis of input performance information, and the "tone generator **processing**" is one for forming waveform data on the basis of the created control information. For example, the CPU normally **executes** the performance **processing** such as detection of depressed keys while it **executes** the tone generator **processing** every sampling cycle in response to an interrupt signal. After formation of waveform data is completed for one sample in the tone generator **processing**, the CPU reverts to the performance **processing**.

14/3,K/1 (Item 1 from file: 347)  
DIALOG(R)File 347:JAPIO  
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03753623 \*\*Image available\*\*  
CONNECTING/PROCESSING SYSTEM FOR NAMED FILE

PUB. NO.: 04-118723 [JP 4118723 A]  
PUBLISHED: April 20, 1992 (19920420)  
INVENTOR(s): TAMURA MASANORI  
APPLICANT(s): NEC CORP [000423] (A Japanese Company or Corporation), JP  
(Japan)  
APPL. NO.: 02-239483 [JP 90239483]  
FILED: September 10, 1990 (19900910)  
JOURNAL: Section: P, Section No. 1400, Vol. 16, No. 373, Pg. 159,  
August 11, 1992 (19920811)

#### ABSTRACT

...means 31 for receiving user's option in which a user's designated matter to **execution** of a program is described at the time of **executing** / **processing** an **OPEN** sentence having a **FILE** designator of a **user** 's **program** generated by FORTRAN language, a user's option analyzing means 32 for analyzing the contents...

14/3,K/2 (Item 2 from file: 347)  
DIALOG(R)File 347:JAPIO  
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03399338 \*\*Image available\*\*  
BACK-UP SYSTEM FOR DECISION OF FILE CAPACITY

PUB. NO.: 03-062238 [JP 3062238 A]  
PUBLISHED: March 18, 1991 (19910318)  
INVENTOR(s): TAMURA MASANORI  
APPLICANT(s): NEC CORP [000423] (A Japanese Company or Corporation), JP  
(Japan)  
APPL. NO.: 01-198202 [JP 89198202]  
FILED: July 31, 1989 (19890731)  
JOURNAL: Section: P, Section No. 1211, Vol. 15, No. 220, Pg. 21, June  
05, 1991 (19910605)

#### ABSTRACT

PURPOSE: To enable a user to set the capacity of an output-only **file** to an **operating** system by displaying the file capacity that may possibly be needed by a dummy file as an output-only file at the end of a **user** **program** .

...  
... 51 of a main storage 5 stores the capacity of data outputted every time an **executing** sentence of a **data** output request is carried out to a file which performs no output **process** . Then a file capacity display means 22 displays the file capacity that may possibly be needed when a file having no output **process** performs an output **process** on a display device 4 by reference to the capacity of the data stored in...

...As a result, the capacity of an output-only file can be set to an **operating** system.

14/3,K/3 (Item 1 from file: 348)  
DIALOG(R) File 348:EUROPEAN PATENTS  
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01694627

Performance data processing and tone signal synthesizing methods and apparatus

Methoden und Vorrichtung zur Verarbeitung von Ausführungsdaten und zur Synthetisierung von Tonsignalen

Methode et dispositif pour le traitement de donnees d'interpretation et pour la synthese de notes

PATENT ASSIGNEE:

YAMAHA CORPORATION, (404964), 10-1, Nakazawa-cho, Hamamatsu-cho  
Shizuoka-ken, (JP), (Applicant designated States: all)

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Hamamatsu-shi, Shizuoka-ken, (JP)  
Umeyama, Yasuyuki, c/o Yamaha Corporation, 10-1, Nakazawa-cho,  
Hamamatsu-shi, Shizuoka-ken, (JP)

LEGAL REPRESENTATIVE:

Emde, Eric et al (78022), Wagner & Geyer, Gewürzmühlstrasse 5, 80538  
München, (DE)

PATENT (CC, No, Kind, Date): EP 1388844 A1 040211 (Basic)

APPLICATION (CC, No, Date): EP 2003018115 030808;

PRIORITY (CC, No, Date): JP 2002231559 020808; JP 2002231560 020808

DESIGNATED STATES: AT; BE; BG; CH; CY; CZ; DE; DK; EE; ES; FI; FR; GB; GR;  
HU; IE; IT; LI; LU; MC; NL; PT; RO; SE; SI; SK; TR

EXTENDED DESIGNATED STATES: AL; LT; LV; MK

INTERNATIONAL PATENT CLASS: G10H-001/10

ABSTRACT WORD COUNT: 148

NOTE:

Figure number on first page: NONE

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200407	3830
SPEC A	(English)	200407	25312
Total word count - document A			29142
Total word count - document B			0
Total word count - documents A + B			29142

...SPECIFICATION IC-chip tone generator.

The following paragraphs describe a specific example of ensemble effect setting operation performed by the user in one embodiment of the inventive performance data processing method.

The user's ensemble effect setting operation is explained below as performed in the case where the...

...editor, the case where the present invention is embodied as the MIDI event front end processor shown in Fig. 9B or the case where the present invention is embodied as the...

14/3,K/4 (Item 2 from file: 348)  
DIALOG(R) File 348:EUROPEAN PATENTS

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01581582

**File system, control method, and program**

**Dateisystem, Steuerungsverfahren und Programm**

**Système de fichiers, méthode de controle et programme**

PATENT ASSIGNEE:

FUJITSU LIMITED, (211463), 1-1, Kamikodanaka 4-chome, Nakahara-ku,  
Kawasaki-shi, Kanagawa 211-8588, (JP), (Applicant designated States:  
all)

INVENTOR:

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Take, Riichiro, c/o Fujitsu Limited, 1-1 Kamikodanaka 4-chome,  
Nakahara-ku, Kawasaki-shi, Kanagawa 211-8588, (JP)  
Noguchi, Yasuo, c/o Fujitsu Limited, 1-1 Kamikodanaka 4-chome,  
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Ogihara, Kazutaka, c/o Fujitsu Limited, 1-1 Kamikodanaka 4-chome,  
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LEGAL REPRESENTATIVE:

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PATENT (CC, No, Kind, Date): EP 1313033 A2 030521 (Basic)

APPLICATION (CC, No, Date): EP 2002251502 020307;

PRIORITY (CC, No, Date): JP 2001350986 011116

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;  
LU; MC; NL; PT; SE; TR

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: G06F-017/30

ABSTRACT WORD COUNT: 85

NOTE:

Figure number on first page: 6

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200321	1449
SPEC A	(English)	200321	5350
Total word count - document A			6799
Total word count - document B			0
Total word count - documents A + B			6799

...SPECIFICATION to the file as a trigger. As mentioned above, according to the invention, since the **file system** directly **executes** the **user** defined **process** by using the access to the file as a trigger, holding of meta data defined by the user and the **user** defined **process** can be **executed** in accordance with **file** contents or the access.

Preferably, the file system has a file managing unit which partitions  
...a pointer;

Fig. 12 is an explanatory diagram of an embodiment in case of directly **processing** the extended meta **data** by **executing** the **user** defined **process** ;

Fig. 13 is an explanatory diagram of an embodiment in the case where a message...

...embodiment in the case where a flag is set to the high level by the **user** defined **process** and the extended meta **data** is **processed** by an external program;

Fig. 15 is an explanatory diagram of an embodiment of the...Therefore,

if the access, namely, "access A" is issued to the file 14-1, the file system of the invention executes the process corresponding to "access A" of the file 14-1 and **executes** the user defined **process** 16-3 as a process which has previously been defined by the user for "access..."

...where an application is used. Fig. 4B is a diagram showing a case where the user defined **process** is **executed** by the file system of the invention. According to the conventional system of Fig. 4A, an application 208 is...

...206-1 and a file 204 of a file system 202 which operates under an **operating** system 200, and the application 208 monitors an access to the file 204, thereby providing a function for **executing** a **process** defined by the user, for example, a **process** for making a notification to the outside. However, although the desired user defined **process** can be **executed** for the user 206-1 who accesses via the application 208, the user defined **process** cannot be **executed** for a user 206-2 who accesses without intervention of the application 208. There is...

...access. To prevent such a problem, according to the invention (Fig. 4B), by implementing a user defined **process** 16 in the file system 12 side, it is possible to make a notification to the outside which has been preset as a **process** by the user by the **execution** of the user defined **process** 16, without depending on a specific application in response to the accesses by the users 24-1 and 24-2. The **processing** overhead caused by the intervention of the application can be avoided.

Fig. 5 is a...of the file 14, extended meta data 54 is changed on the basis of the user defined **process** 16. That is, when **writing** (56) into the data area 30 of the file 14 occurs, the access **executing** unit 20 in the file system 12 of the invention shown in Fig. 1 **executes** the **writing process** to the data area 30 and the defined **process executing** unit 22 **executes** the user defined **process** 16 which has been associated (58) with the extended meta data 54. As definition contents 60, "change of extended meta data" has been defined in the user defined **process** 16. Therefore, as for the extended meta data 54 in the extended meta data area...

...of the extracted information 48 obtained after the change, and its size, are changed. The **writing** into the data area 30 in association with the **writing** (56) into the data area and the **execution** of the user defined **process** 16 are continuously **executed**. Since the extended meta data is automatically changed during such a series of **processes**, the application does not need to be aware of the synchronization of the extended meta data. Other **processes** for the file 14 are blocked during the continuous **processes** in association with the series of **writing** operations.

Fig. 13 shows an embodiment in the case where upon writing into the data area, the file system **executes** the user defined **process** and sends a message to a user program which has additionally been provided and the...

...data is changed by the user program which received the message. When the data is **written** (64) into the data area 30 of the file 14, the access **executing** unit 20 in the file system 12 in Fig. 1 **executes** the **writing process** to the data area 30. Subsequently, the defined **process executing** unit 22 in Fig. 1 **executes** the user defined **process** 16 associated with the **write** access. Since "message

transmission" has been defined in definition contents 68 of the user defined **process** 16, the message is sent to an external user program 70. In the user program...

...14 is changed to the changed contents in the data area 30. As for the **process** for changing the extended meta data by the external user program in association with the change of the data area as mentioned above, since the **process** can be performed at an application level, a more flexible **process** can be **executed**. However, it is necessary that the consistency of synchronization is managed on the application side...

...processes such that a data area change flag is set to the high level upon **writing** into the **data** area, a **user program** which has additionally been provided monitors this flag, and the user program **executes** the **process** for changing the extended meta data by using the fact that the flag has been set to the high level as a trigger. Upon **writing** into the data area 30 of the **file** 14, the access **executing** unit 20 in the file system 12 in Fig. 1 **executes** the **writing process** of the **data** into the data area 30. Subsequently, the defined **process** **executing** unit 22 in Fig. 1 **executes** the user defined **process** 16 associated (80) with the access of the **writing** (78). Since "flag setting" has been defined in definition contents 82 of the **user** defined **process** 16, a **data** area change flag 76 provided in the extended meta data area 34 is set to the high level by the **execution** of the user defined **process** 16. A user program 86 monitors (90) the data area change flag 76 in the...

...area 34. At a point when the high level of the flag is recognized, the **process** for "change of extended meta data" described as program contents 88 is **executed**. The **data** contents in the data area 30 obtained after the **writing** are copied into extended meta data 74 or the pointer is updated. As mentioned above, the flag which is controlled by the user defined **process** is monitored by the user program and the **process** for changing the extended meta data in association with the **data writing** is **executed**, so that the **processes** which are continuously **executed** from the **writing** operation that is **executed** on the **file system** side can be minimized. Although the changing **process** of the extended meta data area 34 can be exclusively performed by the data area...of the extended meta data area 34.

As described above, according to the invention, the **file system** directly **executes** the **user** defined **process** by using the access to the file as a trigger. The user defined **process** can be **executed** by using the access to the file as a trigger without depending on the application...

...CLAIMS defined process.

12. A system according to any of claims 2 to 10, wherein upon **writing** into the data area of said **file**, said defined **process** **executing** unit sends a message to a **user program** which is additionally provided and changes extended meta data in said extended meta data area.
13. A system according to any of claims 2 to 10, wherein upon **writing** into the **data** area of said **file**, said defined **process** **executing** unit sets a **data** area change flag to a high level and changes extended meta data extended in said...

14/3,K/5 (Item 3 from file: 348)  
DIALOG(R) File 348:EUROPEAN PATENTS  
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01160039

**Musical tone-generating method and musical tone-generating apparatus**  
**Verfahren und Vorrichtung zur Musiktonerzeugung**  
**Methode et dispositif pour la generation de son musical**

**PATENT ASSIGNEE:**

YAMAHA CORPORATION, (404962), 10-1, Nakazawa-cho, Hamamatsu-shi,  
Shizuoka-ken 430, (JP), (Proprietor designated states: all)

**INVENTOR:**

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**LEGAL REPRESENTATIVE:**

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PATENT (CC, No, Kind, Date): EP 1011091 A1 000621 (Basic)  
EP 1011091 B1 040428

APPLICATION (CC, No, Date): EP 2000103423 960927;

PRIORITY (CC, No, Date): JP 95275092 950929; JP 95254366 951002

DESIGNATED STATES: DE; GB; IT

EXTENDED DESIGNATED STATES: AL; LT; LV; SI

RELATED PARENT NUMBER(S) - PN (AN):

EP 766226 (EP 96115597)

INTERNATIONAL PATENT CLASS: G10H-007/00

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**NOTE:**

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200025	1302
CLAIMS B	(English)	200418	1284
CLAIMS B	(German)	200418	1113
CLAIMS B	(French)	200418	1564
SPEC A	(English)	200025	18091
SPEC B	(English)	200418	15497
Total word count - document A			19396
Total word count - document B			19458
Total word count - documents A + B			38854

...SPECIFICATION apparatus according to the present embodiment is comprised of a CPU 1 for performing various **data processings**, a keyboard 2 through which a **user** inputs **program execution** commands and **data**, a display unit 3 for displaying various image and character information, a hard disk drive (HDD) 4 **storing data** and programs **executed** by the CPU 1, a ROM 5 storing programs for controlling data inputting/outputting to...

...the keyboard 1, the display unit 3 and the HDD 4, a RAM 6 for **storing** programs being **executed**, waveform **data** and **data** calculated, a timer 7, a MIDI interface 8 connected to a performance unit like a...

...therefrom, a DMA (direct memory access) controller 9 for directly accessing the RAM 6 to **read** therefrom musical tone sample data (waveform data) at a rate corresponding, e.g., to a sampling frequency of 48 kHz and delivering the **read** data, sample by sample, to a D/A

converter 10 in response to a command from the CPU 1 (hereinafter referred to as "the **reading-reproducing processing**"), the D/A converter 10 for converting the musical tone-generating data in the form ...

...SPECIFICATION apparatus according to the present embodiment is comprised of a CPU 1 for performing various **data processings**, a keyboard 2 through which a **user** inputs **program execution** commands and **data**, a display unit 3 for displaying various image and character information, a hard disk drive (HDD) 4 **storing data** and programs **executed** by the CPU 1, a ROM 5 storing programs for controlling data inputting/outputting to...

...the keyboard 1, the display unit 3 and the HDD 4, a RAM 6 for **storing** programs being **executed**, waveform **data** and **data** calculated, a timer 7, a MIDI interface 8 connected to a performance unit like a...

...therefrom, a DMA (direct memory access) controller 9 for directly accessing the RAM 6 to **read** therefrom musical tone sample data (waveform data) at a rate corresponding, e.g., to a sampling frequency of 48 kHz and delivering the **read** data, sample by sample, to a D/A converter 10 in response to a command from the CPU 1 (hereinafter referred to as "the **reading-reproducing processing**"), the D/A converter 10 for converting the musical tone-generating data in the form ...

14/3,K/6 (Item 4 from file: 348)  
DIALOG(R) File 348:EUROPEAN PATENTS  
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01160038

**Musical tone-generating method and musical tone-generating apparatus**  
**Verfahren und Vorrichtung zur Musiktonerzeugung**  
**Methode et dispositif pour la generation de son musical**

**PATENT ASSIGNEE:**

YAMAHA CORPORATION, (404962), 10-1, Nakazawa-cho, Hamamatsu-shi,  
Shizuoka-ken 430, (JP), (Proprietor designated states: all)

**INVENTOR:**

Tamura, Motoichi, Yamaha Corporation, 10-1 Nakazawa-cho, Hamamatsu-shi,  
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**LEGAL REPRESENTATIVE:**

Kehl, Gunther, Dipl.-Phys. (48354), Patentanwaltskanzlei Gunther Kehl  
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PATENT (CC, No, Kind, Date): EP 1011090 A1 000621 (Basic)  
EP 1011090 B1 040609

APPLICATION (CC, No, Date): EP 2000103422 960927;

PRIORITY (CC, No, Date): JP 95275092 950929; JP 95254366 951002

DESIGNATED STATES: DE; GB; IT

EXTENDED DESIGNATED STATES: AL; LT; LV; SI

RELATED PARENT NUMBER(S) - PN (AN):

EP 766226 (EP 96115597)

INTERNATIONAL PATENT CLASS: G10H-007/00

ABSTRACT WORD COUNT: 99

**NOTE:**

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English



FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200025	918
CLAIMS B	(English)	200424	916
CLAIMS B	(German)	200424	839
CLAIMS B	(French)	200424	1098
SPEC A	(English)	200025	18093
SPEC B	(English)	200424	15556
Total word count - document A			19014
Total word count - document B			18409
Total word count - documents A + B			37423

...SPECIFICATION apparatus according to the present embodiment is comprised of a CPU 1 for performing various **data processings**, a keyboard 2 through which a **user** inputs **program execution** commands and **data**, a display unit 3 for displaying various image and character information, a hard disk drive (HDD) 4 **storing data** and programs **executed** by the CPU 1, a ROM 5 storing programs for controlling data inputting/outputting to...

...the keyboard 1, the display unit 3 and the HDD 4, a RAM 6 for **storing** programs being **executed**, waveform **data** and **data** calculated, a timer 7, a MIDI interface 8 connected to a performance unit like a...

...therefrom, a DMA (direct memory access) controller 9 for directly accessing the RAM 6 to **read** therefrom musical tone sample data (waveform data) at a rate corresponding, e.g., to a sampling frequency of 48 kHz and delivering the **read** data, sample by sample, to a D/A converter 10 in response to a command from the CPU 1 (hereinafter referred to as "the **reading-reproducing processing**"), the D/A converter 10 for converting the musical tone-generating data in the form ...

...SPECIFICATION apparatus according to the present embodiment is comprised of a CPU 1 for performing various **data processings**, a keyboard 2 through which a **user** inputs **program execution** commands and **data**, a display unit 3 for displaying various image and character information, a hard disk drive (HDD) 4 **storing data** and programs **executed** by the CPU 1, a ROM 5 storing programs for controlling data inputting/outputting to...

...the keyboard 1, the display unit 3 and the HDD 4, a RAM 6 for **storing** programs being **executed**, waveform **data** and **data** calculated, a timer 7, a MIDI interface 8 connected to a performance unit like a...

...therefrom, a DMA (direct memory access) controller 9 for directly accessing the RAM 6 to **read** therefrom musical tone sample data (waveform data) at a rate corresponding, e.g., to a sampling frequency of 48 kHz and delivering the **read** data, sample by sample, to a D/A converter 10 in response to a command from the CPU 1 (hereinafter referred to as "the **reading-reproducing processing**"), the D/A converter 10 for converting the musical tone-generating data in the form ...

14/3,K/7 (Item 5 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

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00824411

**Musical tone-generating method and musical tone-generating apparatus**

**Verfahren und Vorrichtung zur Musiktonerzeugung**

**Methode et dispositif de generation de son musical**

**PATENT ASSIGNEE:**

YAMAHA CORPORATION, (404962), 10-1, Nakazawa-cho, Hamamatsu-shi,  
Shizuoka-ken 430, (JP), (Proprietor designated states: all)

**INVENTOR:**

Tamura, Motoichi, c/o Yamaha Corporation, 10-1, Nakazawa-cho,  
Hamamatsu-shi, Shizuoka-ken 430, (JP)

**LEGAL REPRESENTATIVE:**

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PATENT (CC, No, Kind, Date): EP 766226 A1 970402 (Basic)  
EP 766226 B1 010228

APPLICATION (CC, No, Date): EP 96115597 960927;

PRIORITY (CC, No, Date): JP 95275092 950929; JP 95254366 951002

DESIGNATED STATES: DE; GB; IT

RELATED DIVISIONAL NUMBER(S) - PN (AN):

EP 1011090 (EP 103422)

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**NOTE:**

Figure number on first page: 4

LANGUAGE (Publication,Procedural,Application): English; English; English

**FULLTEXT AVAILABILITY:**

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB97	2502
CLAIMS B	(English)	200109	2036
CLAIMS B	(German)	200109	1829
CLAIMS B	(French)	200109	2506
SPEC A	(English)	EPAB97	18095
SPEC B	(English)	200109	15572
Total word count - document A			20601
Total word count - document B			21943
Total word count - documents A + B			42544

...SPECIFICATION apparatus according to the present embodiment is comprised of a CPU 1 for performing various **data processings**, a keyboard 2 through which a **user** inputs **program execution** commands and **data**, a display unit 3 for displaying various image and character information, a hard disk drive (HDD) 4 **storing data** and programs **executed** by the CPU 1, a ROM 5 storing programs for controlling data inputting/outputting to...

...the keyboard 1, the display unit 3 and the HDD 4, a RAM 6 for **storing** programs being **executed**, waveform **data** and **data** calculated, a timer 7, a MIDI interface 8 connected to a performance unit like a...

...therefrom, a DMA (direct memory access) controller 9 for directly accessing the RAM 6 to **read** therefrom musical tone sample data (waveform data) at a rate corresponding, e.g., to a sampling frequency of 48 kHz and delivering the **read** data, sample by sample, to a D/A converter 10 in response to a command from the CPU 1 (hereinafter referred to as "the **reading-reproducing processing**"), the D/A converter 10 for converting the musical tone-generating data in the form

...  
...SPECIFICATION apparatus according to the present embodiment is comprised of a CPU 1 for performing various **data processings** , a keyboard 2 through which a **user** inputs **program execution** commands and **data** , a ...unit 3 for displaying various image and character information, a hard disk drive (HDD) 4 **storing data** and programs **executed** by the CPU 1, a ROM 5 storing programs for controlling data inputting/outputting to...

...the keyboard 1, the display unit 3 and the HDD 4, a RAM 6 for **storing** programs being **executed** , waveform **data** and **data** calculated, a timer 7, a MIDI interface 8 connected to a performance unit like a...

...therefrom, a DMA (direct memory access) controller 9 for directly accessing the RAM 6 to **read** therefrom musical tone sample data (waveform data) at a rate corresponding, e.g., to a sampling frequency of 48 kHz and delivering the **read** data, sample by sample, to a D/A converter 10 in response to a command from the CPU 1 (hereinafter referred to as "the **reading** -reproducing **processing** "), the D/A converter 10 for converting the musical tone-generating data in the form  
...

14/3,K/8 (Item 6 from file: 348)  
DIALOG(R)File 348:EUROPEAN PATENTS  
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00635747

**Image processor.**

**Bildprozessor.**

**Processeur d'image.**

PATENT ASSIGNEE:

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INVENTOR:

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PATENT (CC, No, Kind, Date): EP 617561 A2 940928 (Basic)  
EP 617561 A3 950823

APPLICATION (CC, No, Date): EP 94302032 940322;

PRIORITY (CC, No, Date): JP 9387926 930322

DESIGNATED STATES: DE; FR; GB; NL

INTERNATIONAL PATENT CLASS: H04N-007/14; H04N-001/40;

ABSTRACT WORD COUNT: 135

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

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CLAIMS A (English) EPABF2 4061  
 SPEC A (English) EPABF2 27479  
 Total word count - document A 31540  
 Total word count - document B 0  
 Total word count - documents A + B 31540

...SPECIFICATION natural picture and a document picture is transmitted from a communication object, the image data **processing** section 14 receives this picture data D2 through the main **processing** section 12 and decompresses it to its original picture, and afterwards, outputs it to a printer 16 in response to a **user operation**. In addition, the image **data processing** section 14 converts the picture data into a digital video signal and outputs it to...into digital signal and then outputs them to the encoder/decoder 11 or the image **data processing** section 14 through a matrix circuit 22.

In this way, it is so arranged that...

...and furthermore, receives the picture data D( sub(MA)), which is output from the image **data processing** section 14, to the matrix circuit 22 and outputs the output data of this matrix...

...data D( sub(V)), D( sub(ME)), and D( sub(MA)) in response to a **user operation** and synthesizes these picture data D( sub(V)), D( sub(ME)), and D( sub(MA))...

...place of this, when picture data D( sub(MA)), which is output from the image **data processing** section 14, is selected together with picture data D( sub(ME)), the image input/output...

14/3,K/9 (Item 1 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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015390073 \*\*Image available\*\*

WPI Acc No: 2003-451017/200343

XRPX Acc No: N03-359759

**Operating system file system for e.g. UNIX, Windows etc., in which a process defined by the user can be executed by using an access to a file as a trigger**

Patent Assignee: FUJITSU LTD (FUIT )

Inventor: NOGUCHI Y; OGIHARA K; TAKE R; TAMURA M

Number of Countries: 028 Number of Patents: 003

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 1313033	A2	20030521	EP 2002251502	A	20020307	200343 B
JP 2003150424	A	20030523	JP 2001350986	A	20011116	200343
US 20030105780	A1	20030605	US 200286894	A	20020304	200344

Priority Applications (No Type Date): JP 2001350986 A 20011116

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes

EP 1313033 A2 E 26 G06F-017/30

Designated States (Regional): AL AT BE CH CY DE DK ES FI FR GB GR IE IT  
 LI LT LU LV MC MK NL PT RO SE SI TR

JP 2003150424 A 19 G06F-012/00

US 20030105780 A1 G06F-012/00

Abstract (Basic):

... An **operating** system (10) file system (12) has an access-  
**executing** unit which **processes** a **file** (14) when the file is  
accessed e.g. **read** or **write** operation. A user defined **process**  
-holding unit (15) holds a user defined **process** (16-1,16-2) which has  
previously been defined by the **user** . A defined **process** **executing**  
unit **executes** the **user** defined **process** by using the **file** access  
as a trigger.

File 2:INSPEC 1969-2004/Sep W2  
(c) 2004 Institution of Electrical Engineers  
File 6:NTIS 1964-2004/Sep W3  
(c) 2004 NTIS, Intl Cpyrght All Rights Res  
File 8:Ei Compendex(R) 1970-2004/Sep W2  
(c) 2004 Elsevier Eng. Info. Inc.  
File 34:SciSearch(R) Cited Ref Sci 1990-2004/Sep W3  
(c) 2004 Inst for Sci Info  
File 434:SciSearch(R) Cited Ref Sci 1974-1989/Dec  
(c) 1998 Inst for Sci Info  
File 35:Dissertation Abs Online 1861-2004/Aug  
(c) 2004 ProQuest Info&Learning  
File 65:Inside Conferences 1993-2004/Sep W3  
(c) 2004 BLDSC all rts. reserv.  
File 94:JICST-EPlus 1985-2004/Aug W4  
(c)2004 Japan Science and Tech Corp(JST)  
File 99:Wilson Appl. Sci & Tech Abs 1983-2004/Aug  
(c) 2004 The HW Wilson Co.  
File 144:Pascal 1973-2004/Sep W2  
(c) 2004 INIST/CNRS

Set	Items	Description
S1	15399	AU=(TAMURA M? OR TAMURA, M? OR TAKE R? OR TAKE, R? OR NOGU- CHI Y? OR NOGUCHI, Y? OR OGIHARA K? OR OGIHARA, K?)
S2	2080	S1 AND (READ??? OR WRIT??? OR EXECUT??? OR OPERATING?? OR - PROCESS???? OR LAUNCH???)
S3	19508	(S2 OR OPEN???) (3N) (FILE?? OR DATA?? OR SOFTWARE?? OR CODE? ? OR CODING OR FOLDER?? OR FILE??) (SYSTEM?? OR FILESYSTEM?)
S4	15	S1 AND (USER?? OR PROGRAMMER?? OR OPERATOR?? OR PROGRAMER?- ?) (3N) (PROCESS??? OR OPERATION?? OR ALGORITHM?? OR PROGRAM??? OR APPLICATION?? OR PROCEDURE?? OR STEPS?? OR MODULE?? OR MAC- RO??)
S5	3	S2 AND ((HOLD??? OR RESERV??? OR STORING?? OR STOR?? OR EX- ECUT?? OR EXECUTING??) (3N) S3)
S6	2	RD (unique items)
S7	0	S4 AND S5
S8	10	RD S4 (unique items)
S9	10	S8 NOT S6

6/3,K/1 (Item 1 from file: 2)  
DIALOG(R)File 2:INSPEC  
(c) 2004 Institution of Electrical Engineers. All rts. reserv.

4720345 INSPEC Abstract Number: C9409-7420D-005  
**Title: Parallel processing of continuous/discrete-time control systems simulation**  
Author(s): Yamamoto, Y.; Torii, H.; Maekawa, Y.; Tamura, M. ; Kasahara, H.; Narita, S.  
Author Affiliation: Waseda Univ., Tokyo, Japan  
Journal: Transactions of the Institute of Electrical Engineers of Japan, Part C vol.113-C, no.11 p.939-46  
Publication Date: Nov. 1993 Country of Publication: Japan  
CODEN: DGRCDZ ISSN: 0385-4221  
Language: Japanese  
Subfile: C

**Title: Parallel processing of continuous/discrete-time control systems simulation**  
Author(s): Yamamoto, Y.; Torii, H.; Maekawa, Y.; Tamura, M. ; Kasahara, H.; Narita, S.  
Abstract: This paper proposes a parallel processing scheme for continuous- and discrete-time control systems simulation on multiprocessor systems. The scheme allows...  
... program from a block diagram input on X-Window, compiles the program into parallel machine codes , executes them on a multiprocessor and outputs the results graphically.  
...Descriptors: parallel processing ;  
Identifiers: parallel processing ;

6/3,K/2 (Item 1 from file: 8)  
DIALOG(R)File 8:Ei Compendex(R)  
(c) 2004 Elsevier Eng. Info. Inc. All rts. reserv.

03053370 E.I. Monthly No: EIM9104-017287  
**Title: Latest development in brown coal liquefaction pilot plant.**  
Author: Ohzawa, T.; Tamura, M. ; Sonoda, K.; Nakako, Y.; Nanita, H.  
Corporate Source: Brown Coal Liquefaction (Victoria) Pty Ltd, Victoria, Aust  
Conference Title: Eighteenth Australasian Chemical Engineering Conference Part 1 (of 2)  
Conference Location: Auckland, NZ Conference Date: 19900827  
E.I. Conference No.: 14196  
Source: CHEMECA '90, Australasian Chemical Engineering Conference. Publ by Univ of Auckland School of Engineering, Auckland, NZ. p 400-407  
Publication Year: 1990  
CODEN: 85LFAX  
Language: English

Author: Ohzawa, T.; Tamura, M. ; Sonoda, K.; Nakako, Y.; Nanita, H.  
...Abstract: the Deashing Unit Confirmed the performance of the Secondary Hydrogenation Unit Collection of engineering and process data such as: reaction gas hold up, process data under high oil yield condition and slurry dewatering. (Author abstract) 4 Refs.

9/3,K/1 (Item 1 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2004 Institution of Electrical Engineers. All rts. reserv.

7062458 INSPEC Abstract Number: B2001-11-6210L-169, C2001-11-5620L-039

**Title: The home networking system based on IEEE1394 and Ethernet technologies**

Author(s): Shimada, K.; Sasaki, H.; **Noguchi, Y.**

Author Affiliation: Central Technol. Lab., Asahi Kasei Corp., Japan

Conference Title: ICCE. International Conference on Consumer Electronics (IEEE Cat. No.01CH37182) p.234-5

Publisher: IEEE, Piscataway, NJ, USA

Publication Date: 2001 Country of Publication: USA 361 pp.

ISBN: 0 7803 6622 0 Material Identity Number: XX-2001-01445

U.S. Copyright Clearance Center Code: 0 7803 6622 0/2001/\$10.00

Conference Title: ICCE. International Conference on Consumer Electronics

Conference Date: 19-21 June 2001 Conference Location: Los Angeles, CA, USA

Language: English

Subfile: B C

Copyright 2001, IEE

Author(s): Shimada, K.; Sasaki, H.; **Noguchi, Y.**

...Abstract: control application based on Web server technologies. The characteristics of our home networking system are **user friendly operation**, seamless control, low-power and low-cost.

9/3,K/2 (Item 2 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2004 Institution of Electrical Engineers. All rts. reserv.

6145514 INSPEC Abstract Number: C1999-03-6150C-001

**Title: Parallelization in an HPF language processor**

Author(s): Hayashi, Y.; Sakon, S.; Seo, Y.; Suehiro, K.; **Tamura, M.**; Murai, H.

Author Affiliation: 1st Comput. Software Div., NEC Res. & Dev., Japan

Journal: NEC Research and Development Conference Title: NEC Res. Dev. (Japan) vol.39, no.4 p.414-21

Publisher: NEC Creative,

Publication Date: Oct. 1998 Country of Publication: Japan

CODEN: NECRAU ISSN: 0547-051X

SICI: 0547-051X(199810)39:4L:414:PLP;1-D

Material Identity Number: N043-1999-001

Conference Title: High Performance Computing - Towards Reality in Scientific Simulations: NEC's 21st Century Odyssey -

Conference Date: 22 May 1998 Conference Location: Houston, TX, USA

Language: English

Subfile: C

Copyright 1999, IEE

Author(s): Hayashi, Y.; Sakon, S.; Seo, Y.; Suehiro, K.; **Tamura, M.**; Murai, H.

...Abstract: de facto standard data-parallel language mainly aimed at distributed memory multiprocessor systems. HPF allows **users** to develop parallelized **programs** by only specifying how to map data onto processors. The HPF compiler partitions computation among...



9/3,K/3 (Item 3 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2004 Institution of Electrical Engineers. All rts. reserv.

04316075 INSPEC Abstract Number: B9302-6250G-025, C9302-5690-001

**Title: Satellite data distribution system**

Author(s): Haruyama, H.; **Tamura, M.** ; Ueno, T.

Journal: Fujitsu vol.43, no.6 p.660-8

Publication Date: 1992 Country of Publication: Japan

CODEN: FUJTAR ISSN: 0016-2515

Language: Japanese

Subfile: B C

Author(s): Haruyama, H.; **Tamura, M.** ; Ueno, T.

...Abstract: flexible data addressing control for transmission destinations. Fujitsu is planning to enhance this system for **user** -specific **applications** and for applications in new markets.

...Identifiers: **user** -specific **applications** ;

9/3,K/4 (Item 1 from file: 8)

DIALOG(R)File 8:EI Compendex(R)

(c) 2004 Elsevier Eng. Info. Inc. All rts. reserv.

05099974 E.I. No: EIP98084345758

**Title:** Text-to-visual speech synthesis based on parameter generation from **HMM**

Author: Masuko, Takashi; Kobayashi, Takao; **Tamura, Masatsune** ; Masubuchi, Jun; Tokuda, Keiichi

Corporate Source: Tokyo Inst of Technology, Yokohama, Jpn

Conference Title: Proceedings of the 1998 IEEE International Conference on Acoustics, Speech and Signal Processing, ICASSP. Part 6 (of 6)

Conference Location: Seattle, WA, USA Conference Date: 19980512-19980515

E.I. Conference No.: 48801

Source: ICASSP, IEEE International Conference on Acoustics, Speech and Signal Processing - Proceedings v 6 1998. IEEE, Piscataway, NJ, USA, 98CH36181. p 3745-3748

Publication Year: 1998

CODEN: IPRDJ ISSN: 0736-7791

Language: English

Author: Masuko, Takashi; Kobayashi, Takao; **Tamura, Masatsune** ; Masubuchi, Jun; Tokuda, Keiichi

Descriptors: Speech synthesis; Algorithms; Mathematical models; Markov **processes** ; Human computer interaction; **User** interfaces

9/3,K/5 (Item 2 from file: 8)

DIALOG(R)File 8:EI Compendex(R)

(c) 2004 Elsevier Eng. Info. Inc. All rts. reserv.

04612066 E.I. No: EIP97023513353

**Title: Kemari: a portable high performance Fortran system for distributed memory parallel processors**

Author: Kamachi, T.; Muller, A.; Ruhl, R.; Seo, Y.; Suehiro, K.; **Tamura, M.**

Corporate Source: NEC Corp, Tokyo, Jpn  
Source: Scientific Programming v 6 n 1 Spring 1997. p 41-58  
Publication Year: 1997  
CODEN: SCIPV ISSN: 1058-9244  
Language: English

Author: Kamachi, T.; Muller, A.; Ruhl, R.; Seo, Y.; Suehiro, K.; **Tamura, M.**

Identifiers: High performance Fortran; Distributed memory parallel **processors** ; Data distribution; **User** defined mappings; Message passing interface; Parallel debugger

9/3,K/6 (Item 1 from file: 94)  
DIALOG(R)File 94:JICST-EPlus  
(c)2004 Japan Science and Tech Corp(JST). All rts. reserv.

05745111 JICST ACCESSION NUMBER: 04A0305312 FILE SEGMENT: JICST-E  
**The Removal of Weakly Ionized Species by EDI Technology**  
**NOGUCHI YUKIO (1)**  
(1) Nomuramaikurosaiensu  
Nippon Kaisui Gakkaishi(Bulletin of the Society of Sea Water Science, Japan  
, 2004, VOL.58,NO.2, PAGE.167-173, FIG.5, REF.7  
JOURNAL NUMBER: F0235ABU ISSN NO: 0369-4550 CODEN: NKAGB  
UNIVERSAL DECIMAL CLASSIFICATION: 628.16.08/.09 66.087  
LANGUAGE: Japanese COUNTRY OF PUBLICATION: Japan  
DOCUMENT TYPE: Journal  
ARTICLE TYPE: Commentary  
MEDIA TYPE: Printed Publication

**NOGUCHI YUKIO (1)**  
...ABSTRACT: process which does not require acid or caustic regeneration.  
Also EDI is a fairly simple **process** which requires little **operator**  
attention and little maintenance. One of the successes of EDI is the  
high removal ratio...

9/3,K/7 (Item 2 from file: 94)  
DIALOG(R)File 94:JICST-EPlus  
(c)2004 Japan Science and Tech Corp(JST). All rts. reserv.

05730135 JICST ACCESSION NUMBER: 04A0185969 FILE SEGMENT: JICST-E  
**Design of the Modular Interaction Control Rules and Research on the Means  
of Application to the Concrete Interaction Domain.**  
SUZUKI YUKIKO (1); IKEGAYA YUKI (1); **NOGUCHI YASUHIRO (1)**; ITO TOSHIHIKO  
(1); KONISHI TATSUHIRO (1); ITO YUKIHIRO (1); TAKAGI AKIRA (2)  
(1) Shizuoka Univ., Fac. Information, JPN; (2) Csk  
Jinko Chino Gakkai Gengo, Onsei Rikai to Taiwa Shori Kenkyukai Shiryo(  
SIG-SLUD), 2004, VOL.40th, PAGE.73-78, FIG.11, REF.6  
JOURNAL NUMBER: L1425AAP ISSN NO: 0918-5682  
UNIVERSAL DECIMAL CLASSIFICATION: 681.51:007.51 681.3:80 681.3.02+  
LANGUAGE: Japanese COUNTRY OF PUBLICATION: Japan  
DOCUMENT TYPE: Conference Proceeding  
ARTICLE TYPE: Original paper  
MEDIA TYPE: Printed Publication

SUZUKI YUKIKO (1); IKEGAYA YUKI (1); **NOGUCHI YASUHIRO (1)**; ITO TOSHIHIKO

(1); KONISHI TATSUHIRO (1); ITO YUKIHIRO (1)  
...ABSTRACT: executed and the inheritance of information necessary for  
executing the rules. Our system allows the **user** to define these  
**processes** independently of the rules. An example operation of our  
current system is given at the...

9/3,K/8 (Item 3 from file: 94)  
DIALOG(R)File 94:JICST-EPlus  
(c)2004 Japan Science and Tech Corp(JST). All rts. reserv.

05335657 JICST ACCESSION NUMBER: 03A0093563 FILE SEGMENT: JICST-E  
**MPI Performance Evaluation on the Earth Simulator.**  
UEHARA HITOSHI (1); ITAKURA KEN'ICHI (1); **TAMURA MASANORI** (2); YOKOKAWA  
MITSUO (3)  
(1) Kaiyokagakugijutsuse Chikyushimyuretase; (2) Nec; (3) Japan Atomic  
Energy Res. Inst., JPN  
Joho Shori Gakkai Ronbunshi(Transactions of Information Processing Society  
of Japan), 2003, VOL.44,NO.SIG1(HPS6), PAGE.24-34, FIG.23, TBL.5,  
REF.11  
JOURNAL NUMBER: Z0778AAZ ISSN NO: 0387-5806  
UNIVERSAL DECIMAL CLASSIFICATION: 681.32 681.3.06  
LANGUAGE: Japanese COUNTRY OF PUBLICATION: Japan  
DOCUMENT TYPE: Journal  
ARTICLE TYPE: Original paper  
MEDIA TYPE: Printed Publication

; **TAMURA MASANORI** (2)  
...ABSTRACT: distributed memory parallel computers such as the Earth  
Simulator, an optimization of communication processings in **user**  
**applications** is required, and the optimization needs an evaluation for  
performance of communication methods. On the...

9/3,K/9 (Item 4 from file: 94)  
DIALOG(R)File 94:JICST-EPlus  
(c)2004 Japan Science and Tech Corp(JST). All rts. reserv.

05141546 JICST ACCESSION NUMBER: 02A0317005 FILE SEGMENT: JICST-E  
**MPI performance measurement on nodes of the Earth Simulator.**  
UEHARA HITOSHI (1); YOKOKAWA MITSUO (1); **TAMURA MASANORI** (2)  
(1) Japan At. Energy Res. Inst.; (2) Nec Hpcgengogurupu  
Joho Shori Gakkai Shinpojiumu Ronbunshu, 2002, VOL.2002,NO.4, PAGE.73-80,  
FIG.19, TBL.5, REF.3  
JOURNAL NUMBER: Y0978BAT ISSN NO: 1344-0640  
UNIVERSAL DECIMAL CLASSIFICATION: 681.3:65.012.122 681.32  
LANGUAGE: Japanese COUNTRY OF PUBLICATION: Japan  
DOCUMENT TYPE: Conference Proceeding  
ARTICLE TYPE: Original paper  
MEDIA TYPE: Printed Publication

; **TAMURA MASANORI** (2)  
...ABSTRACT: distributed memory parallel computers such as the Earth  
Simulator, an optimization of communication processings in **user**  
**applications** is required, and the optimization needs an evaluation for  
performances of communication methods. In the...

9/3,K/10 (Item 5 from file: 94)  
DIALOG(R)File 94:JICST-EPlus  
(c)2004 Japan Science and Tech Corp(JST). All rts. reserv.

02085404 JICST ACCESSION NUMBER: 94A0564153 FILE SEGMENT: JICST-E  
**Education and training of operators and operation support using a  
computer in "Jyoyo".**

OKUBO TOSHIYUKI (1); SAWADA MAKOTO (1); **TAMURA MASAANKI** (1)  
(1) Power React. and Nucl. Fuel Dev. Corp., Oarai Eng. Center  
UTNL,R. Tokyo Daigaku Kogakubu Fuzoku Genshiryoku Kogaku Kenkyu Shisetsu,  
1994, NO.308, PAGE.8.1-8.10, FIG.5, TBL.1

JOURNAL NUMBER: G0213BAJ

UNIVERSAL DECIMAL CLASSIFICATION: 621.039.515/.519+621.039.56

LANGUAGE: Japanese COUNTRY OF PUBLICATION: Japan

DOCUMENT TYPE: Technical Report

ARTICLE TYPE: Commentary

MEDIA TYPE: Printed Publication

**Education and training of operators and operation support using a  
computer in "Jyoyo".**

OKUBO TOSHIYUKI (1); SAWADA MAKOTO (1); **TAMURA MASAANKI** (1)

File 88:Gale Group Business A.R.T.S. 1976-2004/Sep 23  
     (c) 2004 The Gale Group  
 File 369:New Scientist 1994-2004/Sep W2  
     (c) 2004 Reed Business Information Ltd.  
 File 160:Gale Group PROMT(R) 1972-1989  
     (c) 1999 The Gale Group  
 File 635:Business Dateline(R) 1985-2004/Sep 24  
     (c) 2004 ProQuest Info&Learning  
 File 15:ABI/Inform(R) 1971-2004/Sep 24  
     (c) 2004 ProQuest Info&Learning  
 File 16:Gale Group PROMT(R) 1990-2004/Sep 24  
     (c) 2004 The Gale Group  
 File 9:Business & Industry(R) Jul/1994-2004/Sep 23  
     (c) 2004 The Gale Group  
 File 13:BAMP 2004/Sep W2  
     (c) 2004 The Gale Group  
 File 810:Business Wire 1986-1999/Feb 28  
     (c) 1999 Business Wire  
 File 610:Business Wire 1999-2004/Sep 24  
     (c) 2004 Business Wire.  
 File 647:CMP Computer Fulltext 1988-2004/Sep W2  
     (c) 2004 CMP Media, LLC  
 File 98:General Sci Abs/Full-Text 1984-2004/Aug  
     (c) 2004 The HW Wilson Co.  
 File 148:Gale Group Trade & Industry DB 1976-2004/Sep 24  
     (c)2004 The Gale Group  
 File 634:San Jose Mercury Jun 1985-2004/Sep 23  
     (c) 2004 San Jose Mercury News  
 File 275:Gale Group Computer DB(TM) 1983-2004/Sep 24  
     (c) 2004 The Gale Group  
 File 47:Gale Group Magazine DB(TM) 1959-2004/Sep 24  
     (c) 2004 The Gale group  
 File 75:TGG Management Contents(R) 86-2004/Sep W2  
     (c) 2004 The Gale Group  
 File 636:Gale Group Newsletter DB(TM) 1987-2004/Sep 24  
     (c) 2004 The Gale Group  
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     (c) 2004 McGraw-Hill Co. Inc  
 File 484:Periodical Abs Plustext 1986-2004/Sep W3  
     (c) 2004 ProQuest  
 File 613:PR Newswire 1999-2004/Sep 24  
     (c) 2004 PR Newswire Association Inc  
 File 813:PR Newswire 1987-1999/Apr 30  
     (c) 1999 PR Newswire Association Inc  
 File 141:Readers Guide 1983-2004/Aug  
     (c) 2004 The HW Wilson Co  
 File 239:Mathsci 1940-2004/Nov  
     (c) 2004 American Mathematical Society  
 File 370:Science 1996-1999/Jul W3  
     (c) 1999 AAAS  
 File 696:DIALOG Telecom. Newsletters 1995-2004/Sep 23  
     (c) 2004 The Dialog Corp.  
 File 553:Wilson Bus. Abs. FullText 1982-2004/Aug  
     (c) 2004 The HW Wilson Co  
 File 621:Gale Group New Prod.Annou.(R) 1985-2004/Sep 24  
     (c) 2004 The Gale Group  
 File 674:Computer News Fulltext 1989-2004/Aug W4  
     (c) 2004 IDG Communications

Set      Items      Description

S1 1908341 (READ??? OR WRIT??? OR EXECUT??? OR OPERATING?? OR PROCESS-  
 ???? OR LAUNCH??? OR OPEN???) (3N) (FILE?? OR DATA?? OR SOFTWARE?? OR CODE? ? OR CODING OR FOLDER?? OR FILE??) SYSTEM?? OR FILES-  
 ILESYTEM??)  
 S2 865120 (USER?? OR PROGRAMMER?? OR OPERATOR?? OR PROGRAMER??) (3N) (-  
 PROCESS??? OR OPERATION?? OR ALGORITHM?? OR PROGRAM??? OR APP-  
 LICATION?? OR PROCEDURE?? OR STEPS?? OR MODULE?? OR MACRO??)  
 S3 7218 (HOLD??? OR RESERV??? OR STORING?? OR STOR??) (3N) S2  
 S4 606170 (TRIGGER??? OR INITIAT??? OR ACTIVAT??? OR START???) (7N) (E-  
 XECUT??? OR OPERATING?? OR PROCESS???? OR LAUNCH???)  
 S5 3218 (EXECUT?? OR EXECUTING??) (3N) S2  
 S6 21862 S1(5N) S2  
 S7 191 S6(5N) S4  
 S8 0 S7 AND (S3 AND S5)  
 S9 1 S7 AND S3  
 S10 5 S7 AND S5  
 S11 8 S6 AND S3 AND S5  
 S12 7 RD (unique items)  
 S13 7 S12 NOT S10

9/3,K/1 (Item 1 from file: 610)  
DIALOG(R) File 610:Business Wire  
(c) 2004 Business Wire. All rts. reserv.

00033935 19990422112B0104 (USE FORMAT 7 FOR FULLTEXT)  
**Kinesis Introduces New Programmable Keypad and Programmable Foot Switch to Enhance Computer User Productivity and Comfort**  
Business Wire  
Thursday, April 22, 1999 09:24 EDT  
JOURNAL CODE: BW LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT  
DOCUMENT TYPE: NEWSWIRE  
WORD COUNT: 665

...can create shortcuts for keying intensive activities such as in 10-key use, CAD design, **software** programming and word **processing** . The **user** can use either device to **activate** key actions or trigger macros to personally suit their needs. The Keypad and Foot Switch...

...any three key actions to the three separate pedals located on the foot switch. -- Allows **users** to **store macros** (a sequence of up to 13 keyboard

keys) in the memory of the programmable foot...  
?

10/3,K/1 (Item 1 from file: 16)  
DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2004 The Gale Group. All rts. reserv.

02101100 Supplier Number: 42722418 (USE FORMAT 7 FOR FULLTEXT)  
**SOFTWARE ENHANCEMENTS AVAILABLE FOR NEW LOW-COST 68EC030 CPU**  
News Release, p1  
Feb 2, 1992  
Language: English Record Type: Fulltext  
Document Type: Magazine/Journal; Trade  
Word Count: 365

... enhancements include EPROM patch  
locations for the implementation of user-developed drivers, and  
provisions to **execute user application code** upon **start** -up.

Although the XVME-991 can be used as a "stand-alone" debugger, it  
will...

10/3,K/2 (Item 1 from file: 275)  
DIALOG(R)File 275:Gale Group Computer DB(TM)  
(c) 2004 The Gale Group. All rts. reserv.

01619709 SUPPLIER NUMBER: 14390592 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Me and my shadow. (Knozall Systems' NLMAuto 1.31 and Frye Computer Systems'**  
**NetWare Console Commander NetWare server task schedulers) (Software**  
**Review) (Test Drive) (Evaluation)**  
Carrol, Dan; Hurwicz, Mike  
LAN Magazine, v8, n11, p178(8)  
Oct, 1993  
DOCUMENT TYPE: Evaluation ISSN: 0898-0012 LANGUAGE: ENGLISH  
RECORD TYPE: FULLTEXT; ABSTRACT  
WORD COUNT: 6435 LINE COUNT: 00483

...ABSTRACT: Systems' NLMAuto 1.31 NetWare server task schedulers load and  
unload NetWare Loadable Modules (NLM), **initiate NCF files**, and **execute**  
console commands without **user** intercession. The **programs** are made up  
of a DOS-based administrator's program used to schedule events and...

10/3,K/3 (Item 2 from file: 275)  
DIALOG(R)File 275:Gale Group Computer DB(TM)  
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01541045 SUPPLIER NUMBER: 12745311 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Exclusively mine - how not to share data. (FoxPro data base management**  
**system) (The Fox Files) (Column) (Tutorial)**  
Goley, George F.; Dunn, Melissa  
Data Based Advisor, v10, n10, p122(2)  
Oct, 1992  
DOCUMENT TYPE: Tutorial ISSN: 0740-5200 LANGUAGE: ENGLISH  
RECORD TYPE: FULLTEXT; ABSTRACT  
WORD COUNT: 1529 LINE COUNT: 00117

... a multi-user application, however. Here's a simple scenario to  
demonstrate what I mean: **User A starts the application** first, and



executes this code segment. User B starts the application sometime later, and attempts to execute the same code segment. However, because User A created a TEMP.DBF, User B's attempt will fail...

10/3,K/4 (Item 3 from file: 275)  
DIALOG(R)File 275:Gale Group Computer DB(TM)  
(c) 2004 The Gale Group. All rts. reserv.

01503633 SUPPLIER NUMBER: 11936850 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
The device driver as state machine. (logical constructs)  
Nelson, Thomas  
C Users Journal, v10, n3, p41(1)  
March, 1992  
ISSN: 0898-9788 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT  
WORD COUNT: 4752 LINE COUNT: 00356

... ones like strcpy().  
Most compilers put uninitialized global data in a special segment that the startup code initializes to zeros when a program executes. Many programmers write C code that relies on this behavior, but it's an assumption you can't make in...

10/3,K/5 (Item 1 from file: 621)  
DIALOG(R)File 621:Gale Group New Prod.Annou.(R)  
(c) 2004 The Gale Group. All rts. reserv.

01184181 Supplier Number: 42722418 (USE FORMAT 7 FOR FULLTEXT)  
SOFTWARE ENHANCEMENTS AVAILABLE FOR NEW LOW-COST 68EC030 CPU  
News Release, p1  
Feb 2, 1992  
Language: English Record Type: Fulltext  
Document Type: Magazine/Journal; Trade  
Word Count: 365

... enhancements include EPROM patch  
locations for the implementation of user-developed drivers, and  
provisions to execute user application code upon start-up.

Although the XVME-991 can be used as a "stand-alone" debugger, it will...

?

13/3,K/1 (Item 1 from file: 88)  
DIALOG(R)File 88:Gale Group Business A.R.T.S.  
(c) 2004 The Gale Group. All rts. reserv.

02072734 SUPPLIER NUMBER: 06815391  
**A CASE for reverse engineering. (computer aided software engineering)**  
Bachman, Charlie  
Datamation, v34, n13, p49(5)  
July 1, 1988  
ISSN: 1062-8363 LANGUAGE: English RECORD TYPE: Fulltext; Abstract  
WORD COUNT: 2553 LINE COUNT: 00240

... and statements.  
Everything in IS exists so that, at the operational level, computer and terminal **operators** can successfully **execute programs**, **store real data**, and support the business. Design objects here include end users, terminals, computers, record instances, programs...

13/3,K/2 (Item 1 from file: 160)  
DIALOG(R)File 160:Gale Group PROMT(R)  
(c) 1999 The Gale Group. All rts. reserv.

01551960  
**Microcomputer-based control of a batch process.**  
CHEMICAL ENGINEERING January 19, 1987 p. 23-1251

... valves and motors, performing analog PID control, operating interlocks and alarms, collecting real-time field **data**, displaying real-time **process** and **operational** information, **executing operator** commands and **storing data** for **process** analyses and reports. Article discusses hardware selection and routine operation.  
...

13/3,K/3 (Item 1 from file: 15)  
DIALOG(R)File 15:ABI/Inform(R)  
(c) 2004 ProQuest Info&Learning. All rts. reserv.

02654184 456310571  
**Evaluation of network operating system security controls**  
Dunn, Cheryl L; Gerard, Gregory J; Worrell, James L  
Issues in Accounting Education v18n3 PP: 291-306 Aug 2003  
ISSN: 0739-3172 JRNL CODE: AIIA  
WORD COUNT: 7811

...TEXT: of networked systems. The NOS then authorizes the user to perform various functions (such as **read data**, change **data**, delete **data**, or **execute programs**) based on **stored user** and group settings established by the network administrator. Thus, it is important for auditors to...

13/3,K/4 (Item 2 from file: 15)  
DIALOG(R)File 15:ABI/Inform(R)  
(c) 2004 ProQuest Info&Learning. All rts. reserv.

00559738 91-34095

### Macintosh Computers in Accounting

Viator, Ralph E.; Willenborg, Michael  
CPA Journal v61n7 PP: 70-73 Jul 1991  
ISSN: 0732-8435 JRNL CODE: CPA  
WORD COUNT: 2369

...TEXT: the mouse/pointer and dragging the window until the desired size is obtained. Mac allows **users** to move **programs** into a directory by first **opening** a " **folder** " (the directory), grabbing the program's icon, and dragging it over to the opened folder...

... related programs and data files are placed into "folders," the equivalent of DOS directories. To **execute** a **program**, a **user** simply points to the picture of the program, double clicks the mouse, and the software... allows users to easily manipulate files. With the Windows 3.0 Clipboard, sharing information between **applications** is very easy. **Users** can temporarily **store** text from one application (a word processor, spreadsheet, accounting software) and later "paste" that text...

13/3,K/5 (Item 1 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2004 The Gale Group. All rts. reserv.

08467716 SUPPLIER NUMBER: 18010196 (USE FORMAT 7 OR 9 FOR FULL TEXT)

**Knowing the basics of PLCs, part 1. (programmable logic controllers)**

Hee, Robert B.

EC&M Electrical Construction & Maintenance, v94, n10, p20(5)

Oct, 1995

ISSN: 0013-4260

LANGUAGE: English

RECORD TYPE: Fulltext; Abstract

WORD COUNT: 3144 LINE COUNT: 00255

... as sensors and switches, implements them in a precise pattern determined by ladder-diagram-based **application** progress **stored** in **user** memory, and provides outputs for control of processes or user-supplied devices, such as relays...circuitry provides the signal conditioning required to communicate with the processor. The processor section also **executes** the **programmer**'s RLL software **program**.

The memory section **stores** (electronically) retrievable digital information in three dedicated locations of the memory. These memory locations are...

...program. The PLC not only requires electronic components to operate, it also needs a software **program**. The PLC **programmer** is not limited to **writing software** in one format. There are many types available, each lending itself more readily to one...

13/3,K/6 (Item 1 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)  
(c) 2004 The Gale Group. All rts. reserv.

02057143 SUPPLIER NUMBER: 19077500 (USE FORMAT 7 OR 9 FOR FULL TEXT)

**DB2 and Oracle7: A study of architectures. (Product Information)**

Sniatecki, Jim

Enterprise Systems Journal, v12, n1, p25(5)

Jan, 1997

ISSN: 1053-6566

LANGUAGE: English

RECORD TYPE: Fulltext; Abstract

WORD COUNT: 3491 LINE COUNT: 00275

... objects not currently found in DB2/MVS are triggers, roles, profiles, database links, sequences and **user**. DB2 V4 **stored procedures** are similar to Oracle's package and procedure. Oracle implements referential integrity through the CREATE...logic, buffers and control blocks for database services such as optimizer access logic, buffer management, **processing** SQL, retrieving/releasing **data** pages, set-level **processing**, accepting calls from **users** and returning the result set to the caller, space management and work file management. These...between the client application and the Oracle instance. A user process is created when a **user** **executes** an **application program**. This causes Oracle to create a server process to handle requests from connected **user processes**. The **user** process does not **execute** SQL statements; it merely establishes a communications link to an Oracle server process, which executes...

13/3,K/7 (Item 2 from file: 275)

DIALOG(R) File 275:Gale Group Computer DB(TM)

(c) 2004 The Gale Group. All rts. reserv.

01621437 SUPPLIER NUMBER: 14439624 (USE FORMAT 7 OR 9 FOR FULL TEXT)

Writing OpenVMS privileged code. (DEC's operating system) (Tutorial)

Goatley, Hunter; Heinrich, Edward A.

Digital Systems Journal, v15, n5, p34(5)

Sept-Oct, 1993

DOCUMENT TYPE: Tutorial ISSN: 1067-7224 LANGUAGE: ENGLISH

RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 3926 LINE COUNT: 00309

... system data structures. OpenVMS systems programs execute in one of these inner access modes.

Most **applications code** **executes** in **user** mode. User mode code must rely on system services and run-time library routines to...

...of the CHMx and REI instructions, or PALcode routines under OpenVMS AXP. For example, a **program** **executing** in **user** mode changes to kernel mode by ultimately executing a Change Mode Kernel (CHMK) instruction. The...P0 space, the program region, is the memory in the address range 0000000016 - 3FFFFFFF16. It **holds** **user programs** and any shareable images that are linked with a given program. Additional memory can be...

?

File 8: Ei Compendex(R) 1970-2004/Sep W2  
(c) 2004 Elsevier Eng. Info. Inc.  
File 35: Dissertation Abs Online 1861-2004/Aug  
(c) 2004 ProQuest Info&Learning  
File 202: Info. Sci. & Tech. Abs. 1966-2004/Sep 09  
(c) 2004 EBSCO Publishing  
File 65: Inside Conferences 1993-2004/Sep W3  
(c) 2004 BLDSC all rts. reserv.  
File 2: INSPEC 1969-2004/Sep W2  
(c) 2004 Institution of Electrical Engineers  
File 94: JICST-EPlus 1985-2004/Aug W4  
(c) 2004 Japan Science and Tech Corp (JST)  
File 111: TGG Natl. Newspaper Index(SM) 1979-2004/Sep 24  
(c) 2004 The Gale Group  
File 233: Internet & Personal Comp. Abs. 1981-2003/Sep  
(c) 2003 EBSCO Pub.  
File 6: NTIS 1964-2004/Sep W3  
(c) 2004 NTIS, Intl Cpyrght All Rights Res  
File 144: Pascal 1973-2004/Sep W2  
(c) 2004 INIST/CNRS  
File 434: SciSearch(R) Cited Ref Sci 1974-1989/Dec  
(c) 1998 Inst for Sci Info  
File 34: SciSearch(R) Cited Ref Sci 1990-2004/Sep W3  
(c) 2004 Inst for Sci Info  
File 62: SPIN(R) 1975-2004/Jul W4  
(c) 2004 American Institute of Physics  
File 99: Wilson Appl. Sci & Tech Abs 1983-2004/Aug  
(c) 2004 The HW Wilson Co.  
File 95: TEME-Technology & Management 1989-2004/Jun W1  
(c) 2004 FIZ TECHNIK

Set	Items	Description
S1	10177061	READ??? OR WRIT??? OR EXECUT??? OR OPERATING?? OR PROCESS?- ??? OR LAUNCH???
S2	740432	(S1 OR OPEN???) (3N) (FILE?? OR DATA?? OR SOFTWARE?? OR CODE? ? OR CODING OR FOLDER?? OR FILE??) (SYSTEM?? OR FILESYSTEM??)
S3	149983	(USER?? OR PROGRAMMER?? OR OPERATOR?? OR PROGRAMER??) (3N) (- PROCESS??? OR OPERATION?? OR ALGORITHM?? OR PROGRAM??? OR APP- LICATION?? OR PROCEDURE?? OR STEPS?? OR MODULE?? OR MACRO??)
S4	474	(HOLD??? OR RESERV??? OR STORING?? OR STOR??) (3N) S3
S5	159515	(TRIGGER??? OR INITIAT??? OR ACTIVAT??? OR START???) (7N) (E- XECUT??? OR OPERATING?? OR PROCESS???? OR LAUNCH???)
S6	608	(EXECUT?? OR EXECUTING??) (3N) S3
S7	20756	S2 AND S3
S8	1	S7 AND (S4 AND S5)
S9	1	S7 AND (S4 AND S6)
S10	1	S9 NOT S8
S11	247	S7 AND S5
S12	3	S11 AND S6
S13	3	RD (unique items)
S14	3	S13 NOT (S9 OR S8)
S15	33	S11 AND ACCESS??
S16	33	S11 AND ACCESS???
S17	28	RD (unique items)
S18	25	S17 NOT PY>2001

8/3,K/1 (Item 1 from file: 2)

DIALOG(R) File 2:INSPEC

(c) 2004 Institution of Electrical Engineers. All rts. reserv.

6724181 INSPEC Abstract Number: C2000-11-5520-004

**Title: One small step. I. Measuring flight data in model rockets**

Author(s): Napier, T.

Journal: Circuit Cellar Ink no.122 p.30-3

Publisher: Circuit Cellar Inc,

Publication Date: Sept. 2000 Country of Publication: USA

CODEN: CCINFK ISSN: 0896-8985

SICI: 0896-8985(200009)122L:30:SSMF;1-E

Material Identity Number: F415-2000-009

Language: English

Subfile: C

Copyright 2000, IEE

...Abstract: It has a PIC microcontroller, serial port, parallel port, and 2-KB serial EEPROM. The **processor** interprets the **user program stored** in the EEPROM. I foresaw it recording data, but that was not its primary function...

... similar but repackaged to fit in a long narrow tube. A trivial modificatisn to the **launch** stand can **start data** recording at lift-off using the same snatch-plug technique used in real rockets. Recording...  
?

10/3,K/1 (Item 1 from file: 202)  
DIALOG(R) File 202:Info. Sci. & Tech. Abs.  
(c) 2004 EBSCO Publishing. All rts. reserv.

2800132

**Computer system with file security function.**

Author(s): Orita, Y  
Patent Number(s): US 5163147  
Publication Date: Nov 10, 1992  
Language: English  
Document Type: Patent  
Journal Announcement: 2800

...a security function, environment profile information defining a file to be accessed and an executable **user program** are previously **stored** into a storage unit. The environment profile information is selected by operator profile information corresponding to ID information input via a work station by a user. A host computer **executes** the **user program** defined by the environment profile information. When a specified file access is requested after the execution of the **user program**, whether **execution** of the **file** access is permitted or not is determined according to access protection information. The access protection...

?

14/3,K/1 (Item 1 from file: 8)  
DIALOG(R)File 8:EI Compendex(R)  
(c) 2004 Elsevier Eng. Info. Inc. All rts. reserv.

04762322 E.I. No: EIP97073736287

**Title: Requirements of role-based access control for collaborative systems**

Author: Jaeger, Trent; Prakash, Atul

Corporate Source: Univ of Michigan, Ann Arbor, MI, USA

Conference Title: Proceedings of the 1995 1st ACM Workshop on Role-Based Access Control

Conference Location: Gaithersburg, MD, USA Conference Date: 19951130-19951201

E.I. Conference No.: 46670

Source: Proceedings of the ACM Workshop on Role-Based Access Control 1995. ACM, New York, NY, USA. p 53-64

Publication Year: 1995

CODEN: 002627

Language: English

Abstract: In many collaborative systems, users can **trigger** the execution of commands in a **process** owned by another **user**. Unless the access rights of such **processes** are limited, any **user** in the collaboration can gain access to another's private **files**; **execute applications** on another **user**'s behalf; or **read** public system **files**, such as the password file, on another user's machine. However, some applications require limited...

14/3,K/2 (Item 1 from file: 202)  
DIALOG(R)File 202:Info. Sci. & Tech. Abs.  
(c) 2004 EBSCO Publishing. All rts. reserv.

2703794

**Character reading system and method.**

Author(s): Yamanari, M

Patent Number(s): US 5153927

Publication Date: Oct 6, 1992

Language: English

Document Type: Patent

Journal Announcement: 2700

In a character reading system, a **user**-specific read **processing** is prepared by a user as separate from a standard read processing program (OCR handler...

...by a maker, and it is stored in advance as an object program (executable load **module**) in a **user execution file**. An FC sheet on which format control information used in executing read processing is written...

...file. Further, an interface file is prepared to provide an interface between the standard read **processing program** and the **user processing program**. A standard processing section fetches a slip and checks individual fields in the slip one by one, and **writes** in the interface **file** which file it is presently checking and the result of the checking. When there comes a field having **user** processing designated, the standard **processing** section **starts** a **user processing** section. The **user**



**processing** section **executes** an executable load **module** and performs specific **user processing** based on the information **written** in the interface **file** . After completion of the **user processing** , the **processing** result is **written** in the interface file and the standard **processing** is **started** again.

14/3,K/3 (Item 2 from file: 202)

DIALOG(R)File 202:Info. Sci. & Tech. Abs.

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1100577

**Jsys traps-a tenex mechanism for encapsulation of user processes .**  
Book Title: In American Federation Of Information Processing Societies.  
Afips Conference Proceedings. Volume 44. 1975 National Computer Conference,  
May 19-22, 1975, Anaheim, California. P. 351-360. Contract  
Dahcl5-71-c-0088. 9 Illus. 17 Ref. Supported By Advanced Res  
Author(s): Thomas, Robert H  
Corporate Source: Bolt, Beranek And Newman, Inc., Cambridge, Massachusetts  
Publication Date: 1975  
Language: English  
Document Type: Book Chapter  
Journal Announcement: 1100

**Jsys traps-a tenex mechanism for encapsulation of user processes .**

...runs on a dec pdp-10 processor augmented with paging hardware; tenex provides a multi-**process** job structure with **software** program interrupt capabilities, advanced file handling features, and an interactive command language. The jsys trap...

...is then passed. The trapping mechanism is done within the existing operating system. The tenex **process** has a **user** address space and a monitor address space, the latter being invisible to the **user program** where the **process executes** monitor routines in response to system calls **initiated** by **execution** in the user address space. The multiple process nature of the trapping mechanism makes hierarchies...

18/3,K/1 (Item 1 from file: 8)  
DIALOG(R) File 8: Ei Compendex(R)  
(c) 2004 Elsevier Eng. Info. Inc. All rts. reserv.

06298840 E.I. No: EIP03087365056

**Title: Adaptive interaction for enabling pervasive services**  
Author: Samulowitz, Michael; Michahelles, Florian; Linnhoff-Popien, Claudia  
Corporate Source: Corporate Technology Siemens AG, D-81730 Munich, Germany  
Conference Title: Proceedings of the Second ACM International Workshop on Data Engineering for Wireless and Mobile Access: MobiDE 2001  
Conference Location: Santa Barbara, CA, United States Conference Date: 20010520-20010520  
E.I. Conference No.: 60385  
Source: Proceedings of the Second ACM International Workshop on Data Engineering for Wireless and Mobile Access: MobiDE 2001 2001.  
Publication Year: 2001  
ISBN: 1581134126  
Language: English

Abstract: We describe an architecture that allows mobile users to **access** a variety of services provided by pervasive computing environments. Novel to our approach is that...

...the embedded constraints. Services are identified by characterizing attributes, rather than explicit network addresses. The **execution** of selected services may be deferred, and **triggered** by a specific event. Service requests carry the context of their use, therefore our system...

Descriptors: Mobile computing; **Data processing** ; **User** interfaces; Semantics; Laptop computers

18/3,K/2 (Item 2 from file: 8)  
DIALOG(R) File 8: Ei Compendex(R)  
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05016240 E.I. No: EIP98054214624

**Title: EMIS: a decision support system for forest fire prevention and fighting**

Author: Wybo, J.-L.  
Corporate Source: Ecole des Mines de Paris, Sophia Antipolis, Fr  
Source: IEEE Transactions on Engineering Management v 45 n 2 May 1998. p 127-131  
Publication Year: 1998  
CODEN: IEEMA4 ISSN: 0018-9391  
Language: English

...Abstract: domain being representative of rapidly increasing emergencies. Designing such a system means combining tasks: automate **data processing** , give the **user** efficient **access** to all relevant data, and synthesize pertinent information. Data used for decisions come from several ...

...and a real-time information manager, whose task is to follow the scenarios describing the **processes** to **trigger** for all incoming data. This architecture, based on specifications and knowledge, allows autonomous behavior of...

...s decisions. It uses a declarative approach, i.e., it separates what is application dependent ( **data** , scenarios, and **processes** ) from what is generic and can be used for other applications. The Fire Management Information...

Descriptors: Fire protection; Forestry; Decision support systems; Information retrieval systems; Information management; **Data processing** ; Database systems

**18/3,K/3** (Item 3 from file: 8)  
DIALOG(R)File 8: Ei Compendex(R)  
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04969990 E.I. No: EIP97093836283

**Title: Ambient air measurements and practical limits determinations with an improved DOAS system**

Author: Schiff, Harold I.; Robbins, John; Chanda, Alak; Nadler, Schachar D.; Mackay, Gervase I.; Kita, Pieter; Appel, Dirk

Conference Title: Proceedings of the 1996 Air & Waste Management Association's 89th Annual Meeting & Exhibition

Conference Location: Nashville, TN, USA Conference Date: 19960623-19960628

E.I. Conference No.: 46962

Source: Proceedings of the Air & Waste Management Association's Annual Meeting & Exhibition 1996. Air & Waste Management Assoc, Pittsburgh, PA, USA. 13pp 96-TP26B.04

Publication Year: 1996

CODEN: PAMEE5

Language: English

...Abstract: A RS232 link couples the spectrometer to a host laptop computer. The system uses an **open software** architecture, based on an easy-to-operate Windows...  
...environment. In standard mode, clicking on a menu item **starts** automatic **data** acquisition and **processing** . The screen interface shows only the output results and some 'housekeeping' data. In a second mode of **operation** , a qualified **user** can **access** and display incoming and processed spectra to follow each **data processing** step of a measurement. Practical detection limits for these DOAS instruments have been determined from...

Descriptors: Air pollution control equipment; Optical telescopes; Fiber optics; Spectrometers; Optical **data processing** ; Personal computers; Computer **software** ; Data acquisition

**18/3,K/4** (Item 4 from file: 8)  
DIALOG(R)File 8: Ei Compendex(R)  
(c) 2004 Elsevier Eng. Info. Inc. All rts. reserv.

04762322 E.I. No: EIP97073736287

**Title: Requirements of role-based access control for collaborative systems**

Author: Jaeger, Trent; Prakash, Atul

Corporate Source: Univ of Michigan, Ann Arbor, MI, USA

Conference Title: Proceedings of the 1995 1st ACM Workshop on Role-Based Access Control

Conference Location: Gaithersburg, MD, USA Conference Date:

19951130-19951201

E.I. Conference No.: 46670

Source: Proceedings of the ACM Workshop on Role-Based Access Control  
1995. ACM, New York, NY, USA. p 53-64

Publication Year: 1995

CODEN: 002627

Language: English

**Title: Requirements of role-based access control for collaborative systems**

**Abstract:** In many collaborative systems, users can **trigger** the execution of commands in a **process** owned by another **user**. Unless the **access** rights of such **processes** are limited, any **user** in the collaboration can gain **access** to another's private **files**; **execute applications** on another **user**'s behalf; or **read** public system **files**, such as the password file, on another user's machine. However, some applications require limited sharing of private files, so it may be desirable to grant **access** to these files for a specific purpose. Role-based **access** control (RBAC) models can be used to limit the **access** rights of processes, but current implementations do not enable users to flexibly control the **access** rights of a process at runtime. We define a discretionary **access** control model that enables principals to flexibly control the **access** rights of a collaborative process. We then specify the requirements of RBAC models necessary to implement this discretionary **access** control model. (Author abstract) 18 Refs.

**Identifiers:** Role based **access** control; Collaborative systems; Discretionary **access** control

18/3,K/5 (Item 5 from file: 8)

DIALOG(R)File 8: Ei Compendex(R)

(c) 2004 Elsevier Eng. Info. Inc. All rts. reserv.

04641993 E.I. No: EIP97033562506

**Title: Reducing the communication overhead of dynamic applications on shared memory multiprocessors**

**Author:** Sivasubramaniam, Anand

**Corporate Source:** Pennsylvania State Univ, University Park, PA, USA

**Conference Title:** Proceedings of the 1997 3rd International Symposium on High-Performance Computer Architecture, HPCA

**Conference Location:** San Antonio, TX, USA **Conference Date:** 19970201-19970205

E.I. Conference No.: 46139

**Source:** IEEE High-Performance Computer Architecture Symposium Proceedings 1997. IEEE, Los Alamitos, CA, USA, 97TB100094. p 194-203

**Publication Year:** 1997

**CODEN:** 85QSAT

**Language:** English

...Abstract: This makes them particularly appealing for applications with dynamic communication behavior since the mechanisms for **data** transfer between **processors** is hidden from the **programmer**. But the scalability of these machines is often limited by the latencies incurred in **accessing** locations in remote memories. Caches alleviate this problem by exploiting the temporal and spatial locality...

...the read and write latencies of applications with dynamic communication behavior by employing intelligent sender- **initiated** data transfer

mechanisms. In the **process** , we would like to keep our demands from the **programmer** , the compiler, and the hardware as low as possible. Towards this goal, we present a set of write primitives that lower the communication overhead for shared memory **accesses** governed by locks. We demonstrate the performance benefits of these primitives using a database application...

...Information Systems (GIS) domain. We explore the competitive update mechanism for the remaining shared memory **accesses** . Using a set of applications, we examine the amount of history that we need to...

Identifiers: Cache coherent non uniform memory **access** (NUMA) machines

18/3,K/6 (Item 6 from file: 8)

DIALOG(R) File 8: Ei Compendex(R)

(c) 2004 Elsevier Eng. Info. Inc. All rts. reserv.

04005192 E.I. No: EIP94122476747

**Title: Software-based training simplifies access , records progress**

Author: Hodel, Alan E.

Source: Chemical Processing v 57 n 9 Sept 1994. p 73-74

Publication Year: 1994

CODEN: CPROAI ISSN: 0009-2630

Language: English

**Title: Software-based training simplifies access , records progress**

...Abstract: requirements, electronically presenting documents that personnel know and understand. The system provides immediate, convenient, local **access** to **operating** procedures, **start** -up/shutdown procedures, emergency procedures, Material Safety Data Sheets (MSDS) and Hazardous Material Work Sheets...

Descriptors: Personnel training; Computer **software** ; Computer **operating procedures** ; Personal computers; **User** interfaces; Interactive computer graphics; Cameras; Accident prevention; Process control; Information dissemination

18/3,K/7 (Item 7 from file: 8)

DIALOG(R) File 8: Ei Compendex(R)

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01866474 E.I. Monthly No: EIM8504-022990

**Title: INTEGRATING ACTION CAPABILITIES INTO INFORMATION DATABASES.**

Author: Melkanoff, Michel A.; Chen, Qiming

Corporate Source: Univ of California, Los Angeles, Computer Science Dep, Los Angeles, CA, USA

Conference Title: Proceedings - Second International Conference on Databases, ICOD-2.

Conference Location: Cambridge, Engl Conference Date: 19830830

E.I. Conference No.: 05308

Source: Publ by John Wiley & Sons Ltd (British Computer Soc Workshop Series), Chichester, Engl and New York, NY, USA p 222-245

Publication Year: 1983

ISBN: 0-471-90309-4

Language: English

...Abstract: a GDBMS incorporates actions into a relational database, it can convey the effects of complex **data processing operations** to

users by providing access to the resulting data while shielding the internal complexity of the system. An experimental GDBMS...

...Descriptors: Management; **DATA PROCESSING** --  
Identifiers: INFORMATION DATABASE INTEGRATION; GENERALIZED DATABASE MANAGEMENT SYSTEM (GDBMS); VIRTUAL RELATION; DECISION/ACTION PROGRAMS; DYNAMIC **TRIGGERS** ; MANUFACTURING ORDER **PROCESSING** EXAMPLE

18/3,K/8 (Item 8 from file: 8)  
DIALOG(R)File 8:EI Compendex(R)  
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00816842 E.I. Monthly No: EI7905037696 E.I. Yearly No: EI79080949  
Title: **SCHEMES FOR THE CONTROL OF THE ACTIVATED SLUDGE PROCESS VIA DIGITAL COMPUTER.**

Author: Younkin, Clyde S.; Ballotti, Elmer F.; Nelson, Michael D.  
Corporate Source: Greeley & Hanson, Philadelphia, Pa  
Source: Advances in Instrumentation v 33 1978 Proc of the ISA Conf and Exhib, Philadelphia, Pa, Oct 15-19 1978. Publ by ISA, Pittsburgh, Pa, 1978 pt 3 p 107-118  
Publication Year: 1978  
CODEN: AVINBP ISSN: 0065-2814  
Language: ENGLISH

Title: **SCHEMES FOR THE CONTROL OF THE ACTIVATED SLUDGE PROCESS VIA DIGITAL COMPUTER.**

...Abstract: the computer console. The computer console will contain three cathode ray tubes capable of displaying **process** graphics with live **data** , as well as graphic displays of the various control **algorithms** . The **operator** has **access** to a computer keyboard which, in addition to selecting control schemes, may be used for...

...over the plant. Field instrumentation is used to gather data on the status of the **process** . This **data** is multiplexed to the computer which, on the basis of the operator selected control scheme...

18/3,K/9 (Item 1 from file: 2)  
DIALOG(R)File 2:INSPEC  
(c) 2004 Institution of Electrical Engineers. All rts. reserv.

5909227 INSPEC Abstract Number: C9806-7130-022  
Title: **FMIS: a decision support system for forest fire prevention and fighting**

Author(s): Wybo, J.-L.  
Author Affiliation: Ecole des Mines de Paris, Sophia Antipolis, France  
Journal: IEEE Transactions on Engineering Management vol.45, no.2 p.127-31  
Publisher: IEEE,  
Publication Date: May 1998 Country of Publication: USA  
CODEN: IEEMA4 ISSN: 0018-9391  
SICI: 0018-9391(199805)45:2L:127:FDSS;1-M  
Material Identity Number: I039-98002  
U.S. Copyright Clearance Center Code: 0018-9391/98/\$10.00  
Language: English  
Subfile: C  
Copyright 1998, IEE

...Abstract: domain being representative of rapidly increasing emergencies. Designing such a system means combining tasks: automate **data processing** ; give the **user** efficient **access** to all relevant data; and synthesize pertinent information. Data used for decisions come from several ...

... and a real-time information manager, whose task is to follow the scenarios describing the **processes** to **trigger** for all incoming data. This architecture, based on specifications and knowledge, allows autonomous behavior of...

... s decisions. It uses a declarative approach, i.e. it separates what is application dependent ( **data** , scenarios and **processes** ) from what is generic, and can be used for other applications. The Fire Management Information...

18/3,K/10 (Item 2 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2004 Institution of Electrical Engineers. All rts. reserv.

5834578 INSPEC Abstract Number: C9803-6160J-022

Title: Reduction of response time for retrieval operation in object-oriented database systems by introducing speculative execution technique in parallel environments

Author(s): Takayama, T.; Hironaka, T.; Fujino, S.

Author Affiliation: Dept. of Comput. Eng., Hiroshima City Univ., Japan

Journal: Transactions of the Information Processing Society of Japan  
vol.38, no.11 p.2274-85

Publisher: Inf. Process. Soc. Japan,

Publication Date: Nov. 1997 Country of Publication: Japan

CODEN: JSGRD5 ISSN: 0387-5806

SICI: 0387-5806(199711)38:11L:2274:RRTR;1-4

Material Identity Number: T205-98001

Language: Japanese

Subfile: C

Copyright 1998, IEE

...Abstract: time for a retrieval operation in object oriented database systems (OODB) by introducing a speculative **execution** technique, which **starts** to **execute** a set of program **codes** before evaluating its **execution** condition, in parallel environments. Recently, many researchers have tried parallel query processing in OODB. However, load balancing is known to be more difficult than in relational DBs because of the **access** method to an object using its object identifier. In general, it is response time that is important for a DB **user** , and not query **processing** time for a DB system. The speculative execution technique is known as an effective technique...

...Identifiers: **access** method

18/3,K/11 (Item 3 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2004 Institution of Electrical Engineers. All rts. reserv.

5033827 INSPEC Abstract Number: B9510-6320-016, C9510-7840-014

Title: The EARSEC SAR processing system

Author(s): Protheroe, M.; Sloggett, D.; Sieber, A.

Author Affiliation: Earth Observation Scis. Ltd., Farnham, UK  
Journal: Proceedings of the SPIE - The International Society for Optical  
Engineering Conference Title: Proc. SPIE - Int. Soc. Opt. Eng. (USA)  
vol.2316 p.42-50  
Publication Date: 1994 Country of Publication: USA  
CODEN: PSISDG ISSN: 0277-786X  
U.S. Copyright Clearance Center Code: 0 8194 1646 0/94/\$6.00  
Conference Title: SAR Data Processing for Remote Sensing  
Conference Sponsor: SPIE; Comm. Eur. Communities; Eur. Opt. Soc.; et al  
Conference Date: 28-30 Sept. 1994 Conference Location: Rome, Italy  
Language: English  
Subfile: B C  
Copyright 1995, IEE

...Abstract: Centre (JRC) Institute for Remote Sensing at Ispra in Italy,  
realising the limitations on current **processing** abilities, **initiated**  
its own programme to build airborne SAR and electro-optical (EO) sensor  
systems. This programme...

... of the EARSEC airborne SAR processing system. It highlights the  
development of an open SAR **processing** architecture where **users** have  
full **access** to intermediate products that arise from each of the major  
processing stages. It also describes...

... the overall architecture and illustrates the results of each of the key  
stages in the **processor**. The EARSEC **data processing** environment  
comprises four major elements. These are the data ingestion facility, the  
graphical user interface...

...Identifiers: **data processing** environment

18/3,K/12 (Item 4 from file: 2)  
DIALOG(R)File 2:INSPEC  
(c) 2004 Institution of Electrical Engineers. All rts. reserv.

04329954 INSPEC Abstract Number: C9303-7210L-009

Title: **CD-ROM networking developments amongst the Oxford libraries**

Author(s): Price, D.J.

Book Title: Innovation for information: international contributions to  
librarianship. Festschrift in honour of Dr. Ahmed H. Helal p.183-96

Editor(s): Weiss, J.W.

Publisher: Essen University Library, Essen, Germany

Publication Date: 1992 Country of Publication: West Germany x+284 pp.

ISBN: 3 922602 16 9

Language: German; English; French; Arabic

Subfile: C

Abstract: Describes two approaches to providing wide area, multi-user  
**access** to CD-ROM databases in Oxford University's mixed networking  
environment. The main difficulties perceived...

... CD-Ware which is only suitable for VAX/VMS hosts. An advantage is that  
each **user starts** a VMS **process** when logging on, so the administrator  
has sophisticated tools like DCL and All-In-1...

... and VTnnn terminals are supported. Against it, perhaps, is the cost:  
Pounds 11000 to provide **access** to two CD drives for four users. Opti-Net  
should provide a reasonable service though, initially at least, remote  
**access** is not as easy as one would wish and might have to be restricted  
to...



... the CD suppliers look towards a future of open systems and distributed processing, and re- write their **software** accordingly.

...Identifiers: multi-user **access** ; ...

...remote **access** ;

**18/3,K/13** (Item 5 from file: 2)

DIALOG(R) File 2:INSPEC

(c) 2004 Institution of Electrical Engineers. All rts. reserv.

03816536 INSPEC Abstract Number: B91011688, C91017293

**Title: dBase IV in a LAN environment**

Author(s): Medvec, L.

Journal: Mechanizace Automatizace Administrativy vol.30, no.9 p.  
346-51

Publication Date: 1990 Country of Publication: Czechoslovakia

CODEN: MAUAAU ISSN: 0322-8452

Language: Slovak

Subfile: B C

...Abstract: dBase IV in a local area network. Aspects covered by the author include network architecture, **initiating** and **operating** procedures, **data** protection, **opening data files** and commands relating to the latter, problems encountered in multi- **user applications**. Errors which may arise and their remedy by the system or by operator with system...

...Descriptors: multi- **access** systems...

...Identifiers: multi- **user applications**

**18/3,K/14** (Item 6 from file: 2)

DIALOG(R) File 2:INSPEC

(c) 2004 Institution of Electrical Engineers. All rts. reserv.

03244376 INSPEC Abstract Number: B88064675, C88062046

**Title: Statistical process control: higher quality through worksite data management**

Author(s): Johnson, W.; Smith, A.

Author Affiliation: Prometrix Corp., Santa Clara, CA, USA

Journal: Microelectronic Manufacturing and Testing vol.11, no.6 p.  
24-6

Publication Date: May 1988 Country of Publication: USA

CODEN: MMTEEN ISSN: 0161-7427

Language: English

Subfile: B C

...Abstract: advanced quality control methods is establishing an efficient link between the collection and analysis of **process data**, and the timely **initiation** of corrective measures if required. The concept of local worksite data management unites all these functions. Semiconductor process lines now have **access** to integrated systems that combine sophisticated statistical packages with the most advanced measurement technology. Such systems enable a single **operator** to monitor a **process**, collect and display **data** in a number of forms, and evaluate results to obtain valuable insight about a process...

... help meet the data management needs of the semiconductor industry,

'StatTrax', a highly sophisticated statistical **software** package that displays **process data** in the form of trend charts, X-bar and R charts and histograms has been...

18/3,K/15 (Item 1 from file: 94)  
DIALOG(R)File 94:JICST-EPlus  
(c)2004 Japan Science and Tech Corp(JST). All rts. reserv.

01647296 JICST ACCESSION NUMBER: 93A0003450 FILE SEGMENT: JICST-E  
**Use of JERS-1 data for resources applications.**  
YOSHIDA KAZUHIKO (1); KAWAKAMI TOORU (1); SAITO BU (1)  
(1) Earth Resources Satellite Data Analysis Center  
Nippon Rimoto Senshingu Gakkaishi(Journal of Remote Sensing Society of  
Japan), 1992, VOL.12,NO.3, PAGE.339-342, FIG.2  
JOURNAL NUMBER: X0714AAX ISSN NO: 0289-7911  
UNIVERSAL DECIMAL CLASSIFICATION: 550.82/.87  
LANGUAGE: Japanese COUNTRY OF PUBLICATION: Japan  
DOCUMENT TYPE: Journal  
ARTICLE TYPE: Commentary  
MEDIA TYPE: Printed Publication

...ABSTRACT: sponsorship by MITI, is composed of 3 main subsystems: PRS for performing of basic image **processings** and management of **data** storages, ALS for making of standard products and support of R&D activities, and DRS...

...of routine works related to user services would be performed on the semi-automatic basis, **initiated** by **accesses** from **user** terminals (word **processors** or personal computers) through data communication lines. (author abst.)

...DESCRIPTORS: **data processing** ;

18/3,K/16 (Item 1 from file: 233)  
DIALOG(R)File 233:Internet & Personal Comp. Abs.  
(c) 2003 EBSCO Pub. All rts. reserv.

00227651 90SN10-004  
**As systems distribute, security concerns rise -- OSI only part of distributed 'security perimeter'; standards groups push multivendor net solutions**

Cashin, Jerry  
Software Magazine , October 1, 1990 , v10 n12 p83-86, 91+, 6 Page(s)  
ISSN: 0897-8085  
Company Name: IBM Corp.; Digital Equipment Corp.; Unix International;  
**Open Software** Foundation; National Institute for Science and Technology

Company Name: IBM Corp.; Digital Equipment Corp.; Unix International;  
**Open Software** Foundation; National Institute for Science and Technology

... as distributed systems disperse at a faster rate than ever. Explains the three approaches in **initiating** basic **access** security within networks of dispersed **processing** system **users** . Explains also the benefits and shortcomings imparted by local area networks (LANs) in military applications...

...others discuss security strategies presented by Unix International Inc., Digital Equipment Corp. (DEC) and the **Open Software** Foundation (OSF). Includes two diagrams and photos. (PAM)

Identifiers: IBM Corp.; Digital Equipment Corp.; Unix International; **Open Software** Foundation; National Institute for Science and Technology

18/3,K/17 (Item 1 from file: 6)

DIALOG(R)File 6:NTIS

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1513603 NTIS Accession Number: PB90-225269

**Echodispisp Custom Analysis Routines. ECAR, Version 2.0. User and Programming Documentation**

Normann, O. F. ; Linker, D.

Selskapet for Industriell og Teknisk Forskning, Trondheim (Norway). Div. of Medical Technology.

Corp. Source Codes: 084057004

Sponsor: Royal Norwegian Council for Scientific and Industrial Research, Oslo.

Report No.: STF23-A90007; ISBN-82-595-5746-0

16 Mar 90 76p

Languages: English

Journal Announcement: GRAI9017

Sponsored by Royal Norwegian Council for Scientific and Industrial Research, Oslo.

Order this product from NTIS by: phone at 1-800-553-NTIS (U.S. customers); (703)605-6000 (other countries); fax at (703)321-8547; and email at orders@ntis.fedworld.gov. NTIS is located at 5285 Port Royal Road, Springfield, VA, 22161, USA.

NTIS Prices: PC E06/MF E06

...which will be recognized, and appear as a part of EchoDisp itself when EchoDisp is **started**. The ECAR-routines have the ability to **execute** functions and **access data** structures internal to EchoDisp. There is also a mechanism for communication to EchoBase (a database...

Identifiers: Foreign technology; \*Image **processing** ; \*Computer **applications** ; Ultrasonics; **Data** structures; **User** manuals(Computer **programs** ); NTISTFSIN

18/3,K/18 (Item 2 from file: 6)

DIALOG(R)File 6:NTIS

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1321644 NTIS Accession Number: DE87010323

**SLAC (Stanford Linear Accelerator Center) Courier and Remote Login Facility**

Huffer, M. E.

Stanford Linear Accelerator Center, CA.

Corp. Source Codes: 014489000; 5910000

Sponsor: Department of Energy, Washington, DC.

Report No.: SLAC-314

Apr 87 33p

Languages: English

Journal Announcement: GRAI8722; NSA1200

Paper copy only, copy does not permit microfiche production. Original copy available until stock is exhausted. Order this product from NTIS by:

phone at 1-800-553-NTIS (U.S. customers); (703)605-6000 (other countries); fax at (703)321-8547; and email at orders@ntis.fedworld.gov. NTIS is located at 5285 Port Royal Road, Springfield, VA, 22161, USA.

NTIS Prices: PC A03

The Courier and Remote Login Facility (CRLF) serves two functions. First, it provides for **application programmers** a presentation layer protocol called Courier. Courier enforces a remote procedure call discipline and allows the **programmer** to pass data between systems in a system independent form. Second, it implements a Remote Login Facility. This facility verifies a client's **access** rights, constructs an environment based on these rights, and **starts** up a program to **process** client requested services. Courier allows communication between two system elements (or hosts) by causing the creation of two **programs**: a **user program** on the initiating host and a server program on the remote host. Communication then takes...

Descriptors: Computer Networks; Communications; **Executive Codes** ; Security

Identifiers: Computer programming; ERDA/990220; Application **programs** (Computers); Courier protocol; **Programmers** ; NTISDE

18/3,K/19 (Item 3 from file: 6)

DIALOG(R)File 6:NTIS

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1231901 NTIS Accession Number: DE86057772

**ARCHIVE; NOS2.2 Permanent File Backup System**

(Software)

Ahlgrim, S.

Fermi National Accelerator Lab., Batavia, IL.

Corp. Source Codes: 056178000

Report No.: ANL/NESC-9772

1985 mag tape

Languages: English

Journal Announcement: GRAI8611

Price includes documentation. Tapes can be prepared in most recording modes for one-half inch tape. Specify recording mode desired. Call NTIS Computer Products if you have questions.

NTIS Prices: CP T99

... consists of: (1) creating backup files by dumping disk files to magnetic tape storage, (2) **processing user - initiated** requests for backed-up files, (3) controlling disk space by releasing disk files that have...

...ARCHIVE locates the tape copy of the file and restores it to disk. Users may **access** the database identifying the locations in the archive of all known versions of any of...

Descriptors: **Software** ; CDC computers; **Operating** systems; Information systems; Management

18/3,K/20 (Item 4 from file: 6)

DIALOG(R)File 6:NTIS

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1162356 NTIS Accession Number: N85-15377/3

# **High Speed Magnetic Tape Interface for a Microcomputer**

Scott, J. C.

Commonwealth Scientific and Industrial Research Organization, Aspendale (Australia). Div. of Atmospheric Research.

Corp. Source Codes: 007209004; CX575216

Sponsor: National Aeronautics and Space Administration, Washington, DC.

Report No.: CSIRO-TP-7; ISBN-0-643-03743-8

1984 20p

Languages: English

Journal Announcement: GRAI8509; STAR2306

Order this product from NTIS by: phone at 1-800-553-NTIS (U.S. customers); (703)605-6000 (other countries); fax at (703)321-8547; and email at orders@ntis.fedworld.gov. NTIS is located at 5285 Port Royal Road, Springfield, VA, 22161, USA.

NTIS Prices: PC A02/MF A01

... magnetic tape unit and an Apple type microcomputer is described. The interface uses direct memory **access** (DMA) which is capable of data transfer rates up to  $2 \times 10^6$  8 bit bytes per second. After **initiation** of **data** transfer, the **process** is completely transparent to the processor. All functions of the magnetic tape unit are available to the **programmer** via any level of programming, and the tapes produced are fully compatible with ANSI and...

Descriptors: Magnetic tapes; \*Microcomputers; Interfaces; **Access** control; Data storage; Infrared scanners

18/3,K/21 (Item 5 from file: 6)

DIALOG(R)File 6:NTIS

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0751783 NTIS Accession Number: N79-17305/0/XAB

## **Erlangen Project Interactive Cartography Erlanger Projekt Interaktive Kartographie**

Henning, W. ; Kleinoeder, W. ; Kneissl, F. ; Volkert, J.

Erlangen-Nuremberg Univ. (West Germany). Inst. fuer Mathematische Maschinen und Datenverarbeitung.

Report No.: BMVG-FBWT-78-9

1978 202p

Languages: German

Journal Announcement: GRAI7912; STAR1708

Subm-Sponsored by Bundesmin. Fuer Verteidigung. Text in German, Summary in English.

Order this product from NTIS by: phone at 1-800-553-NTIS (U.S. customers); (703)605-6000 (other countries); fax at (703)321-8547; and email at orders@ntis.fedworld.gov. NTIS is located at 5285 Port Royal Road, Springfield, VA, 22161, USA.

NTIS Prices: PC A10/MF A01

Concept and implementation of an interactive information system, based on topographic data, are presented. **Starting** from maps, data were obtained via different **processes** of acquisition and preprocessing for a data base in which storage and **access** for the information system were organized. A set-oriented data manipulation language and a graphic subsystem for interactive manipulation of visual data operated on the data base. Some **user algorithms** were included. The system was suitable for modelling of aspects of the real world with...

Descriptors: Information systems; \*Topography; Algorithms; **Data** bases;

**Data processing**

**18/3,K/22 (Item 1 from file: 144)**

DIALOG(R)File 144:Pascal

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10576438 PASCAL No.: 93-0085690

**Design of optimal distributed file systems : a framework for research**

BORGHOFF U M

Tech. Univ. Muenchen, Inst. informatik, 8000 Muenchen, Federal Republic of Germany

Journal: Operating systems review, 1992, 26 (4) 30-61

Language: English

...to reduce the complexity of the optimization problems, a cluster-based approach is used. To **access** the data files of a distributed file system, a **user** **initiates** a **program execution** . Based on the current allocation of the program and data files as well as the...

English Descriptors: System performance; Replica method; Optimal algorithm; Optimal allocation; Complexity; Program **execution** ; **File** management; **File** structure; Distributed system; **Operating** system; Computer network ; Computer system

**18/3,K/23 (Item 1 from file: 99)**

DIALOG(R)File 99:Wilson Appl. Sci & Tech Abs

(c) 2004 The HW Wilson Co. All rts. reserv.

1394538 H.W. WILSON RECORD NUMBER: BAST96054105

**Fire in the sky**

Riley, W. D;

Datamation v. 42 (Sept. '96) p. 43

DOCUMENT TYPE: Feature Article ISSN: 0011-6963

...ABSTRACT: archive a large quantity of data in a reasonable amount of time. To use the **application** , the **user** must first "share" the drives at an administrative level and, as there is no remote administrator, go to the server to **start** the **process** ; this can result in the backup of critical systems that are unattended by end users. Moreover, as NT is a true preemptive-multitasking operating system, the user can have full **access** to the computer while Backup Exec runs in the background. Other backup devices that have...

DESCRIPTORS: Backup storage ( **Data processing** ) ; ;

**18/3,K/24 (Item 1 from file: 95)**

DIALOG(R)File 95:TEME-Technology & Management

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01180029 I98022151300

**Titel japanisch**

(Reduction of response time for retrieval operation in object-oriented database systems by introducing speculative execution technique in parallel environments)

Takayama, T; Hironaka, T; Fujino, S

Dept. of Comput. Eng., Hiroshima City Univ., Japan  
Transactions of the Information Processing Society of Japan, v38, n11,  
pp2274-2285, 1997  
Document type: journal article Language: Japanese  
Record type: Abstract  
ISSN: 0387-5806

ABSTRACT:

...time for a retrieval operation in object oriented database systems (OODB) by introducing a speculative **execution** technique, which **starts** to **execute** a set of program **codes** before evaluating its **execution** condition, in parallel environments. Recently, many researchers have tried parallel query processing in OODB. However, load balancing is known to be more difficult than in relational DBs because of the **access** method to an object using its object identifier. In general, it is response time that is important for a DB **user**, and not query **processing** time for a DB system. The speculative execution technique is known as an effective technique...  
DESCRIPTORS: OBJECT ORIENTED DATABASES; PARALLEL PROGRAMMING; RESPONSE TIME ; QUERY **PROCESSING** ; **SOFTWARE** PERFORMANCE EVALUATION

18/3,K/25 (Item 2 from file: 95)  
DIALOG(R) File 95:TEME-Technology & Management  
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00668723 E93056003021

**A graphical programming environment for simulation of control and signal processing systems**

(Eine graphische Programmierungsumgebung zur Simulation von Steuerungs- und Signalverarbeitungssystemen)  
Prashant Wakis; Karsai, G; Sztipanovits, J  
Vanderbilt Univ., Nashville, USA  
IEEE Southeastcon '92, Volume 1, Birmingham, USA, April 12-15, 19921992  
Document type: Conference paper Language: English  
Record type: Abstract  
ISBN: 0-7803-0495-0

ABSTRACT:

...the application programming. The authors have developed a graphical programming library for control and signal **processing** applications. The library covers a wide range **starting** from basic primitives such as simple arithmetic and boolean functions to complicated units like generalized neural networks. The user gets **access** to these units in the form of icons. By making connections to and from these...  
...DESCRIPTORS: **PROCESS DATA PROCESSING** ; PROGRAMMING ENVIRONMENTS; COMPUTER GRAPHICS; GRAPHIC PRESENTATION; COMPUTER PROGRAMMING; WINDOW SYSTEM; **USER** INTERFACES; **PROGRAM** LIBRARY; ARTIFICIAL NEURAL NETWORKS  
?

File 348:EUROPEAN PATENTS 1978-2004/Sep W02

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File 349:PCT FULLTEXT 1979-2002/UB=20040923,UT=20040916

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Set	Items	Description
S1	1403798	READ??? OR WRIT??? OR EXECUT??? OR OPERATING?? OR PROCESS?- ??? OR LAUNCH???
S2	226042	(S1 OR OPEN???) (3N) (FILE?? OR DATA?? OR SOFTWARE?? OR CODE? ? OR CODING OR FOLDER?? OR FILE??) SYSTEM?? OR FILESYSTEM??)
S3	107067	(USER?? OR PROGRAMMER?? OR OPERATOR?? OR PRGRAMER??) (3N) (- PROCESS??? OR OPERATION?? OR ALGORITHM?? OR PROGRAM??? OR APP- PLICATION?? OR PROCEDURE?? OR STEPS?? OR MODULE?? OR MACRO??)
S4	5252	(HOLD??? OR RESERV??? OR STORING?? OR STOR??) (3N) S3
S5	148549	(TRIGGER??? OR INITIAT??? OR ACTIVAT??? OR START???) (7N) (E- XECUT??? OR OPERATING?? OR PROCESS???? OR LAUNCH???)
S6	4299	(EXECUT?? OR EXECUTING??) (3N) S3
S7	8727	S2 (5N) S3
S8	482	S2 (5N) S4
S9	10179	S2 (5N) S5
S10	585	S2 (5N) S6
S11	467	S7 (5N) S8
S12	18	S11 (5N) S9
S13	4	S12 AND S10
S14	14	S12 NOT S13
S15	766	S1 (5N) (S4 AND S6)
S16	81	S15 (3N) S5
S17	50	S16 AND IC=G06F?
S18	34	S17 NOT PY>2001
S19	33	S18 NOT S14



13/3,K/1 (Item 1 from file: 348)  
DIALOG(R) File 348:EUROPEAN PATENTS  
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00917866

Navigation system

Navigationssystem

Système de navigation

PATENT ASSIGNEE:

Xanavi Informatics Corporation, (1813720), 4991, Hironodai 2-chome,  
Zama-shi, Kanagawa-ken, (JP), (Proprietor designated states: all)

INVENTOR:

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Tokyo, (JP)

LEGAL REPRESENTATIVE:

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Bardehle . Pagenberg . Dost . Altenburg . Geissler . Isenbruck  
Galileiplatz 1, 81679 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 837434 A1 980422 (Basic)  
EP 837434 B1 030108

APPLICATION (CC, No, Date): EP 97117749 971014;

PRIORITY (CC, No, Date): JP 96274617 961017

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G08G-001/0968; G01C-021/20

ABSTRACT WORD COUNT: 4571

NOTE:

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	200302	647
CLAIMS B	(German)	200302	578
CLAIMS B	(French)	200302	777
SPEC B	(English)	200302	3461
Total word count - document A			0
Total word count - document B			5463
Total word count - documents A + B			5463

...SPECIFICATION 3A, user setting compensation processing of a landmark is performed.

After the daytime/nighttime compensation **processing**, the system **executes the user setting compensation processing** as follows.  
During this processing, the system checks the user setting to see if it  
...

...later. If any of the user setting values has been changed, the system adds a **predetermined** value ( **for** example, 3000) to the corresponding point column of the landmark **data** table in FIG. 4 to increase the point to a new point (step 321). If...

...display 8 during the user specification processing (step 308). On this landmark selection screen, the **types of landmarks stored** in the landmark **data table** shown in FIG. 4 are arranged in the sequence according to the points at that time. This...

13/3,K/2 (Item 2 from file: 348)  
DIALOG(R) File 348:EUROPEAN PATENTS

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00306058

Digital data processing system.

Digitales Datenverarbeitungssystem.

Systeme de traitement de donnees numeriques.

PATENT ASSIGNEE:

DATA GENERAL CORPORATION, (410940), Route 9, Westboro Massachusetts 01581  
, (US), (applicant designated states: AT;BE;CH;DE;FR;GB;IT;LI;LU;NL;SE)

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Robson, Aidan John et al (69471), Reddie & Grose 16 Theobalds Road,  
London WC1X 8PL, (GB)

PATENT (CC, No, Kind, Date): EP 290111 A2 881109 (Basic)  
EP 290111 A3 890503  
EP 290111 B1 931222

APPLICATION (CC, No, Date): EP 88200917 820521;

PRIORITY (CC, No, Date): US 266404 810522

DESIGNATED STATES: AT; BE; CH; DE; FR; GB; IT; LI; LU; NL; SE

RELATED PARENT NUMBER(S) - PN (AN):

EP 67556 (EP 823025960)

INTERNATIONAL PATENT CLASS: G06F-009/30;

ABSTRACT WORD COUNT: 123

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	1044
CLAIMS B	(German)	EPBBF1	890
CLAIMS B	(French)	EPBBF1	1185
SPEC B	(English)	EPBBF1	154314
Total word count - document A			0
Total word count - document B			157433

Total word count - documents A + B 157433

- ...SPECIFICATION interconnected data processing networks, which internal structure is flexible, protected from users, effectively invisible to **users**, and provides a flexible and simplified interface to **users**. The **data processing system** provides an addressing mechanism allowing permanent and unique identification of all information generated for use...
- ...to the user, into addresses. The present system additionally provides multilevel control and a stack **mechanisms** protecting the **system's** internal mechanism from interference by **users**. Yet another feature is a **data processing** system having a flexible internal structure capable of performing multiple, concurrent operations and comprised of...
- ...a data processing system. The system is yet further advantageous in that it provides a **data processing** system which is equally efficient with any **user level language** by providing a mechanism for referring to operands in user programs by uniform format names and...operation will first be described in an Introductory Overview. Next, these and other features will be described in **further** detail in a more detailed Introduction to the detailed descriptions of the computer system. Following...ERF, in addition to registers associated with, for example, ALUs.
- A primary feature of CS 101 is that IOS 116, MEM 112, FU 120 and EU 122 each contain separate and...
- ...a serial number referred to as a Unique Identifier (UID). A UID is a 128 **bit** value comprised of a serial number dependent upon, for example, the particular CS 101 system and **user**, and a time **code** indicating time of creation of that Object. UIDs are permanently assigned to Objects, no two...
- ...in a system to be assigned an AON, but can be active in that system **only** if it has been assigned an AON.
- A particular bit within a particular Object may...case of physical addresses provided by JP 114, these addresses are referred to as Physical **Descriptors** (PDs). As described below, JP 114 contains circuitry to translate logical descriptors into physical descriptors...
- ...101, for example, may have SOP Dialects for COBOL, FORTRAN, and SPL. A particular distinction of CS 101 is **that** all SOPs are of a uniform, fixed length, for example 16 bits. CS 101 may...
- ...within individual CS 101 systems. Names, however, are unique only within the context of a **user's program**. That is, a particular Name may appear in two different user's programs and, within...
- ...to operands. SOPs and Names allow user's programs to be expressed in very compact **code**. Fewer SOPs than machine language instructions are required to express a user's program. Also...
- ...or calls, to other Procedure Objects containing, for example, procedures available in common to many **users**. Second, a Static **Data** Area may contain static **data**, that is **data** having an existence for at least a single execution of the program. And third, a...
- ...AONs and addressable through UID and AON addresses and descriptors.
- Locations of information within a **user's Procedure** Objects, Static

instruction stream by merely **repeating** the operand's Name; and, (4) allows partially **completed** Name to address translations to be stored in a cache to speed up operand references...data in the format specified by the requester in the descriptor. MEM 10112 also accepts **data** in a format **specified** in a descriptor and reformats **that data** into a format **most** efficiently used by MEM 10112 to store the data.

As previously described, all operands are...cache, is indicated, RM 20722 is interrupted and forced to place other MEM 10112 operations in idle until cache load is completed. A block by-pass read operation results in by...

13/3,K/3 (Item 1 from file: 349)  
DIALOG(R)File 349:PCT FULLTEXT  
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00761422

**BUSINESS ALLIANCE IDENTIFICATION**  
**SYSTEME, PROCEDE ET ARTICLE DE PRODUCTION POUR L'IDENTIFICATION D'ALLIANCES**  
**COMMERCIALES DANS UN CADRE D'ARCHITECTURE RESEAU**

Patent Applicant/Assignee:

ACCENTURE LLP, 100 South Wacker Drive, Chicago, IL 60606, US, US  
(Residence), US (Nationality)

Inventor(s):

GUHEEN Michael F, 2218 Mar East Street, Tiburon, CA 94920, US,  
MITCHELL James D, 3004 Alma, Manhattan Beach, CA 90266, US,  
BARRESE James J, 757 Pine Avenue, San Jose, CA 95125, US,

Legal Representative:

BRUESS Steven C (agent), Merchant, Gould, Smith, Edell, Welter & Schmidt,  
P.A., P.O. Box 2903, Minneapolis, MN 55402-0903, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200073928 A2-A3 20001207 (WO 0073928)  
Application: WO 2000US14375 20000524 (PCT/WO US0014375)  
Priority Application: US 99320816 19990527

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY CA CH CN CR CU CZ DE DK DM DZ EE ES  
FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU  
LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR  
TT TZ UA UG UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 149371

Fulltext Availability:

Detailed Description

Detailed Description

... also engaging and informative. This is especially true of Internet and kiosk-based systems, where **users** have a notoriously short concentration span.

This requirement for more attractive **user** interfaces has **triggered**

the evolution of media-rich applications, the development of which requires new tools and processes...particular function(s) impacted by the request. Following such interaction, accepted requests will be planned, **executed** , and tracked.

#### Implementation Considerations

Will **users** be given access to the Request Management system?

Users will benefit by gaining up to...

13/3,K/4 (Item 2 from file: 349)  
DIALOG(R)File 349:PCT FULLTEXT  
(c) 2004 WIPO/Univentio. All rts. reserv.

00456834 \*\*Image available\*\*

#### A SYSTEM, METHOD AND ARTICLE OF MANUFACTURE FOR SWITCHED TELEPHONY COMMUNICATION

#### SYSTEME PROCEDE ET ARTICLE CONCU POUR LES COMMUNICATIONS TELEPHONIQUES PAR RESEAU COMMUTE

Patent Applicant/Assignee:

MCI WORLDCOM INC,

Inventor(s):

ZEY David A,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9847298 A2 19981022

Application: WO 98US7927 19980415 (PCT/WO US9807927)

Priority Application: US 97835789 19970415; US 97834320 19970415

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES FI GB GE GH HU  
IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG MK MN MW MX NO NZ PL  
PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG UZ VN YU ZW GH GM KE LS MW  
SD SZ UG ZW AM AZ BY KG KZ MD RU TJ TM AT BE CH CY DE DK ES FI FR GB GR  
IE IT LU MC NL PT SE BF BJ CF CG CI CM GA GN ML MR NE SN TD TG

Publication Language: English

Fulltext Word Count: 156638

Fulltext Availability:

Detailed Description

#### Detailed Description

... specification is entered in the flow specification table in the packet classifier 2050 via the **process** to **process** interface 2065. The **process** to **process** interface 2065 need not be identical to the process to process interface 2085, but the...a result based on a percentage distribution).

#### d) Service Data

There are three sources for **data** while a service **executes** .

Static **Data** defined in the service template, which include default values for a given service invocation.

Interactive...

14/3,K/1 (Item 1 from file: 348)  
DIALOG(R) File 348:EUROPEAN PATENTS  
(c) 2004 European Patent Office. All rts. reserv.

01431914

**Adaptive graphical user-interface for a network peripheral**  
**Anpassbare grafische Anwenderschnittstelle fur ein Netzwerkperiphergerat**  
**Interface utilisateur graphique adaptive pour un peripherique de reseau**  
PATENT ASSIGNEE:

CANON KABUSHIKI KAISHA, (542361), 30-2, 3-chome, Shimomaruko, Ohta-ku,  
Tokyo, (JP), (Applicant designated States: all)

INVENTOR:

Sanchez, George A., 657 Los Pinos Place, Fremont, California 94539, (US)  
Manhajan, Rakesh, 14 Pepperwood, Laguna Hills, California 92656, (US)  
Man, Chiu Ming, 1125 Hilda Street, Anaheim, California 92806, (US)  
Hildreth, Mark S., 205 Rustic Place, San Ramon, California 94583, (US)

LEGAL REPRESENTATIVE:

Beresford, Keith Denis Lewis et al (28276), Beresford & Co., 2-5 Warwick  
Court, High Holborn, London WC1R 5DJ, (GB)

PATENT (CC, No, Kind, Date): EP 1209558 A2 020529 (Basic)  
EP 1209558 A3 020619

APPLICATION (CC, No, Date): EP 2002075261 960529;

PRIORITY (CC, No, Date): US 454013 950530

DESIGNATED STATES: DE; FR; GB; IT

RELATED PARENT NUMBER(S) - PN (AN):

EP 745929 (EP 96303848)

INTERNATIONAL PATENT CLASS: G06F-003/12

ABSTRACT WORD COUNT: 261

NOTE:

Figure number on first page: 7

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200222	777
SPEC A	(English)	200222	8841
Total word count - document A			9618
Total word count - document B			0
Total word count - documents A + B			9618

...SPECIFICATION memory 30 and executes those stored programs out of main  
memory 30. In accordance with **user** instructions, **stored application**  
**programs** are **activated** which permit **processing** and manipulating of  
**data** . For example, a desktop publishing program such as Wordperfect(R)  
for Windows may be activated...

14/3,K/2 (Item 2 from file: 348)  
DIALOG(R) File 348:EUROPEAN PATENTS  
(c) 2004 European Patent Office. All rts. reserv.

01389415

**Computer-aided color selection and colorizing system**  
**Rechnerunterstutzte Farbauswahl und farbgebendes System**  
**Selection de couleurs assistee par ordinateur et systeme de coloration**  
PATENT ASSIGNEE:

CANON KABUSHIKI KAISHA, (542361), 30-2, 3-chome, Shimomaruko, Ohta-ku,  
Tokyo, (JP), (Applicant designated States: all)

INVENTOR:

Lavendel, Laurence A, Canon Informat Systems Inc, 110 Innovation Drive,  
Irvine, California 92612, (US)

LEGAL REPRESENTATIVE:

Beresford, Keith Denis Lewis et al (28274), 2-5 Warwick Court High  
Holborn, London WC1R 5DH, (GB)

PATENT (CC, No, Kind, Date): EP 1178437 A2 020206 (Basic)

APPLICATION (CC, No, Date): EP 2001201942 950331;

PRIORITY (CC, No, Date): US 231911 940425

DESIGNATED STATES: DE; FR; GB; IT

RELATED PARENT NUMBER(S) - PN (AN):

EP 680018 (EP 95302175)

INTERNATIONAL PATENT CLASS: G06T-011/00

ABSTRACT WORD COUNT: 140

NOTE:

Figure number on first page: 9

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200206	948
SPEC A	(English)	200206	5880
Total word count - document A			6828
Total word count - document B			0
Total word count - documents A + B			6828

...SPECIFICATION memory 30 and executes those stored programs out of main  
memory 30.

In accordance with **operator** instructions, **stored application  
programs** are **activated** which permit **processing** and manipulating of  
**data** . For example, a desktop publishing program such as WordPerfect(R)  
for Windows may be activated...

**14/3,K/3 (Item 3 from file: 348)**

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2004 European Patent Office. All rts. reserv.

00914606

**World wide web news retrieval system**

**System zum Wiederauffinden von Nachrichten auf dem World-Wide-Web**

**Systeme de recouvrement de nouvelles sur le World-Wide-Web**

PATENT ASSIGNEE:

Canon Information Systems, Inc., (1553870), 3188 Pullman Street, Costa  
Mesa, CA 92626, (US), (applicant designated states:

AT;BE;CH;DE;DK;ES;FI;FR;GB;GR;IE;IT;LI;LU;MC;NL;PT;SE)

INVENTOR:

Nehab, Smadar, c/o 3188 Pullman Street, Costa Mesa, California 92626,  
(US)

Wickramaratne, Manjula G., c/o 3188 Pullman Street, Costa Mesa,  
California 92626, (US)

Klark, Paul L., c/o 3188 Pullman Street, Costa Mesa, California 92626,  
(US)

LEGAL REPRESENTATIVE:

Beresford, Keith Denis Lewis et al (28273), BERESFORD & Co. 2-5 Warwick  
Court High Holborn, London WC1R 5DJ, (GB)

PATENT (CC, No, Kind, Date): EP 834822 A2 980408 (Basic)

EP 834822 A3 990428

APPLICATION (CC, No, Date): EP 97307009 970910;

PRIORITY (CC, No, Date): US 726853 961004

DESIGNATED STATES: DE; FR; GB; IT  
INTERNATIONAL PATENT CLASS: G06F-017/30;  
ABSTRACT WORD COUNT: 220

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9815	2347
SPEC A	(English)	9815	10671
Total word count - document A			13018
Total word count - document B			0
Total word count - documents A + B			13018

...SPECIFICATION memory 14 and executes the software applications out of main memory 14. In accordance with **user** instructions, **stored application programs** are **activated** which permit **processing** and manipulation of **data**. Typically, the software applications stored on disk drive 5, such as personal-news-profile editor...

14/3,K/4 (Item 4 from file: 348)  
DIALOG(R)File 348:EUROPEAN PATENTS  
(c) 2004 European Patent Office. All rts. reserv.

00810991

**Machining method using numerical control apparatus**  
**Bearbeitungsverfahren mit Verwendung von einem numerischen Steuerungsgerat**  
**Methode d'usage utilisant un appareil a commande numerique**  
PATENT ASSIGNEE:

MITSUBISHI DENKI KABUSHIKI KAISHA, (208580), 2-3, Marunouchi 2-chome  
Chiyoda-ku, Tokyo 100, (JP), (applicant designated states:  
CH;DE;FR;GB;LI)

INVENTOR:

Hirai, Hayao, c/o Mitsubishi Denki K.K., Nagoya Seisakusho, 1-14,  
Yadaminami 5-chome, Higashi-ku, Nagoya-shi, Aichi 461, (JP)  
Fujimoto, Akihiko, Mitsubishi E.M.S. Co., Ltd., 1071,  
Higashi-Ozone-cho-Kami 5-chome, Kita-ku, Nagoya-shi, Aichi 462-91, (JP)

LEGAL REPRESENTATIVE:

Ritter und Edler von Fischern, Bernhard, Dipl.-Ing. et al (9672),  
Hoffmann Eitle, Patent- und Rechtsanwälte, Arabellastrasse 4, 81925  
Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 753805 A1 970115 (Basic)  
EP 753805 B1 990506

APPLICATION (CC, No, Date): EP 96111105 960710;

PRIORITY (CC, No, Date): JP 95197308 950710

DESIGNATED STATES: CH; DE; FR; GB; LI

INTERNATIONAL PATENT CLASS: G05B-019/418;

ABSTRACT WORD COUNT: 173

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9918	2061
CLAIMS B	(German)	9918	1991
CLAIMS B	(French)	9918	2306
SPEC B	(English)	9918	189869
Total word count - document A			0
Total word count - document B			196227
Total word count - documents A + B			196227



14/3,K/5 (Item 5 from file: 348)  
DIALOG(R)File 348:EUROPEAN PATENTS  
(c) 2004 European Patent Office. All rts. reserv.

00802311

**Adaptive graphical user-interface for a network peripheral**  
**Anpassungsfahige grafische Schnittstelle fur ein Netzwerk-Peripheriegerat**  
**Interface utilisateur graphique adaptatif pour un peripherique de reseaux**  
PATENT ASSIGNEE:

CANON KABUSHIKI KAISHA, (542361), 30-2, 3-chome, Shimomaruko, Ohta-ku,  
Tokyo, (JP), (Proprietor designated states: all)

INVENTOR:

Sanchez, George A., 657 Los Pinos Place, Fremont, California 94539, (US)  
Mahajan, Rakesh, 14 Pepperwood, Laguna Hills, California 92656, (US)  
Man, Chiu Ming, 1125 Hilda Street, Anaheim, California 92806, (US)  
Hildreth, Mark S., 205 Rustic Place, San Ramon, California 94583, (US)

LEGAL REPRESENTATIVE:

Beresford, Keith Denis Lewis et al (28273), BERESFORD & Co. 2-5 Warwick  
Court, High Holborn, London WC1R 5DH, (GB)

PATENT (CC, No, Kind, Date): EP 745929 A1 961204 (Basic)  
EP 745929 B1 020911

APPLICATION (CC, No, Date): EP 96303848 960529;

PRIORITY (CC, No, Date): US 454013 950530

DESIGNATED STATES: DE; FR; GB; IT

RELATED DIVISIONAL NUMBER(S) - PN (AN):

EP 1209558 (EP 2002075261)

INTERNATIONAL PATENT CLASS: G06F-003/12

ABSTRACT WORD COUNT: 305

NOTE:

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB96	1677
CLAIMS B	(English)	200237	1436
CLAIMS B	(German)	200237	1358
CLAIMS B	(French)	200237	1716
SPEC A	(English)	EPAB96	7159
SPEC B	(English)	200237	6770
Total word count - document A			8838
Total word count - document B			11280
Total word count - documents A + B			20118

...SPECIFICATION memory 30 and executes those stored programs out of main memory 30. In accordance with **user** instructions, **stored application programs** are **activated** which permit **processing** and manipulating of **data** . For example, a desktop publishing program such as Wordperfect(R) for Windows may be activated...

...SPECIFICATION memory 30 and executes those stored programs out of main memory 30. In accordance with **user** instructions, **stored application programs** are **activated** which permit **processing** and manipulating of **data** . For example, a desktop publishing program such as Wordperfect(R) for Windows may be activated...

**14/3,K/6 (Item 6 from file: 348)**  
DIALOG(R)File 348:EUROPEAN PATENTS  
(c) 2004 European Patent Office. All rts. reserv.

00724661

**Objective-based color selection system**  
**Objektivbasiertes Farbwahlssystem**  
**Systeme de selection de couleurs a base d'objective**

PATENT ASSIGNEE:

Canon Information Systems, Inc., (1553870), 3188 Pullman Street, Costa  
Mesa, CA 92626, (US), (Proprietor designated states: all)

INVENTOR:

Haikin, John S., 804 Bedford Street, Fremont, California 94539, (US)

LEGAL REPRESENTATIVE:

Beresford, Keith Denis Lewis et al (28273), BERESFORD & Co. High Holborn  
2-5 Warwick Court, London WC1R 5DJ, (GB)

PATENT (CC, No, Kind, Date): EP 684584 A2 951129 (Basic)  
EP 684584 A3 960612  
EP 684584 B1 001102

APPLICATION (CC, No, Date): EP 95302176 950331;

PRIORITY (CC, No, Date): US 231912 940425

DESIGNATED STATES: DE; FR; GB; IT

INTERNATIONAL PATENT CLASS: G06T-011/00

ABSTRACT WORD COUNT: 183

NOTE:

Figure number on first page: 2

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	200044	1030
CLAIMS B	(German)	200044	860
CLAIMS B	(French)	200044	1224
SPEC B	(English)	200044	8674
Total word count - document A			0
Total word count - document B			11788
Total word count - documents A + B			11788

**14/3,K/7 (Item 7 from file: 348)**  
DIALOG(R)File 348:EUROPEAN PATENTS  
(c) 2004 European Patent Office. All rts. reserv.

00718595

**Computer-aided color selection and colorizing system.**  
**Rechnerunterstutzte Farbauswahl und farbgebendes System.**  
**Selection de couleurs assistee par ordinateur et systeme de coloration.**

PATENT ASSIGNEE:

CANON INFORMATION SYSTEMS, INC., (1553870), 3188 Pullman Street, Costa  
Mesa, CA 92626, (US), (applicant designated states: DE;FR;GB;IT)

INVENTOR:

Lavendel, Laurence A., 2500 Huntington Drive, Aptos, California 95003,  
(US)

LEGAL REPRESENTATIVE:

Beresford, Keith Denis Lewis et al (28273), BERESFORD & Co. 2-5 Warwick  
Court High Holborn, London WC1R 5DJ, (GB)

PATENT (CC, No, Kind, Date): EP 680018 A2 951102 (Basic)  
EP 680018 A3 960612

APPLICATION (CC, No, Date): EP 95302175 950331;

PRIORITY (CC, No, Date): US 231911 940425  
DESIGNATED STATES: DE; FR; GB; IT  
RELATED DIVISIONAL NUMBER(S) - PN (AN):  
(EP 2001201942)  
INTERNATIONAL PATENT CLASS: G06T-011/00;  
ABSTRACT WORD COUNT: 170

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB95	1503
SPEC A	(English)	EPAB95	5946
Total word count - document A			7449
Total word count - document B			0
Total word count - documents A + B			7449

...SPECIFICATION memory 30 and executes those stored programs out of main memory 30.

In accordance with **operator** instructions, **stored application programs** are **activated** which permit **processing** and manipulating of **data**. For example, a desktop publishing program such as WordPerfect(R) for Windows may be activated...

14/3,K/8 (Item 8 from file: 348)  
DIALOG(R)File 348:EUROPEAN PATENTS  
(c) 2004 European Patent Office. All rts. reserv.

00709713

**Method and Apparatus for forming an abstract image**  
**Verfahren und Gerat zur Wiedergabe von einem abstrakten Bild**  
**Methode et appareil pour la formation d'une image abstraite**  
PATENT ASSIGNEE:

CANON KABUSHIKI KAISHA, (542361), 30-2, 3-chome, Shimomaruko, Ohta-ku,  
Tokyo, (JP), (Proprietor designated states: all)

INVENTOR:

Luther, Willis J., c/o Canon Information Sys. Inc., 3188 Pullman Street,  
Costa Mesa CA 92626, (US)

LEGAL REPRESENTATIVE:

Beresford, Keith Denis Lewis (28273), BERESFORD & Co. High Holborn 2-5  
Warwick Court, London WC1R 5DJ, (GB)

PATENT (CC, No, Kind, Date): EP 673153 A2 950920 (Basic)  
EP 673153 A3 960828  
EP 673153 B1 011010

APPLICATION (CC, No, Date): EP 95301150 950222;

PRIORITY (CC, No, Date): US 214621 940318

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G06T-003/40

ABSTRACT WORD COUNT: 170

NOTE:

Figure number on first page: 4

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB95	1942
CLAIMS B	(English)	200141	2190
CLAIMS B	(German)	200141	2058
CLAIMS B	(French)	200141	2482

SPEC A	(English)	EPAB95	3602
SPEC B	(English)	200141	3829
Total word count - document A			5545
Total word count - document B			10559
Total word count - documents A + B			16104

...SPECIFICATION 24 is provided for outputting information processed by computing equipment 10.

In accordance with computer **operator** instructions, **stored application programs** are selectively **activated to process** and manipulate **data**. For example, and as described in more detail below, an optical disk access program may...

...SPECIFICATION 24 is provided for outputting information processed by computing equipment 10.

In accordance with computer **operator** instructions, **stored application programs** are selectively **activated to process** and manipulate **data**. For example, and as described in more detail below, an optical disk access program may...

14/3,K/9 (Item 9 from file: 348)  
 DIALOG(R) File 348:EUROPEAN PATENTS  
 (c) 2004 European Patent Office. All rts. reserv.

00699463

**Two-line telephone controller.**

**Telefonsteuereinrichtung mit zwei Leitungen.**

**Dispositif de controle pour poste telephonique a deux lignes.**

PATENT ASSIGNEE:

CANON KABUSHIKI KAISHA, (542361), 30-2, 3-chome, Shimomaruko, Ohta-ku, Tokyo, (JP), (applicant designated states: DE;FR;GB;IT)

CANON INFORMATION SYSTEMS, INC., (1553870), 3188 Pullman Street, Costa Mesa, CA 92626, (US), (applicant designated states: DE;FR;GB;IT)

INVENTOR:

Beck, Gregory F., 23851 Formello, Laguna Hills, CA 92653, (US)

Fontana, James, A., 26402 Ambia, Mission Viejo, CA 92692, (US)

Ray, Richard, D., 28143 Via Fierro, Laguna Niguel, CA 92656, (US)

Palmer, Douglas, L., 1 Calle Cabrillo, Foothill Ranch, CA 92610, (US)

Egan, Alistair, 22743 Islamare Lane, Lake Forest, CA 92630, (US)

Walker, Lisay F., 26381-A Via Scramento, Capistrano Beach, CA 92624, (US)

LEGAL REPRESENTATIVE:

Beresford, Keith Denis Lewis (28273), BERESFORD & Co. 2-5 Warwick Court High Holborn, London WC1R 5DJ, (GB)

PATENT (CC, No, Kind, Date): EP 666680 A2 950809 (Basic)  
 EP 666680 A3 980422

APPLICATION (CC, No, Date): EP 94308969 941202;

PRIORITY (CC, No, Date): US 160824 931203

DESIGNATED STATES: DE; FR; GB; IT

INTERNATIONAL PATENT CLASS: H04M-011/06;

ABSTRACT WORD COUNT: 186

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB95	1911
SPEC A	(English)	EPAB95	4628
Total word count - document A			6539
Total word count - document B			0

Total word count - documents A + B 6539

...SPECIFICATION to a multiple telephone lines, here two telephone lines 20 and 21.

In accordance with **operator** instructions, **stored application programs** are **activated** and permit **processing** and manipulation of **data** . For example, any of a variety of application programs such as a multimedia message management...

14/3,K/10 (Item 10 from file: 348)  
DIALOG(R)File 348:EUROPEAN PATENTS  
(c) 2004 European Patent Office. All rts. reserv.

00684887

**System for speaking hypertext documents such as computerized help files**  
**System zur Sprachlichen Wiedergabe von Hypertextdokumenten, wie**  
**Hilfsdateien**

**Systeme de prononciation de documents hypertext, comme des fichiers d'aide**  
PATENT ASSIGNEE:

Canon Information Systems, Inc., (1553870), 3188 Pullman Street, Costa  
Mesa, CA 92626, (US), (Proprietor designated states: all)

INVENTOR:

Tullis, Thomas S., 29182 Bobolink Drive, Laguna Niguel, California 92677,  
(US)

Kodimer, Marianne L., 921 So. Bucknell Circle, Anaheim, California 92807,  
(US)

LEGAL REPRESENTATIVE:

Beresford, Keith Denis Lewis et al (28273), BERESFORD & Co. High Holborn  
2-5 Warwick Court, London WC1R 5DJ, (GB)

PATENT (CC, No, Kind, Date): EP 654734 A1 950524 (Basic)  
EP 654734 B1 000419

APPLICATION (CC, No, Date): EP 94308623 941123;

PRIORITY (CC, No, Date): US 156690 931124

DESIGNATED STATES: DE; FR; GB; IT

INTERNATIONAL PATENT CLASS: G06F-009/44

ABSTRACT WORD COUNT: 211

NOTE:

Figure number on first page: 4

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	200016	1571
CLAIMS B	(German)	200016	1549
CLAIMS B	(French)	200016	1857
SPEC B	(English)	200016	3883
Total word count - document A			0
Total word count - document B			8860
Total word count - documents A + B			8860

...SPECIFICATION a motion video or interface for inputting frames of analog video information.

In accordance with **operator** instructions, **stored application programs** are **activated** and permit **processing** and manipulation of **data** . For example, a word **processing** program may be **activated** to permit an operator to create, view, manipulate and print documents which may contain objects...

14/3,K/11 (Item 11 from file: 348)  
DIALOG(R) File 348:EUROPEAN PATENTS  
(c) 2004 European Patent Office. All rts. reserv.

00391159

**A control apparatus for automobiles**  
**Kontrollvorrichtung fur Autos**  
**Dispositif de controle pour automobiles**

PATENT ASSIGNEE:

Hitachi, Ltd., (204141), 6, Kanda Surugadai 4-chome, Chiyoda-ku, Tokyo  
101, (JP), (applicant designated states: DE;FR;GB)

INVENTOR:

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PATENT (CC, No, Kind, Date): EP 392411 A2 901017 (Basic)  
EP 392411 A3 920108  
EP 392411 B1 940302

APPLICATION (CC, No, Date): EP 90106737 900409;

PRIORITY (CC, No, Date): JP 8992863 890414

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: B60R-025/00

ABSTRACT WORD COUNT: 138

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9901	720
CLAIMS B	(German)	9901	676
CLAIMS B	(French)	9901	814
SPEC B	(English)	9901	7026
Total word count - document A			0
Total word count - document B			9236
Total word count - documents A + B			9236

...CLAIMS 13) is adapted to record on the portable recording medium  
specifications of control units selected **by a user** .

2. A **control apparatus** according to claim 1,

wherein the information recorded on said recording medium includes  
a code...

14/3,K/12 (Item 1 from file: 349)  
DIALOG(R) File 349:PCT FULLTEXT  
(c) 2004 WIPO/Univentio. All rts. reserv.

00939222 \*\*Image available\*\*

**TECHNIQUES FOR PROVIDING ELECTRONIC DELIVERY ORDERS AND ORDER TRACKING**  
**TECHNIQUES D'ETABLISSEMENT D'ORDRES DE LIVRAISON ELECTRONIQUES ET DE SUIVI**  
**D'ORDRES**

Patent Applicant/Assignee:

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Legal Representative:

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Patent and Priority Information (Country, Number, Date):

Patent: WO 200273345 A2-A3 20020919 (WO 0273345)  
Application: WO 2002US6889 20020308 (PCT/WO US02006889)  
Priority Application: US 2001273973 20010308

Designated States:

(Protection type is "patent" unless otherwise stated - for applications  
prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ  
EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR  
LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SD SE SG SI  
SK SL TJ TM TN TR TT TZ UA UG UZ VN YU ZA ZM ZW  
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR  
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG  
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW  
(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 8484

Fulltext Availability:

Detailed Description

Detailed Description

... memory 18 and executes the software applications out of main memory  
18. In accordance with **user** instructions, **stored** software  
**applications** are **activated** which permit **processing** and manipulation  
of **data**. Typically, software applications stored on disk drive 20, such  
as eDO software application 21, client...

14/3,K/13 (Item 2 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00909145 \*\*Image available\*\*

**PLANAR LASER ILLUMINATION AND IMAGING (PLIIM) SYSTEMS WITH INTEGRATED  
DESPECKLING MECHANISMS PROVIDED THEREIN**  
**SYSTEMES PLIIM D'ILLUMINATION ET D'IMAGERIE AU LASER PLANAIRE A MECANISME  
DE DECHATOIEMENT INTEGRE**

Patent Applicant/Assignee:

METROLOGIC INSTRUMENTS INC, 90 Coles Road, Blackwood, NJ 08012, US, US  
(Residence), US (Nationality), (For all designated states except: US)

Patent Applicant/Inventor:

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Patent and Priority Information (Country, Number, Date):  
 Patent: WO 200243195 A2-A3 20020530 (WO 0243195)  
 Application: WO 2001US44011 20011121 (PCT/WO US0144011)  
 Priority Application: US 2000721885 20001124; US 2001780027 20010209; US  
 2001781665 20010212; US 2001883130 20010615; US 2001954477 20010917; US  
 2001999687 20011031

Parent Application/Grant:  
 Related by Continuation to: US 2001954477 20010917 (CIP)



Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ  
EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR  
LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL  
TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR

(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 298301

Fulltext Availability:

Claims

Claim

... of Fig. 25, illustrating that object identity data element inputs (e.g. from a bar code symbol reader, RFID reader, or the like) and object attribute data element inputs (e.g. object dimensions, weight, x ...by a set of VLD driver circuits), the linear-type image formation and detection (IFD) module, the image frame grabber, the image data buffer, and the image processing computer, via the...PLIIM-based hand-supportable imager;

Fig. 53A2 is a block schematic diagram of an automatically- activated version of the PLIIM-based hand-supportable area imager of Fig. 52A, shown configured with...

14/3,K/14 (Item 3 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00779719 \*\*Image available\*\*

**LIVESTOCK INVENTORY AND MATERIALS SYSTEM WITH INTERACTIVE GRAPHICAL USER INTERFACE**

**SYSTEME D'INVENTAIRE DE BETAIL ET DE MATERIEL COMPORTANT UNE INTERFACE D'UTILISATEUR GRAPHIQUE INTERACTIVE**

Patent Applicant/Assignee:

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except: US)

Patent Applicant/Inventor:

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US (Nationality), (Designated only for: US)

Legal Representative:

JEANG Wei Wei (et al) (agent), Munsch Hardt Kopf & Harr, P.C., 4000  
Fountain Place, 1445 Ross Avenue, Dallas, TX 75202-2790, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200113302 A2-A3 20010222 (WO 0113302)

Application: WO 2000US22311 20000814 (PCT/WO US0022311)

Priority Application: US 99373927 19990812

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ DE DK DM DZ EE  
ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT  
LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM  
TR TT TZ UA UG US UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 14299

Fulltext Availability:

Detailed Description

Detailed Description

... of livestock inventory and materials system 10 is the control and processing modules 18 that **store** and **process data** based on **user initiated** events or user-entered data. Depending on the specific data explorer environment that is currently...

19/3,K/1 (Item 1 from file: 348)  
DIALOG(R) File 348:EUROPEAN PATENTS  
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01310699

Method and system for tracking screen activities in online transactions  
Verfahren und System zum Verfolgen von Bildschirmaktivitäten bei  
Online-Transaktionen  
Methode et système pour suivre les activités sur un écran lors des  
transactions en ligne

PATENT ASSIGNEE:

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INVENTOR:

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LEGAL REPRESENTATIVE:

Perkins, Sarah (69642), Stevens, Hewlett & Perkins Halton House 20/23  
Holborn, London EC1N 2JD, (GB)

PATENT (CC, No, Kind, Date): EP 1120732 A2 010801 (Basic)  
EP 1120732 A3 011107

APPLICATION (CC, No, Date): EP 2001300722 010126;

PRIORITY (CC, No, Date): US 492559 000127

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;  
LU; MC; NL; PT; SE; TR

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: G06F-017/60 ; G06F-001/00 ; G07F-007/10

ABSTRACT WORD COUNT: 91

NOTE:

Figure number on first page: 2A

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200131	566
SPEC A	(English)	200131	6307
Total word count - document A			6873
Total word count - document B			0
Total word count - documents A + B			6873

INTERNATIONAL PATENT CLASS: G06F-017/60 ...

... G06F-001/00

...SPECIFICATION a second memory space for code of a screen capturing  
module, the screen capturing module **activated** only and **executed** by  
the **processor** when the **user** decides to release to the server one or  
more inputs entered in a web page...

19/3,K/2 (Item 2 from file: 348)  
DIALOG(R) File 348:EUROPEAN PATENTS  
(c) 2004 European Patent Office. All rts. reserv.

01030324

MOBILE ELECTRONIC COMMERCE SYSTEM  
MOBILES ELEKTRONISCHES HANDELSYSTEM

**SYSTEME DE COMMERCE ELECTRONIQUE MOBILE**

**PATENT ASSIGNEE:**

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD, (216884), 1006, Oaza-Kadoma,  
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**INVENTOR:**

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**LEGAL REPRESENTATIVE:**

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**PATENT (CC, No, Kind, Date):** EP 950968 A1 991020 (Basic)  
WO 9909502 990225

**APPLICATION (CC, No, Date):** EP 98937807 980813; WO 98JP3608 980813

**PRIORITY (CC, No, Date):** JP 97230564 970813

**DESIGNATED STATES:** DE; FR; GB

**RELATED DIVISIONAL NUMBER(S) - PN (AN):**  
(EP 2004015278)

**INTERNATIONAL PATENT CLASS:** G06F-017/60

**ABSTRACT WORD COUNT:** 150

**NOTE:**

Figure number on first page: 1

**LANGUAGE (Publication,Procedural,Application):** English; English; Japanese

**FULLTEXT AVAILABILITY:**

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9942	17239
SPEC A	(English)	9942	160346
Total word count - document A			177585
Total word count - document B			0
Total word count - documents A + B			177585

**INTERNATIONAL PATENT CLASS:** G06F-017/60

...SPECIFICATION specific diagram showing the data structure of an  
examination object ticket for the ticket setup **processing** according to  
the embodiment of the present invention;

Fig. 111A is a specific diagram showing...

19/3,K/3 (Item 3 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2004 European Patent Office. All rts. reserv.

00957813

**PERSONAL ELECTRONIC SETTLEMENT SYSTEM, ITS TERMINAL, AND MANAGEMENT  
APPARATUS**

**PERSONLICHES ELEKTRONISCHES REGELUNGSSYSTEM, TERMINAL UND MANAGEMENTAPPARAT  
SYSTEME DE REGLEMENT ELECTRONIQUE PERSONNEL, TERMINAL DE CE DERNIER ET  
APPAREIL PERMETTANT DE GERER CE SYSTEME**

**PATENT ASSIGNEE:**

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD., (216883), 1006, Oaza Kadoma,  
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**INVENTOR:**

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**LEGAL REPRESENTATIVE:**

Casalonga, Axel et al (14511), BUREAU D.A. CASALONGA - JOSSE  
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**PATENT (CC, No, Kind, Date):** EP 910028 A1 990421 (Basic)

WO 9821677 980522  
APPLICATION (CC, No, Date): EP 97912468 971114; WO 97JP4161 971114  
PRIORITY (CC, No, Date): JP 96316897 961114; JP 97117681 970422  
DESIGNATED STATES: DE; FR; GB  
INTERNATIONAL PATENT CLASS: G06F-017/60  
ABSTRACT WORD COUNT: 119

LANGUAGE (Publication,Procedural,Application): English; English; Japanese  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9916	12261
SPEC A	(English)	9916	116678
Total word count - document A			128939
Total word count - document B			0
Total word count - documents A + B			128939

INTERNATIONAL PATENT CLASS: G06F-017/60

...SPECIFICATION 2B, reference numeral 211 denotes an execution switch whereby is transmitted an instruction for the initiation of a process requiring the confirmation of a user, such as the payment of a price, the confirmation of the contents of a transaction...

19/3,K/4 (Item 4 from file: 348)  
DIALOG(R) File 348:EUROPEAN PATENTS  
(c) 2004 European Patent Office. All rts. reserv.

00804170

Customer information control system and method with transaction serialization control functions in a loosely coupled parallel processing environment

Kundeninformationssteuerungssystem und Verfahren mit Steuerfunktionen zur Transaktionsserialisierung in einer lose gekoppelten parallelen Verarbeitungsumgebung

Systeme et methode de commande d'information client avec fonctions de commande de serialisation de transaction dans un environnement de traitement parallele a c

PATENT ASSIGNEE:

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PATENT (CC, No, Kind, Date): EP 747814 A1 961211 (Basic)

APPLICATION (CC, No, Date): EP 96303661 960522;

PRIORITY (CC, No, Date): US 479701 950607

DESIGNATED STATES: DE; FR; GB; IT; SE

INTERNATIONAL PATENT CLASS: G06F-009/46

ABSTRACT WORD COUNT: 233

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB96	1050
SPEC A	(English)	EPAB96	7383
Total word count - document A			8433
Total word count - document B			0
Total word count - documents A + B			8433

INTERNATIONAL PATENT CLASS: G06F-009/46

...SPECIFICATION Furthermore, the transaction that cancels a pending transaction (via a CICS Cancel command) can be **executed** in a different **user application process** and on a different server than the **user application process** and server that **executed** the transaction that initiated that pending transaction (via a CICS Start command).

Since all scheduled transaction starts initiated by transactions **executing** in all **user application processes** on all servers are handled by a single transaction start process 172, the system of...

19/3,K/5 (Item 5 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

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00804169

Customer information control system and method in a loosely coupled parallel processing environment

Benutzerinformationssteuersystem und -verfahren in einer lose gekoppelten parallelen Datenverarbeitungsumgebung

Systeme et procede de controle d'information des clients dans un environnement de traitement parallele avec couplage lache

PATENT ASSIGNEE:

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Street, London EC4A 1PQ, (GB)

PATENT (CC, No, Kind, Date): EP 747832 A2 961211 (Basic)  
EP 747832 A3 980401

APPLICATION (CC, No, Date): EP 96303660 960522;

PRIORITY (CC, No, Date): US 478058 950607

DESIGNATED STATES: DE; FR; GB; IT; SE

INTERNATIONAL PATENT CLASS: G06F-009/46

ABSTRACT WORD COUNT: 249

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
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CLAIMS A	(English)	EPAB96	1055
SPEC A	(English)	EPAB96	7261
Total word count - document A			8316
Total word count - document B			0
Total word count - documents A + B			8316

INTERNATIONAL PATENT CLASS: G06F-009/46

...SPECIFICATION Furthermore, the transaction that cancels a pending transaction (via a CICS Cancel command) can be **executed** in a different **user application process** and on a different server than the **user application process** and server that **executed** the transaction that initiated that pending transaction (via a CICS Start command).

Since all scheduled transaction starts initiated by transactions **executing** in all **user application processes** on all servers are handled by a single transaction start process 172, the system of...

...CLAIMS storing transaction start data representing transactions whose execution has been requested by other transactions being **executed** by said **user application processes**; said transaction **start** data indicating a **start** condition for each said transaction whose **execution** has been requested;

a transaction **start process**, **executing** on one of said server computers and coupled to said transaction start table; said transaction...

...transmitting data between said end user terminals and said transaction routing procedures;

said transaction routing **procedures** selecting which **user application process** is to **execute** each said transaction and **initiating** said **execution** of said each transaction by said selected user application process.

8. The method of claim...

...table transaction start data representing transactions whose execution has been requested by other transactions being **executed** by said **user application processes**; said transaction **start** data indicating a **start** condition for each said transaction whose **execution** has been requested;

executing a transaction router process on one of said server computers for...

19/3,K/6 (Item 6 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

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00804162

Customer information control system and method with API start and cancel transaction functions in a loosely coupled parallel processing environment

CICS-System und Verfahren mit API Funktionen zum Starten und Annullieren von Transaktionen in einer lose gekoppelten Parallelverarbeitungsumgebung

Systeme et methode CICS avec des fonctions API pour demarrer et annuler des transactions dans un environnement de traitement parallele a couplage lache

PATENT ASSIGNEE:

TANDEM COMPUTERS INCORPORATED, (524035), 10435 N. Tantau Avenue,  
Cupertino, CA 95014-3548, (US), (applicant designated states:  
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INVENTOR:

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PATENT (CC, No, Kind, Date): EP 747812 A2 961211 (Basic)  
EP 747812 A3 980325

APPLICATION (CC, No, Date): EP 96303648 960522;

PRIORITY (CC, No, Date): US 479702 950607

DESIGNATED STATES: DE; FR; GB; IT; SE

INTERNATIONAL PATENT CLASS: G06F-009/46

ABSTRACT WORD COUNT: 229

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB96	844
SPEC A	(English)	EPAB96	7319
Total word count - document A			8163
Total word count - document B			0
Total word count - documents A + B			8163

INTERNATIONAL PATENT CLASS: G06F-009/46

...ABSTRACT stores transaction start data representing transactions whose execution has been requested by other transactions being **executed** by the **user application processes**. The transaction **start** data indicates a **start** condition for each transaction whose **execution** has been requested. For most requested transactions the start condition is a time value indicating an earliest time at which the requested transaction's **execution** should be **started**. When any of the **user application processes** **executes** a **Start** transaction function, a transaction **start** record is generated and stored in the transaction start table. The transaction start record is...

...SPECIFICATION stores transaction start data representing transactions whose execution has been requested by other transactions being **executed** by the **user application processes**. The transaction **start** data indicates a **start** condition for each transaction whose **execution** has been requested. For most requested transactions the start condition is a time value indicating an earliest time at which the requested transaction's **execution** should be **started**.

When any of the **user application processes** **execute** a **Start** transaction function, a transaction **start** record is generated and stored in the transaction start table. The transaction start record is ...Furthermore, the transaction that cancels a pending transaction (via a CICS Cancel command) can be **executed** in a different **user application process** and on a different server than the **user application process** and server that **executed** the transaction that initiated that pending transaction (via a CICS Start command).



Since all scheduled transaction starts initiated by transactions **executing** in all **user application processes** on all servers are handled by a single transaction start process 172, the system of...

- ...CLAIMS storing transaction start data representing transactions whose execution has been requested by other transactions being **executed** by said **user application processes**; said transaction **start** data indicating a **start** condition for each said transaction whose **execution** has been requested;  
said user application processes including means, responsive to execution of a Start...
- ...specified transaction start record in said transaction start table, regardless of which one of said **user application processes** **executed** the corresponding **Start** transaction instruction to generate and store said any specified transaction start record.
- 4. The distributed...
- ...storing transaction start data representing transactions whose execution has been requested by other transactions being **executed** by said **user application processes**; said transaction **start** data indicating a **start** condition for each said transaction whose **execution** has been requested;  
whenever any executing transaction executes a Start transaction instruction, generating and storing...
- ...by one of said user application processes.
- 6. The method of claim 5, wherein said **initiating execution** step includes selecting a **user application processes** to **execute** said requested transaction based on computational load distribution criteria and without regard to information sharing...
- ...specified transaction start record in said transaction start table, regardless of which one of said **user application processes** **executed** the corresponding **Start** transaction instruction to generate and store said any specified transaction start record.
- 8. The method...

19/3,K/7 (Item 7 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

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00576020

**A method and a device for booting a computer at a programmed time**

**Verfahren und Gerat zum Umladen eines Rechners zu einer programmierten Zeit**

**Methode et dispositif pour demarrer un ordinateur a un temps programme**

PATENT ASSIGNEE:

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PATENT (CC, No, Kind, Date): EP 572332 A1 931201 (Basic)

EP 572332 B1 980701

APPLICATION (CC, No, Date): EP 93420204 930519;

PRIORITY (CC, No, Date): FR 926659 920525

DESIGNATED STATES: DE; FR; GB; IT; NL

INTERNATIONAL PATENT CLASS: G06F-009/445

ABSTRACT WORD COUNT: 110

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9827	870
CLAIMS B	(German)	9827	861
CLAIMS B	(French)	9827	972
SPEC B	(English)	9827	5697
Total word count - document A			0
Total word count - document B			8400
Total word count - documents A + B			8400

INTERNATIONAL PATENT CLASS: G06F-009/445

...SPECIFICATION method comprises the following steps carried out by a startup file, different from the default **startup** file, provided by the network: **executing user** -elected **programs** ; initializing the identification register; and resetting the task pending bit.

According to an embodiment of...

...carried out by a startup file loaded through a local media, different from the default **startup** file: **executing user** -selected **programs** ; restoring the default **startup** file; initializing the identification register; and resetting the task pending bit.

Viewed from a second...

...CLAIMS 5 comprising the following steps carried out by a startup file, different from the default **startup** file, provided by the network:

- **executing user** -selected **programs** ;
- initialising the identification register (ID); and
- resetting the task pending bit (TPx))).

8. The booting...

...carried out by a startup file loaded from a local media, different from the default **startup** file:

- **executing user** -selected **programs** ;
- restoring the default **startup** file;
- initializing the identification register (ID); and
- resetting the task pending bit (TPx))).

9. A...

19/3,K/8 (Item 8 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2004 European Patent Office. All rts. reserv.

00401210

Interprocessor communication

Übertragung zwischen Prozessoren

Communication entre processeurs

PATENT ASSIGNEE:

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PATENT (CC, No, Kind, Date): EP 398697 A2 901122 (Basic)

EP 398697 A3 940202

EP 398697 B1 980902

APPLICATION (CC, No, Date): EP 90305312 900516;

PRIORITY (CC, No, Date): US 353115 890517

DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FR; GB; GR; IT; LI; LU; NL; SE

INTERNATIONAL PATENT CLASS: G06F-015/16

ABSTRACT WORD COUNT: 219

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
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CLAIMS B	(English)	9836	397
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CLAIMS B	(German)	9836	352
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CLAIMS B	(French)	9836	454
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SPEC B	(English)	9836	71173
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Total word count - document A	0
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Total word count - document B	72376
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Total word count - documents A + B	72376
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INTERNATIONAL PATENT CLASS: G06F-015/16

...SPECIFICATION Operating System is the master over all system hardware and I/O devices. The peer **processor** pairs **execute** their respective **Operating** Systems in a single system environment without significant rewriting of either operating system.

Introduction - Prior...

19/3,K/9 (Item 9 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

(c) 2004 European Patent Office. All rts. reserv.

00401205

Method and apparatus for adding a data processing function to a data processing system

Verfahren und Anordnung zum Hinzufügen von einer Datenverarbeitungsfunktion zu einem Datenverarbeitungssystem

Methode et appareil pour l'addition d'un fonction de traitement des donnees a un systeme de traitement de donnees

PATENT ASSIGNEE:

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PATENT (CC, No, Kind, Date): EP 398693 A2 901122 (Basic)

EP 398693 A3 940202

EP 398693 B1 980909

APPLICATION (CC, No, Date): EP 90305306 900516;

PRIORITY (CC, No, Date): US 353111 890517

DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FR; GB; GR; IT; LI; LU; NL; SE

INTERNATIONAL PATENT CLASS: G06F-015/16 ; G06F-013/12

ABSTRACT WORD COUNT: 219

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9837	1109
CLAIMS B	(German)	9837	979
CLAIMS B	(French)	9837	1299
SPEC B	(English)	9837	71715

Total word count - document A 0

Total word count - document B 75102

Total word count - documents A + B 75102

INTERNATIONAL PATENT CLASS: G06F-015/16 ...

... G06F-013/12

...SPECIFICATION Operating System is the master over all system hardware  
and I/O devices. The peer **processor** pairs **execute** their respective  
**Operating** Systems in a single system environment without significant  
rewriting of either operating system.

Introduction - Prior...

19/3,K/10 (Item 10 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2004 European Patent Office. All rts. reserv.

00311807

Portable computer.

Tragbarer Rechner.

Ordinateur portable.

PATENT ASSIGNEE:

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Takano, Yasuhiko, 2-3-6-402, Ohmiya-cho, Nara-shi Nara-ken, (JP)

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PATENT (CC, No, Kind, Date): EP 289288 A2 881102 (Basic)

EP 289288 A3 891220  
EP 289288 B1 940202  
APPLICATION (CC, No, Date): EP 88303805 880427;  
PRIORITY (CC, No, Date): JP 87104975 870427  
DESIGNATED STATES: DE; GB  
INTERNATIONAL PATENT CLASS: G06F-015/02  
ABSTRACT WORD COUNT: 145

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	225
CLAIMS B	(German)	EPBBF1	209
CLAIMS B	(French)	EPBBF1	255
SPEC B	(English)	EPBBF1	1665
Total word count - document A			0
Total word count - document B			2354
Total word count - documents A + B			2354

INTERNATIONAL PATENT CLASS: G06F-015/02

...SPECIFICATION to the respective program running-start lines. (Such programs are hereinafter referred to as definition **programs**.) To **execute** a certain **program**, the **user** depresses the **definition** key **corresponding** to the **label** of the **program**.

Fig. 3 shows the hardware construction of the portable computer of the present invention. 6...

19/3,K/11 (Item 11 from file: 348)  
DIALOG(R) File 348:EUROPEAN PATENTS  
(c) 2004 European Patent Office. All rts. reserv.

00306062

**Digital data processing system.**

**Digitales Datenverarbeitungssystem.**

**Systeme du traitement de donnees numeriques.**

PATENT ASSIGNEE:

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PATENT (CC, No, Kind, Date): EP 300516 A2 890125 (Basic)  
EP 300516 A3 890426

EP 300516 B1 931124  
 APPLICATION (CC, No, Date): EP 88200921 820521;  
 PRIORITY (CC, No, Date): US 266413 810522; US 266539 810522; US 266521  
 810522; US 266415 810522; US 266409 810522; US 266424 810522; US 266421  
 810522; US 266404 810522; US 266414 810522; US 266532 810522; US 266403  
 810522; US 266408 810522; US 266401 810522; US 266524 810522  
 DESIGNATED STATES: AT; BE; CH; DE; FR; GB; IT; LI; LU; NL; SE  
 RELATED PARENT NUMBER(S) - PN (AN):  
 EP 67556 (EP 823025960)  
 INTERNATIONAL PATENT CLASS: G06F-009/46 ; G06F-012/14  
 ABSTRACT WORD COUNT: 122

LANGUAGE (Publication,Procedural,Application): English; English; English  
 FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	1018
CLAIMS B	(German)	EPBBF1	868
CLAIMS B	(French)	EPBBF1	1115
SPEC B	(English)	EPBBF1	154256
Total word count - document A			0
Total word count - document B			157257
Total word count - documents A + B			157257

INTERNATIONAL PATENT CLASS: G06F-009/46 ...

... G06F-012/14

...SPECIFICATION Fig. 5 is a diagram illustrating a stack mechanism;  
 Fig. 6 is a diagram illustrating **procedures** , **procedure** objects,  
**processes** , and virtual **processors** ;  
 Fig. 7 is a diagram illustrating **operating** levels and mechanisms  
 of the present computer;  
 Fig. 8 is a diagram illustrating a physical...or AON. O fields  
 indicate offset, or location, of a particular bit relative to the **start**  
 of a particular Object.  
 Segments of information (sequences of information bits) within  
 particular Objects may...  
 ...or logical descriptor is formed by appending a 32 bit L field to an AON  
**address** . L fields **identify** length of a segment of information bits  
 within an Object, **starting** from the information bit identified by the  
 UID or AON address. In addition to length...code. Fewer SOPs than machine  
 language instructions are required to express a user's program. **Also** ,  
 use of SOPs allows easier and simpler construction of compilers, and  
 facilitates adaption of CS...is not saved. Execution of a current Machine  
 Language Instruction 404 is later resumed at **start** of the  
 microinstruction sequence for **executing** that **Machine** Language  
 Instruction 404.  
 Referring to Fig. 4A, top level control in CS 101 is by User Language  
 Instructions 412...  
 ...aside to contain Secure Stack (SS) 504, operating on the microcode  
 level, as described above **and** of which **MIS** 424 is a part.  
 As described further below, both **FU** 120 and **EU** 122 contain register  
 file arrays, referred to respectively as GRF and ERF of a **process** 's **SS**  
 504 pertaining to **EU** 122 **operations** .  
 Considering first **MASS** 502, as stated above **MASS** 502 operate generally  
 upon the **SINS** level...no greater than 5 bits, that is, specify lengths of

no greater than 32 data bits .

A logical **descriptor** describing a data item to be transferred by means of a string transfer will be stored in...

...and L field in LENGRF 1936. At each successive transfer of a 32 bit word in the string transfer, O field of that original logical descriptor will be incremented by the...below, IOS 10116 and MEM 10112 operate independently under general Control of JP 10114 in **executing** multiple **user 's programs** . In this regard, MEM 10112 is an intelligent, prioritizing memory having separate and independent ports...are commonly available to all users; for example, a compiler comprises of a set of **procedures** for compiling a **user language program** into an S-language program. **Operating system programs** are groups of procedures ...user program and data plus one or more utility programs (e.g., a compiler) and **operating system programs** necessary to **execute** the **user program** .

An object is a uniquely identifiable portion of "data space" accessible to CS 10110. An object...

...for purposes of CS 10110's protection mechanisms, as a combination of the current principle ( **user** ), the current **process** being **executed** , and the domain the **process** is currently being **executed** in. In addition to principle, **process** , and domain, which are identified by UIDs, subject may include a Tag, which is a...mechanism is constructed to prevent a user from (1) gaining access to or disrupting another **user 's process** , including **data** , and (2) interfering with or otherwise subverting the operation of CS 10110. Access rights to...10232. ASN Register 10916 stores the ASN of a currently active subject while PC 10234 **stores** certain access right information for objects being used by the current process. PC 10234 entries...10116. Other such independently operating processors, for example, special arithmetic processors such as an array **processor** , or multiple FU 10120 's, may be added to the present CS 10110.

In this regard, MEM 10112 is...is provided to FU 10120, at each change in S-Language dialect, and is used **by** FU 10120 in parsing Names **from** the instruction stream. In **an** alternate embodiment of CS 10110, all Names are the same size in all S-Language...10112 will be described next below.

#### f. MEM 10112 Operations

MEM 10112 may perform two **general** types of **operation** . The first type **are** data transfer operations and the second type are memory maintenance operations. Data

19/3,K/12 (Item 12 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00306058

Digital data processing system.

Digitales Datenverarbeitungssystem.

Systeme de traitement de donnees numeriques.

PATENT ASSIGNEE:

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PATENT (CC, No, Kind, Date): EP 290111 A2 881109 (Basic)  
EP 290111 A3 890503  
EP 290111 B1 931222

APPLICATION (CC, No, Date): EP 88200917 820521;

PRIORITY (CC, No, Date): US 266404 810522

DESIGNATED STATES: AT; BE; CH; DE; FR; GB; IT; LI; LU; NL; SE

RELATED PARENT NUMBER(S) - PN (AN):

EP 67556 (EP 823025960)

INTERNATIONAL PATENT CLASS: G06F-009/30

ABSTRACT WORD COUNT: 123

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	1044
CLAIMS B	(German)	EPBBF1	890
CLAIMS B	(French)	EPBBF1	1185
SPEC B	(English)	EPBBF1	154314
Total word count - document A			0
Total word count - document B			157433
Total word count - documents A + B			157433

INTERNATIONAL PATENT CLASS: G06F-009/30

...SPECIFICATION are formed by appending a 32 bit Offset (0) field to that  
Object's UID or AON . O fields indicate offset , or location, of a  
particular bit relative to the start of a particular Object.

Segments of information (sequences of information bits) within  
particular Objects may...within individual CS 101 systems. Names,  
however, are unique only within the context of a user 's program . That  
is, a particular Name may appear in two different user's programs and,  
within...



...two sets of instructions. A first set is comprised of SOPs, that is instructions selecting **algorithms** to be **executed**. The second set of instructions are comprised of Names, which may be regarded as **entry** points into tables of instructions for making references regarding operands.

Referring to Fig.3, a...

...in very compact code. Fewer SOPs than machine language instructions are required to express a **user's program**. Also, use of SOPs allows easier and simpler construction of compilers, and facilitates adaption of CS 101 systems to new **user** languages. In addition, use of Names to refer to operands means that SOPs are independent...

...are not required.

### 3. Architectural Base Pointer Addressing

As will be described further below, a **user's program** residing in CS 101 will include one or more Objects. First, a Procedure Object contains ...

...AONs and addressable through UID and AON addresses and descriptors.

Locations of information within a **user's Procedure** Objects, Static Data Area, and Macro-stack are expressed as offsets from one of three... is not saved. Execution of a current Machine Language Instruction 404 is later resumed at **start** of the microinstruction sequence for **executing** that Machine Language Instruction 404.

Referring to Fig. 4A, top level control in CS 101...

...424 while the second stack is referred to as Monitor Stack (MOS) 426. CS 101 **SIN** Microcode 428 and MIS 424 are primarily concerned with **execution** of SOPs of user's programs. Monitor Microcode 430 and MOS 426 are concerned with...

...Secure Stack (SS) 504, operating on the microcode level, as described above and of which MIS 424 is a part.

As described further below, both FU 120 and EU 122 contain register file arrays, referred to as **Execute Unit State (EUS)** 512 and a **second** block referred to as SOP Stack 514. EUS 512 is similar to mCS 510 in...

...in MEM 112, may contain, for all practical purposes, an unlimited number of frames so that MIS 424 and SS 504 **appear** to a **user** to be effectively an infinitely deep stack.

MOS 426 resides entirely in FU 120 and...the PBP used by the calling procedure. In these cases, the compiler uses symbolic Names to define the **locations**. Binder 703 is a utility which translates symbolic Names into UID-offset addresses. It does so in two ways: by combining separate Procedure Objects 608 into a single large...domains, and correspondingly, any number of Stack Objects. Stack Object 906 comprises Process 610's **Secure Stack** 504 and is required to **store state** which may be manipulated only by KOS 706, 710.

Each invocation made by a **Process 610** results in the addition of frames to Secure Stack 504 and to Macro-Stack 502...incurred by transferring data from MC 1816 to MSB 1810 before new data may be **written** into MC 1816.

MEM 112's FIU 1820 allows manipulation of data formats in writes to and reads...operation if EU 122 receives an instruction and operands requiring EU 122 to perform a **divide by zero**.

Referring to Fig. 20, a partial block diagram of EU 122 is shown. EU...

10116) and JP 10114. MEM 10112 thus operates both as a buffer for receiving and **storing** various individual **user 's programs** (e.g., data, instructions, and results of program execution) and as a main memory for...will be discussed further below all operands in a program are referred to by "NAME". **An operating system program translates** operand NAME into the physical locations of the operands in MEM 10112. The NAME translation...

...may, for example have four domains: User domain, Data Base Management System (DBMS) domain, Extended **Operating System (EOS)** domain, and Kernel Operating System (KOS) domain. User domain is the domain of execution of all **user provided procedures**, such as **user** or utility procedures. DBMS domain is the domain of **execution** for **operating system procedures** for storing, **retrieving**, and **handling** data. EOS domain is the domain of execution of operating system procedures defining and forming the user level interface with CS 10110, such as procedures for controlling an executing files, **processes**, and I/O operations. KOS domain is the domain of **execution** of the low level, secure **operating system** which manages and controls CS 10110 's physical resources. Other embodiments of CS 10110 may have fewer or more domains than...contained in an object may be located by a logical address comprising the object's **UID**, an offset **from the start** of the object of **the** first bit of the segment, and the length (number of bits) of the information segment...is resident in an object active in CS 10110. While an object need not have a page in MEM 10112 to be active, the object must be active to have a...10122, MEM 10112 and IOS 10116. Other such independently operating processors, for example, special arithmetic **processors** such as an array **processor**, or multiple FU 10120's, may be added to the present CS 10110.

In this...specified by the requestor in the descriptor. MEM 10112 also accepts data in a format **specified** in a descriptor and reformats **that** data into a format **most** efficiently used by MEM 10112 to store the data.

As previously described, all operands are...

19/3,K/13 (Item 13 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

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00306057

Digital data processing system.

Digitales Datenverarbeitungssystem.

Systeme de traitement de donnees numeriques.

PATENT ASSIGNEE:

DATA GENERAL CORPORATION, (410940), Route 9, Westboro Massachusetts 01581  
, (US), (applicant designated states: AT;BE;CH;DE;FR;GB;IT;LI;LU;NL;SE)

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LEGAL REPRESENTATIVE:

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PATENT (CC, No, Kind, Date): EP 290110 A2 881109 (Basic)  
EP 290110 A3 890412

APPLICATION (CC, No, Date): EP 88200916 820521;

PRIORITY (CC, No, Date): US 266401 810522

DESIGNATED STATES: AT; BE; CH; DE; FR; GB; IT; LI; LU; NL; SE

RELATED PARENT NUMBER(S) - PN (AN):

EP 67556

INTERNATIONAL PATENT CLASS: G06F-012/06 ; G06F-009/30

ABSTRACT WORD COUNT: 119

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	1390
SPEC A	(English)	EPABF1	155314
Total word count - document A			156704
Total word count - document B			0
Total word count - documents A + B			156704

INTERNATIONAL PATENT CLASS: G06F-012/06 ...

... G06F-009/30

...SPECIFICATION is not saved. Execution of a current Machine Language  
Instruction 404 is later resumed at **start** of the microinstruction  
sequence for **executing** that Machine Language Instruction 404.

Referring to Fig. 4A, top level control in CS 101...these processors  
include FU 10120, EU 10122, MEM 10112 and IOS 10116. Other such  
independently **operating processors**, for example, special arithmetic  
**processors** such as an array **processor**, or multiple FU 10120's, may be  
added to the present CS 10110.

In this...

19/3,K/14 (Item 14 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2004 European Patent Office. All rts. reserv.

00271933

**Electronic data processing apparatus for improved algebraic expression  
manipulation.**

**Elektronische Datenverarbeitungsanlage zur Manipulation von algebraischen  
Formeln.**

**Installation de traitement electronique de donnees pour la manipulation de**

**formule algebrigue.**

**PATENT ASSIGNEE:**

Hewlett-Packard Company, (206031), Mail Stop 20 B-O, 3000 Hanover Street,  
Palo Alto, California 94304, (US), (applicant designated states:  
DE;FR;GB)

**INVENTOR:**

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**LEGAL REPRESENTATIVE:**

Colgan, Stephen James et al (29461), CARPMAELS & RANSFORD 43 Bloomsbury  
Square, London WC1A 2RA, (GB)

**PATENT (CC, No, Kind, Date):** EP 266145 A2 880504 (Basic)  
EP 266145 A3 900418  
EP 266145 B1 940413

**APPLICATION (CC, No, Date):** EP 87309419 871026;

**PRIORITY (CC, No, Date):** US 923921 861027

**DESIGNATED STATES:** DE; FR; GB

**INTERNATIONAL PATENT CLASS:** G06F-009/44 ; G06F-015/02

**ABSTRACT WORD COUNT:** 146

**LANGUAGE (Publication,Procedural,Application):** English; English; English  
**FULLTEXT AVAILABILITY:**

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	655
CLAIMS B	(German)	EPBBF1	635
CLAIMS B	(French)	EPBBF1	773
SPEC B	(English)	EPBBF1	7570
Total word count - document A			0
Total word count - document B			9633
Total word count - documents A + B			9633

**INTERNATIONAL PATENT CLASS:** G06F-009/44 ...

... G06F-015/02

...SPECIFICATION In processing block (1214) of Figure 12c, the data  
processing commands of the menu item of the current menu, whose  
indicator is displayed, and which correspond to the user 's input are  
executed (Key 104 of Figure 1). The program then proceeds via connector  
(C1) to processing block...

**19/3,K/15 (Item 15 from file: 348)**

**DIALOG(R)File 348:EUROPEAN PATENTS**

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00214897

**Multiprocess computer and method for operating same.**

**Multiprozessrechner und abhangiges Betriebssystem.**

**Ordinateur multitache et systeme d'exploitation.**

**PATENT ASSIGNEE:**

N.V. Philips' Gloeilampenfabrieken, (200769), Groenewoudseweg 1, NL-5621  
BA Eindhoven, (NL), (applicant designated states: DE;FR;GB)

**INVENTOR:**

Den Boef, Johannes Hendrik, INT. OCTROOIBUREAU B.V. Prof. Holstlaan 6,  
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**LEGAL REPRESENTATIVE:**

Strijland, Wilfred et al , INTERNATIONAAL OCTROOIBUREAU B.V. Prof.  
Holstlaan 6, NL-5656 AA Eindhoven, (NL)

**PATENT (CC, No, Kind, Date):** EP 222443 A2 870520 (Basic)

EP 222443 A3 880921  
APPLICATION (CC, No, Date): EP 86201901 861031;  
PRIORITY (CC, No, Date): US 794863 851104  
DESIGNATED STATES: DE; FR; GB  
INTERNATIONAL PATENT CLASS: **G06F-009/46**  
ABSTRACT WORD COUNT: 134  
LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:  
Available Text Language Update Word Count  
CLAIMS A (English) EPABF1 1311  
SPEC A (English) EPABF1 3213  
Total word count - document A 4524  
Total word count - document B 0  
Total word count - documents A + B 4524

INTERNATIONAL PATENT CLASS: **G06F-009/46**

...CLAIMS prevailing process context (88, 98, 108).

3. A multiprocess computer comprising sequencing means for sequentially activating respective operating system routines and user processes from a main store, said user processes including at least one process pertaining to a preferentially treatable peripheral device, said main store comprising a driver program for...

19/3,K/16 (Item 16 from file: 348)  
DIALOG(R)File 348:EUROPEAN PATENTS  
(c) 2004 European Patent Office. All rts. reserv.

00170004

**Maintenance subsystem for computer network.**

**Wartungssystem für Rechnernetzwerk.**

**Sous-système de maintenance pour réseau de calculateurs.**

PATENT ASSIGNEE:

UNISYS CORPORATION, (842794), Township Line and Union Meeting Roads P.O.  
Box 500, Blue Bell, PA 19424-0001, (US), (applicant designated states:  
DE;FR;GB;NL)

INVENTOR:

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LEGAL REPRESENTATIVE:

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PATENT (CC, No, Kind, Date): EP 179425 A2 860430 (Basic)  
EP 179425 A3 880831  
EP 179425 B1 930526

APPLICATION (CC, No, Date): EP 85113330 851021;

PRIORITY (CC, No, Date): US 664670 841025

DESIGNATED STATES: DE; FR; GB; NL

INTERNATIONAL PATENT CLASS: **G06F-011/22**

ABSTRACT WORD COUNT: 141

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	1424
CLAIMS B	(German)	EPBBF1	1377
CLAIMS B	(French)	EPBBF1	1687
SPEC B	(English)	EPBBF1	14799
Total word count - document A			0
Total word count - document B			19287
Total word count - documents A + B			19287

INTERNATIONAL PATENT CLASS: G06F-011/22

...SPECIFICATION level interface (MLI) to another external I/O subsystem supporting non-self-testing data link **processors** through maintenance card means, **initiated** by said **user** interface **processor** (UIP) for **executing** test operations on said non-self-testing data link processors (DLP), said processor interface card...

...CLAIMS level interface (MLI) to another external I/O subsystem supporting non-self-testing data link **processors** through maintenance card means (100 m), **initiated** by said **user** interface **processor** (UIP) for **executing** test operations on said non-self-testing data link processors (DLP), said processor interface card...

19/3,K/17 (Item 1 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

(c) 2004 WIPO/Univentio. All rts. reserv.

00843106 \*\*Image available\*\*

**SYSTEM AND METHOD FOR ESTABLISHING ELECTRONIC BUSINESS SYSTEMS FOR SUPPORTING COMMUNICATIONS SERVICES COMMERCE**

**SYSTEME ET PROCEDE PERMETTANT D'ETABLIR DES SYSTEMES DE COMMERCE ELECTRONIQUE POUR LE SUPPORT DU COMMERCE PAR DES SERVICES DE COMMUNICATION**

Patent Applicant/Assignee:

CYGENT INC, 201 3rd Street, 2nd Floor, San Francisco, CA 94103, US, US  
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Patent Applicant/Inventor:

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(Residence), IN (Nationality), (Designated only for: US)

Legal Representative:

GLENN Michael (et al) (agent), Glenn Patent Group, Suite L., 3475 Edison  
Way, Menlo Park, CA 94025, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200175549 A2-A3 20011011 (WO 0175549)  
Application: WO 2001US10473 20010330 (PCT/WO US0110473)  
Priority Application: US 2000193315 20000330

Designated States:

(Protection type is "patent" unless otherwise stated - for applications  
prior to 2004)

AE AL AM AT AU AZ BA BB BG BR BY CA CH CN CR CU CZ DE DK DM EE ES FI GB  
GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA  
MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT TZ UA  
UG US UZ VN YU ZA ZW  
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR  
(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG  
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW  
(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 84642

Main International Patent Class: G06F-009/46

International Patent Class: G06F-017/30 ...

... G06F-017/60

Fulltext Availability:

Detailed Description

Detailed Description

... CSP may also want to

4 8

execute a transition policy that performs some business process, such  
as  
persisting data, before re-executing the transition policy that  
initiated entry into  
the nested flow. To do this, create an entry in the TRANS-MAP...

19/3,K/18 (Item 2 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

(c) 2004 WIPO/Univentio. All rts. reserv.

00823191 \*\*Image available\*\*

APPARATUS AND METHOD FOR ACCESSING MULTIMEDIA CONTENT

DISPOSITIF ET PROCEDURE SERVANT A ACCEDER A UN CONTENU MULTIMEDIA

Patent Applicant/Assignee:

OMRAD OPTO-ELECTRONICS LTD, Yehoshua Hatzoref Street 15, 84003 Beer-Sheva  
, IL, IL (Residence), IL (Nationality), (For all designated states  
except: US)

Patent Applicant/Inventor:

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COOPER Sagi, Malkhey Israel Street 65, 48032 Rosh Ha'ayn, IL, IL  
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Patent and Priority Information (Country, Number, Date):  
Patent: WO 200155922 A2-A3 20010802 (WO 0155922)  
Application: WO 2001IB269 20010126 (PCT/WO IB0100269)  
Priority Application: US 2000178637 20000128; US 2000213223 20000619  
Designated States:  
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)  
AE AL AM AT AU AZ BA BB BG BR BY CA CH CN CR CU CZ DE DK DM EE ES FI GB  
GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA  
MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT TZ UA  
UG US UZ VN YU ZA ZW  
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR  
(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG  
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW  
(EA) AM AZ BY KG KZ MD RU TJ TM  
Publication Language: English  
Filing Language: English  
Fulltext Word Count: 13618

Main International Patent Class: G06F-017/60  
International Patent Class: G06F-017/30 ...  
Fulltext Availability:  
Detailed Description

Detailed Description  
... 102 identifies content associated with a physical bookmark, according to one embodiment. Beginning at a **start** step 1002, a client **application** **executing** on a **user** computer 104 receives an identification code from a play unit I IO coupled to the...

19/3,K/19 (Item 3 from file: 349)  
DIALOG(R)File 349:PCT FULLTEXT  
(c) 2004 WIPO/Univentio. All rts. reserv.

00806392  
**TECHNOLOGY SHARING DURING ASSET MANAGEMENT AND ASSET TRACKING IN A NETWORK-BASED SUPPLY CHAIN ENVIRONMENT AND METHOD THEREOF**  
**PARTAGE TECHNOLOGIQUE LORS DE LA GESTION ET DU SUIVI DU PARC INFORMATIQUE DANS UN ENVIRONNEMENT DU TYPE CHAINE D'APPROVISIONNEMENT RESEAUTE, ET PROCEDE ASSOCIE**

Patent Applicant/Assignee:  
ACCENTURE LLP, 1661 Page Mill Road, Palo Alto, CA 94304, US, US  
(Residence), US (Nationality)  
Inventor(s):  
MIKURAK Michael G, 108 Englewood Blvd., Hamilton, NJ 08610, US,  
Legal Representative:  
HICKMAN Paul L (agent), Oppenheimer Wolff & Donnelly, LLP, 38th Floor,  
2029 Century Park East, Los Angeles, CA 90067-3024, US,  
Patent and Priority Information (Country, Number, Date):  
Patent: WO 200139086 A2 20010531 (WO 0139086)  
Application: WO 2000US32310 20001122 (PCT/WO US0032310)  
Priority Application: US 99444653 19991122; US 99447623 19991122  
Designated States:  
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)



AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ DE DK DM DZ EE ES  
FI GB GE GH GM HR HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA  
MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT TZ  
UA UG UZ VN YU ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 156214

Main International Patent Class: G06F-017/60

Fulltext Availability:

Detailed Description

Detailed Description

... programmer called libraries provided by the operating system to  
perform certain tasks, but basically the **program** **executed** down the  
page from **start** to finish, and the programiner was

22

one way.

The development of graphical user interfaces...

19/3,K/20 (Item 4 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00806383

COLLABORATIVE CAPACITY PLANNING AND REVERSE INVENTORY MANAGEMENT DURING  
DEMAND AND SUPPLY PLANNING IN A NETWORK-BASED SUPPLY CHAIN ENVIRONMENT  
AND METHOD THEREOF

PLANIFICATION EN COLLABORATION DES CAPACITES ET GESTION ANTICIPEE DES  
STOCKS LORS DE LA PLANIFICATION DE L'OFFRE ET DE LA DEMANDE DANS UN  
ENVIRONNEMENT DE CHAINE D'APPROVISIONNEMENT FONDEE SUR LE RESEAU ET  
PROCEDE ASSOCIE

Patent Applicant/Assignee:

ACCENTURE LLP, 1661 Page Mill Road, Palo Alto, CA 94304, US, US

(Residence), US (Nationality)

Inventor(s):

MIKURAK Michael G, 108 Englewood Blvd., Hamilton, NJ 08610, US,

Legal Representative:

HICKMAN Paul L (agent), Oppenheimer Wolff & Donnelly, LLP, 1400 Page Mill  
Road, Palo Alto, CA 94304, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200139029 A2 20010531 (WO 0139029)

Application: WO 2000US32309 20001122 (PCT/WO US0032309)

Priority Application: US 99444655 19991122; US 99444886 19991122

Designated States:

(Protection type is "patent" unless otherwise stated.- for applications  
prior to 2004)

AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ DE DK DM DZ EE ES  
FI GB GE GH GM HR HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA  
MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT TZ  
UA UG UZ VN YU ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM  
Publication Language: English  
Filing Language: English  
Fulltext Word Count: 157840

Main International Patent Class: **G06F-017/60**  
Fulltext Availability:  
Detailed Description

Detailed Description

... includes three layers of management: element management, information services management and presentation management. Every action **starts** with an incident. **Processing** is tailored to handling the incident with technology that responds to the unique characteristics of...

19/3,K/21 (Item 5 from file: 349)  
DIALOG(R)File 349:PCT FULLTEXT  
(c) 2004 WIPO/Univentio. All rts. reserv.

00806382

**METHOD FOR AFFORDING A MARKET SPACE INTERFACE BETWEEN A PLURALITY OF MANUFACTURERS AND SERVICE PROVIDERS AND INSTALLATION MANAGEMENT VIA A MARKET SPACE INTERFACE**

**PROCEDE DE MISE A DISPOSITION D'UNE INTERFACE D'ESPACE DE MARCHÉ ENTRE UNE PLURALITE DE FABRICANTS ET DES FOURNISSEURS DE SERVICES ET GESTION D'UNE INSTALLATION VIA UNE INTERFACE D'ESPACE DE MARCHÉ**

Patent Applicant/Assignee:

ACCENTURE LLP, 1661 Page Mill Road, Palo Alto, CA 94304, US, US  
(Residence), US (Nationality)

Inventor(s):

MIKURAK Michael G, 108 Englewood Blvd., Hamilton, NJ 08610, US,

Legal Representative:

HICKMAN Paul L (et al) (agent), Oppenheimer Wolff & Donnelly LLP, 1400  
Page Mill Road, Palo Alto, CA 94304, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200139028 A2 20010531 (WO 0139028)  
Application: WO 2000US32308 20001122 (PCT/WO US0032308)  
Priority Application: US 99444773 19991122; US 99444798 19991122

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ DE DK DM DZ EE  
ES FI GB GE GH GM HR HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV  
MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT  
TZ UA UG UZ VN YU ZW  
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR  
(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG  
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW  
(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English  
Filing Language: English  
Fulltext Word Count: 170977

Main International Patent Class: **G06F-017/60**  
Fulltext Availability:  
Detailed Description

Detailed Description

... programmer called libraries provided by the operating system to perform certain tasks, but basically the **program** **executed** down the page from **start** to finish, and the programmer was  
22  
solely responsible for the flow of control. This...

19/3,K/22 (Item 6 from file: 349)  
DIALOG(R)File 349:PCT FULLTEXT  
(c) 2004 WIPO/Univentio. All rts. reserv.

00802534

**ANY-TO-ANY COMPONENT COMPUTING SYSTEM**

**SYSTEME INFORMATIQUE A COMPOSANTS TOUTE CATEGORIE**

Patent Applicant/Assignee:

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Patent Applicant/Inventor:

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Legal Representative:

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Patent and Priority Information (Country, Number, Date):

Patent: WO 200135216 A2-A3 20010517 (WO 0135216)

Application: WO 2000US31231 20001113 (PCT/WO US0031231)

Priority Application: US 99164884 19991112

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ DE DK DM DZ EE  
ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT  
LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM  
TR TT TZ UA UG US UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 275671

Main International Patent Class: G06F-009/44

International Patent Class: G06F-017/22

19/3,K/23 (Item 7 from file: 349)  
DIALOG(R)File 349:PCT FULLTEXT  
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00799831 \*\*Image available\*\*

**BUSINESS TRANSACTION PROCESSING SYSTEMS AND METHODS**

**SYSTEMES ET PROCEDES DE TRAITEMENT DE TRANSACTIONS COMMERCIALES**

Patent Applicant/Assignee:

COMPUTER SCIENCES CORPORATION, 9500 Arboretum Blvd., Austin, TX 78759, US  
, US (Residence), US (Nationality)

Inventor(s):

BOBBITT Charles P, 6606 Mapleshade Lane, Dallas, TX 78252, US,  
DOUGHTY Steven G, 2332 Brennan Drive, Plano, TX 75075-6618, US,  
SHAW Robert Jay, 4312 Seabury, Dallas, TX 78287, US,

Legal Representative:

MEYERTONS Eric B (agent), Conley, Rose & Tayon, P.C., P.O. Box 398,  
Austin, TX 78767-0398, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200133398 A2 20010510 (WO 0133398)

Application: WO 2000US29978 20001030 (PCT/WO US0029978)

Priority Application: US 99162412 19991029; US 99162411 19991029; US  
99162602 19991029; US 99162509 19991029; US 99162708 19991029; US  
99162567 19991029; US 99162603 19991029; US 2000699036 20001027; US  
2000699015 20001027; US 2000699054 20001027; US 2000699038 20001027; US  
2000699021 20001027; US 2000699058 20001027; US 2000699056 20001027; US  
2000699037 20001027

Designated States:

(Protection type is "patent" unless otherwise stated - for applications  
prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ DE DK DM DZ EE  
ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT  
LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM  
TR TT TZ UA UG UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 77244

Main International Patent Class: G06F-017/60

Fulltext Availability:

Detailed Description

Detailed Description

... step 501, the user of the FSO computer system may schedule a task to  
be **executed** at a user defined period to **process** the Smart **Trigger**  
table. In step 502, an evaluation may be made if certain predefined  
condition such as...

19/3,K/24 (Item 8 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

(c) 2004 WIPO/Univentio. All rts. reserv.

00781825

SYSTEM OF REUSABLE SOFTWARE PARTS AND METHODS OF USE

SYSTEME D'UNITES LOGICIELLES REUTILISABLES ET PROCEDES D'UTILISATION

Patent Applicant/Assignee:

Z-FORCE CORPORATION, 151 Kalmus Drive, Suite B-250, Costa Mesa, CA 92626,  
US, US (Residence), US (Nationality).

Inventor(s):

MILOUSHEV Vladimir I, 30802 Calle Barbosa, Laguna Nigel, CA 92677, US,  
NICKOLOV Peter A, 158 Giotto, Irvine, CA 92614, US,

Legal Representative:

TACHNER Adam H (et al) (agent), Crosby, Heafey, Roach & May, Suite 2000,  
Two Embarcadero Center, San Francisco, CA 94111, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200114959 A2-A3 20010301 (WO 0114959)  
Application: WO 2000US22694 20000816 (PCT/WO US0022694)  
Priority Application: US 99149371 19990816; US 99149624 19990816  
Designated States:  
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ DE DK DM DZ EE  
ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT  
LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM  
TR TT TZ UA UG UZ VN YU ZA ZW  
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE  
(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG  
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW  
(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English  
Filing Language: English  
Fulltext Word Count: 182432

Main International Patent Class: G06F-009/46  
International Patent Class: G06F-009/44  
Fulltext Availability:  
Detailed Description

Detailed Description

... A connects its evs terminal to DM-EST's evs terminal.

3. Both parts are activated .

4. Part A arms DM-EST passing a time period and a context.

5. At...

19/3,K/25 (Item 9 from file: 349)  
DIALOG(R)File 349:PCT FULLTEXT  
(c) 2004 WIPO/Univentio. All rts. reserv.

00777022

A SYSTEM, METHOD AND ARTICLE OF MANUFACTURE FOR AN E-COMMERCE BASED  
ARCHITECTURE  
SYSTEME, PROCEDE ET ARTICLE DE PRODUCTION POUR UNE ARCHITECTURE BASEE SUR  
LE COMMERCE ELECTRONIQUE

Patent Applicant/Assignee:

AC PROPERTIES BV, Parkstraat 83, NL-2514 JG 'S Gravenhage, NL, NL  
(Residence), NL (Nationality), (For all designated states except: US)

Patent Applicant/Inventor:

UNDERWOOD Roy A, 4436 Hearthmoor Court, Long Grove, IL 60047, US, US  
(Residence), US (Nationality), (Designated only for: US)

Legal Representative:

HICKMAN Paul L (et al) (agent), Hickman Coleman & Hughes, LLP, P.O. Box  
52037, Palo Alto, CA 94303-0746, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200109794 A2-A3 20010208 (WO 0109794)  
Application: WO 2000US20704 20000728 (PCT/WO US0020704)  
Priority Application: US 99364734 19990730

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES FI GB GE GH GM  
HR HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG MK MN MW MX

NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG US UZ VN YU ZW  
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE  
(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG  
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW  
(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 122424

Main International Patent Class: G06F-009/46

International Patent Class: G06F-009/44 ...

... G06F-017/30 ...

... G06F-017/60

Fulltext Availability:

Detailed Description

Detailed Description

... Create the users and roles for the database. To run a database script,  
Connect as **execute** Oracle for WinNT  
SQLPlus 8.0 from the **start** menu. A Username.

script is **executed** by typing '@' followed by the full path and name of  
system ; the script. The scripts...

19/3,K/26 (Item 10 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

(c) 2004 WIPO/Univentio. All rts. reserv.

00777021

A SYSTEM, METHOD AND ARTICLE OF MANUFACTURE FOR AN E-COMMERCE BASED USER  
FRAMEWORK DESIGN FOR MAINTAINING USER PREFERENCES, ROLES AND DETAILS  
SYSTEME, PROCEDE ET ARTICLE MANUFACTURE UTILISES EN COMMERCE ELECTRONIQUE  
POUR LA CONCEPTION DE STRUCTURES D'UTILISATEURS DESTINEES A PRESERVER  
LES PREFERENCES, ROLES ET DETAILS DES UTILISATEURS

Patent Applicant/Assignee:

ACCENTURE LLP, Parkstraat 83, NL-2514 JG 's Gravenhage, The Hague, NL, NL  
(Residence), NL (Nationality), (For all designated states except: US)

Patent Applicant/Inventor:

UNDERWOOD Roy A, 4436 Hearthmoor Court, Long Grove, IL 60047, US, US  
(Residence), US (Nationality), (Designated only for: US)

Legal Representative:

HICKMAN Paul L (agent), Oppenheimer Wolff & Donnelly LLP, 1400 Page Mill  
Road, Palo Alto, CA 94304, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200109792 A2-A3 20010208 (WO 0109792)

Application: WO 2000US20549 20000728 (PCT/WO US0020549)

Priority Application: US 99364091 19990730

Designated States:

(Protection type is "patent" unless otherwise stated - for applications  
prior to 2004)

AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES FI GB GE GH GM  
HR HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG MK MN MW MX  
NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG US UZ VN YU ZW  
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE  
(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW  
(EA) AM AZ BY KG KZ MD RU TJ TM  
Publication Language: English  
Filing Language: English  
Fulltext Word Count: 122232

Main International Patent Class: **G06F-017/30**  
International Patent Class: **G06F-009/44**  
Fulltext Availability:  
Detailed Description

Detailed Description

... ReTA Activity framework distributes the application development responsibilities as follows.

Webpage(ActiveServerPage)(ViewIController)Theapplication'swebpagelogic832  
**starts** the activity 834, **executes** the sub-activity and creates the  
**user** interfaces. No business logic is contained directly in the web page  
code. The application developer...it and copies it into the system test  
environment. A validation or test plan is **executed** pass/fail/deviation.  
The system test team 2806 signs the change control portion of the...

19/3,K/27 (Item 11 from file: 349)  
DIALOG(R)File 349:PCT FULLTEXT  
(c) 2004 WIPO/Univentio. All rts. reserv.

00761432

**METHODS, CONCEPTS AND TECHNOLOGY FOR DYNAMIC COMPARISON OF PRODUCT FEATURES  
AND CUSTOMER PROFILE**

**PROCEDES, CONCEPTS ET TECHNIQUE DE COMPARAISON DYNAMIQUE DE  
CARACTERISTIQUES D'UN PRODUIT ET DU PROFIL DES CONSOMMATEURS**

Patent Applicant/Assignee:

ACCENTURE LLP, 100 South Wacker Drive, Chicago, IL 60606, US, US  
(Residence), US (Nationality)

Inventor(s):

GUHEEN Michael F, 2218 Mar East Street, Tiburon, CA 94920, US,  
MITCHELL James D, 3004 Alma, Manhattan Beach, CA 90266, US,  
BARRESE James J, 757 Pine Avenue, San Jose, CA 95125, US,

Legal Representative:

BRUESS Steven C (agent), Merchant & Gould P.C., P.O. Box 2903,  
Minneapolis, MN 55402-0903, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200073958 A2 20001207 (WO 0073958)  
Application: WO 2000US14459 20000524 (PCT/WO US0014459)  
Priority Application: US 99320818 19990527

Designated States:

(Protection type is "patent" unless otherwise stated - for applications  
prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY CA CH CN CR CU CZ DE DK DM DZ EE ES  
FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU  
LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR  
TT TZ UA UG UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English  
Filing Language: English

Fulltext Word Count: 151011

Main International Patent Class: **G06F-017/60**

Fulltext Availability:

Detailed Description

Detailed Description

... programmer called libraries provided by the operating system to perform certain tasks, but basically the **program** **executed** down the page from **start** to finish, and the programmer was solely responsible for the flow of control. This was...

19/3,K/28 (Item 12 from file: 349)

DIALOG(R) File 349:PCT FULLTEXT

(c) 2004 WIPO/Univentio. All rts. reserv.

00761422

**BUSINESS ALLIANCE IDENTIFICATION**

**SYSTEME, PROCEDE ET ARTICLE DE PRODUCTION POUR L'IDENTIFICATION D'ALLIANCES  
COMMERCIALES DANS UN CADRE D'ARCHITECTURE RESEAU**

Patent Applicant/Assignee:

ACCENTURE LLP, 100 South Wacker Drive, Chicago, IL 60606, US, US

(Residence), US (Nationality)

Inventor(s):

GUHEEN Michael F, 2218 Mar East Street, Tiburon, CA 94920, US,

MITCHELL James D, 3004 Alma, Manhattan Beach, CA 90266, US,

BARRESE James J, 757 Pine Avenue, San Jose, CA 95125, US,

Legal Representative:

BRUESS Steven C (agent), Merchant, Gould, Smith, Edell, Welter & Schmidt,  
P.A., P.O. Box 2903, Minneapolis, MN 55402-0903, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200073928 A2-A3 20001207 (WO 0073928)

Application: WO 2000US14375 20000524 (PCT/WO US0014375)

Priority Application: US 99320816 19990527

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY CA CH CN CR CU CZ DE DK DM DZ EE ES  
FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU  
LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR  
TT TZ UA UG UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 149371

Main International Patent Class: **G06F-017/60**

Fulltext Availability:

Detailed Description

Detailed Description

... also engaging and informative. This is especially true of Internet and kiosk-based systems, where **users** have a notoriously short concentration span.



This requirement for more attractive **user** interfaces has **triggered** the evolution of media-rich applications, the development of which requires new tools and processes...

19/3,K/29 (Item 13 from file: 349)

DIALOG(R) File 349:PCT FULLTEXT

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00574699 \*\*Image available\*\*

**METHOD FOR EXECUTING A SECURITY CRITICAL ACTIVITY**

**PROCEDE D'EXECUTION D'UNE ACTIVITE CRITIQUE QUANT A LA SECURITE**

Patent Applicant/Assignee:

MYSPLACE AB,

WETTERGREN Christian,

Inventor(s):

WETTERGREN Christian,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200038072 A1 20000629 (WO 0038072)

Application: WO 99SE2422 19991217 (PCT/WO SE9902422)

Priority Application: SE 984421 19981218

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AE AL AM AT AU AZ BA BB BG BR BY CA CH CN CR CU CZ DE DK DM EE ES FI GB  
GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA  
MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT TZ UA  
UG US UZ VN YU ZA ZW GH GM KE LS MW SD SL SZ TZ UG ZW AM AZ BY KG KZ MD  
RU TJ TM AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE BF BJ CF  
CG CI CM GA GN GW ML MR NE SN TD TG

Publication Language: English

Fulltext Word Count: 5223

Main International Patent Class: **G06F-012/14**

Fulltext Availability:

Claims

Claim

... a security critical activity in a security device, wherein the security critical activity is **executed** with **user** involvement, comprising the **steps** of  
i) **starting** the **execution** of an action of a security critical activity,  
ii) checking if this action, under the...

19/3,K/30 (Item 14 from file: 349)

DIALOG(R) File 349:PCT FULLTEXT

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00418748 \*\*Image available\*\*

**SYSTEMS AND METHODS FOR SECURE TRANSACTION MANAGEMENT AND ELECTRONIC RIGHTS PROTECTION**

**SYSTEMES ET PROCEDES DE GESTION DE TRANSACTIONS SECURISEES ET DE PROTECTION DE DROITS ELECTRONIQUES**

Patent Applicant/Assignee:

INTERTRUST TECHNOLOGIES CORP,

Inventor(s):

GINTER Karl L,  
SHEAR Victor H,  
SIBERT W Olin,  
SPAHN Francis J,  
VAN WIE David M,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9809209 A1 19980305  
Application: WO 97US15243 19970829 (PCT/WO US9715243)  
Priority Application: US 96706206 19960830

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES FI GB GE GH HU  
IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG MK MN MW MX NO NZ PL  
PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG UZ VN YU ZW GH KE LS MW SD  
SZ UG ZW AM AZ BY KG KZ MD RU TJ TM AT BE CH DE DK ES FI FR GB GR IE IT  
LU MC NL PT SE BF BJ CF CG CI CM GA GN ML MR NE SN TD TG

Publication Language: English

Fulltext Word Count: 195626

Main International Patent Class: G06F-001/00

Fulltext Availability:

Detailed Description

Detailed Description

... form of a "secure database" or  
management file system 610. This secure database 610 may  
store and organize information used by ROS 602 to perform VDE  
functions 604. Thus, the code...

...VDE files, etc. Portions of the elements indicated in  
secondary storage 652 may allso be stored in ROM 658, so long as  
- 193  
those elements do not require changes (except when...

19/3,K/31 (Item 15 from file: 349)

DIALOG(R) File 349:PCT FULLTEXT

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00387760 \*\*Image available\*\*

**METHOD AND SYSTEM FOR IMPLEMENTING DATA TRANSFERS**

**PROCEDE ET SYSTEME DE REALISATION DE TRANSFERTS DE DONNEES**

Patent Applicant/Assignee:

THE TRUSTEES OF PRINCETON UNIVERSITY,

Inventor(s):

BLUMRICH Matthias A,  
FELTEN Edward W,  
LI Kai,  
DUBNICKI Cezary,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9728503 A1 19970807  
Application: WO 97US1108 19970127 (PCT/WO US9701108)  
Priority Application: US 96595792 19960202

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES FI GB GE HU IL  
IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG MK MN MW MX NO NZ PL PT

RO RU SD SE SG SI SK TJ TM TR TT UA UG UZ VN KE LS MW SD SZ UG AM AZ BY  
KG KZ MD RU TJ TM AT BE CH DE DK ES FI FR GB GR IE IT LU MC NL PT SE BF  
BJ CF CG CI CM GA GN ML MR NE SN TD TG

Publication Language: English

Fulltext Word Count: 14454

Main International Patent Class: **G06F-013/00**

Fulltext Availability:

Detailed Description

Detailed Description

... The context-switch code can do this with a single  
STORE instruction.

When the interrupted **user process** resumes, it will **execute**  
the LOAD instruction of its transfer- **initiation** sequence, which  
will return a failure code signifying that the hardware is in the  
Idle...

**19/3,K/32 (Item 16 from file: 349)**

DIALOG(R)File 349:PCT FULLTEXT

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00344642

**SYSTEMS AND METHODS FOR SECURE TRANSACTION MANAGEMENT AND ELECTRONIC RIGHTS  
PROTECTION**

**SYSTEMES ET PROCEDES DE GESTION SECURISEE DE TRANSACTIONS ET DE PROTECTION  
ELECTRONIQUE DES DROITS**

Patent Applicant/Assignee:

ELECTRONIC PUBLISHING RESOURCES INC,

Inventor(s):

GINTER Karl L,

SHEAR Victor H,

SPAHN Francis J,

VAN WIE David M,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9627155 A2 19960906

Application: WO 96US2303 19960213 (PCT/WO US9602303)

Priority Application: US 95388107 19950213

Designated States:

(Protection type is "patent" unless otherwise stated - for applications  
prior to 2004)

AL AM AT AU AZ BB BG BR BY CA CH CN CZ DE DK EE ES FI GB GE HU IS JP KE  
KG KP KR KZ LK LR LS LT LU LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE  
SG SI SK TJ TM TR TT UA UG UZ VN KE LS MW SD SZ UG AZ BY KG KZ RU TJ TM  
AT BE CH DE DK ES FR GB GR IE IT LU MC NL PT SE BF BJ CF CG CI CM GA GN  
ML MR NE SN TD TG

Publication Language: English

Fulltext Word Count: 207972

Main International Patent Class: **G06F-001/00**

International Patent Class: **G06F-17:60**

Fulltext Availability:

Detailed Description

Detailed Description

... include locating other services and/or  
resources such as information resources, language or

format translation, **processing** , credit (or additional credit) authorization, etc. Resources include reference databases, networks, high powered or specialized...

19/3,K/33 (Item 17 from file: 349)  
DIALOG(R) File 349:PCT FULLTEXT  
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00299581

**A SECURE SOFTWARE RENTAL SYSTEM USING CONTINUOUS ASYNCHRONOUS PASSWORD VERIFICATION**

**SYSTEME DE LOCATION DE LOGICIEL SECURISE UTILISANT UNE VERIFICATION CONTINUE ASYNCHRONE DE MOTS DE PASSE**

Patent Applicant/Assignee:

ANANDA Mohan,

Inventor(s):

ANANDA Mohan,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9517732 A1 19950629

Application: WO 94US14923 19941222 (PCT/WO US9414923)

Priority Application: US 93398 19931222

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AM AT AU BB BG BR BY CA CH CN CZ DE DK ES FI GB GE HU JP KE KG KP KR KZ  
LK LT LU LV MD MG MN MW NL NO NZ PL PT RO RU SD SE SI SK TJ TT UA UZ VN  
KE MW SD SZ AT BE CH DE DK ES FR GB GR IE IT LU MC NL PT SE BF BJ CF CG  
.CI CM GA GN ML MR NE SN TD TG

Publication Language: English

Fulltext Word Count: 11382

Main International Patent Class: **G06F-017/60**

Fulltext Availability:

Detailed Description

Detailed Description

... The user display interface  
generates and provides messages to the user as necessary.

When the **user** **executes** the **application** software, the rental security manager **initiates** authorization verification. The authorization verification **process** begins by obtaining the time through the user processor clock interface.

The rental security manager...

File 344:Chinese Patents Abs Aug 1985-2004/May  
 (c) 2004 European Patent Office  
 File 347:JAPIO Nov 1976-2004/May(Updated 040903)  
 (c) 2004 JPO & JAPIO  
 File 350:Derwent WPIX 1963-2004/UD,UM &UP=200461  
 (c) 2004 Thomson Derwent

Set	Items	Description
S1	5241274	READ??? OR WRIT??? OR EXECUT??? OR OPERATING?? OR PROCESS?- ??? OR LAUNCH???
S2	491189	(S1 OR OPEN???) (3N) (FILE?? OR DATA?? OR SOFTWARE?? OR CODE? ? OR CODING OR FOLDER?? OR FILE??()SYSTEM?? OR FILESYSTEM??)
S3	78458	(USER?? OR PROGRAMMER?? OR OPERATOR?? OR PROGRAMER??) (3N) (- PROCESS??? OR OPERATION?? OR ALGORITHM?? OR PROGRAM??? OR APP- LICATION?? OR PROCEDURE?? OR STEPS?? OR MODULE?? OR MACRO??)
S4	3207	(HOLD??? OR RESERV??? OR STORING?? OR STOR??) (3N) S3
S5	2513	(EXECUT?? OR EXECUTING?) (3N) S3
S6	52531	(TRIGGER??? OR INITIAT??? OR ACTIVAT??? OR START???) (4N) (E- XECUT??? OR OPERATING?? OR PROCES??? OR LAUNCH???)
S7	12503	S2 AND S3
S8	45	S7 AND S4 AND S5
S9	2	S8 AND S6
S10	33	S8 AND IC=G06F?
S11	31	S10 NOT S9
S12	22	S11 NOT PY>2001

9/3,K/1 (Item 1 from file: 350)  
DIALOG(R) File 350:Derwent WPIX  
(c) 2004 Thomson Derwent. All rts. reserv.

014750639 \*\*Image available\*\*  
WPI Acc No: 2002-571343/200261  
XRPX Acc No: N02-452604

Data processor transfers process state information and command  
addresses from registers to new register based on the process mode  
variation by shifting initiation mode to exception mode

Patent Assignee: ZHIYUAN SCI & TECH CO LTD (ZHIY-N)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 2002196938	A	20020712	JP 2000376063	A	20001211	200261 B

Priority Applications (No Type Date): JP 2000376063 A 20001211

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
JP 2002196938	A	6	G06F-009/46	

Data processor transfers process state information and command  
addresses from registers to new register based on the process mode  
variation by shifting initiation mode to exception mode

Abstract (Basic):

... The process state information and addresses of each program  
command executed in user mode are stored in respective registers  
during process exception mode. Based on the input process mode  
variation, the register contents are transferred to a new register for  
executing specific processes while the initiation mode is changed  
to exception mode.

... An INDEPENDENT CLAIM is included for data processing method  
...

... Data processor with exception flow control facility

9/3,K/2 (Item 2 from file: 350)  
DIALOG(R) File 350:Derwent WPIX  
(c) 2004 Thomson Derwent. All rts. reserv.

010642374 \*\*Image available\*\*  
WPI Acc No: 1996-139328/199614  
XRPX Acc No: N96-116769

Programmable controller for executing user program having general  
and relay ladder instruction - has special processor for executing relay  
ladder instruction and releasing microprocessor from wait state to  
process long read instruction, indicate next instruction and point to  
machine codes terminating in another read to set address

Patent Assignee: ALLEN BRADLEY CO (ALLB )

Inventor: GIBART A G; KNULL K

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5495588	A	19960227	US 93154232	A	19931118	199614 B
			US 94365979	A	19941228	

Priority Applications (No Type Date): US 93154232 A 19931118; US 94365979 A 19941228

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 5495588	A		8	G06F-009/455	Cont of application US 93154232

**Programmable controller for executing user program having general and relay ladder instruction...**

...from wait state to process long read instruction, indicate next instruction and point to machine codes terminating in another read to set address

...Abstract (Basic): The controller comprises a computer memory storing the user program . The memory holds words at addresses in an address space, which includes one predetermined designated address. A general...

...A special processor monitors the first addresses of the general processor . The wait signal is initiated upon detecting the predetermined designated address on the address bus. The computer memory is provided...

...The instructions from the user program in the computer memory at addresses identified by an instruction address in a processor instruction...

?

12/3,K/1 (Item 1 from file: 347)  
DIALOG(R)File 347:JAPIO  
(c) 2004 JPO & JAPIO. All rts. reserv.

06018706 \*\*Image available\*\*  
TASK TRACING DEVICE

PUB. NO.: 10-301806 [JP 10301806 A]  
PUBLISHED: November 13, 1998 (19981113)  
INVENTOR(s): TAKAKURA NORIAKI  
SAKAI TAKASHI  
APPLICANT(s): NEC IC MICROCOMPUT SYST LTD [470861] (A Japanese Company or Corporation), JP (Japan)  
NEC CORP [000423] (A Japanese Company or Corporation), JP (Japan)  
APPL. NO.: 09-111397 [JP 97111397]  
FILED: April 28, 1997 (19970428)

INTL CLASS: G06F-011/28

ABSTRACT

...for a tracing processing by constituting a task tracing device by a user system for **executing** a **user program** and a tracing device for gathering and storing tracing information...

...SOLUTION: The user system 1 incorporates a CPU 2 for **executing** the **user program** stored in a **user memory** and the user system 1 and the tracing device 11 are connected through an address path 4 and a data path 5. In the tracing device 11, a **data write** detection/ **data** gathering part 12 detects whether or not **data** are **written** to the certain specified area of the user memory 3 and gathers only the **written data** as tracing **data**. Then, the gathered tracing data are stored and preserved in a tracing memory 13 by...

12/3,K/2 (Item 2 from file: 347)  
DIALOG(R)File 347:JAPIO  
(c) 2004 JPO & JAPIO. All rts. reserv.

05575485 \*\*Image available\*\*  
DATA INPUT DEVICE

PUB. NO.: 09-190285 [JP 9190285 A]  
PUBLISHED: July 22, 1997 (19970722)  
INVENTOR(s): OKAZAKI NORITOSHI  
APPLICANT(s): TOSHIBA SYST KAIHATSU KK [000000] (A Japanese Company or Corporation), JP (Japan)  
TOSHIBA CORP [000307] (A Japanese Company or Corporation), JP (Japan)  
APPL. NO.: 08-004223 [JP 964223]  
FILED: January 12, 1996 (19960112)

INTL CLASS: G06F-003/033 ; G06F-003/02

ABSTRACT

...the keyboard 16 is operated, it is detected by a touch panel 5 and a **data** processing is **executed** with a **user program** stored in the electronic circuit 6.



12/3,K/3 (Item 3 from file: 347)  
DIALOG(R)File 347:JAPIO  
(c) 2004 JPO & JAPIO. All rts. reserv.

05331662 \*\*Image available\*\*  
WORK FLOW SYSTEM

PUB. NO.: 08-287162 [JP 8287162 A]  
PUBLISHED: November 01, 1996 (19961101)  
INVENTOR(s): ENDO YOSHIE  
APPLICANT(s): TOSHIBA CORP [000307] (A Japanese Company or Corporation), JP  
(Japan)  
APPL. NO.: 07-295577 [JP 95295577]  
FILED: November 14, 1995 (19951114)

INTL CLASS: G06F-017/60

ABSTRACT

... individual information on individual's performing ability to carry out unit operations that can be **executed**, user by **user**, and an **operation** model file 6 **storing** an operation model consisting of plural work points constituting operations, a flow showing the execution...

... the work points of the operation model which are read out of the operation model **file** are **executed** in order by the respective users according to the flow, the execution result including the...

12/3,K/4 (Item 4 from file: 347)  
DIALOG(R)File 347:JAPIO  
(c) 2004 JPO & JAPIO. All rts. reserv.

02919924 \*\*Image available\*\*  
DATA PROCESSING SYSTEM

PUB. NO.: 01-217524 [JP 1217524 A]  
PUBLISHED: August 31, 1989 (19890831)  
INVENTOR(s): SATO YUTAKA  
USUHA HIDEYUKI  
APPLICANT(s): HITACHI LTD [000510] (A Japanese Company or Corporation), JP  
(Japan)  
HITACHI MICRO COMPUT ENG LTD [470864] (A Japanese Company or Corporation), JP (Japan)  
APPL. NO.: 63-042063 [JP 8842063]  
FILED: February 26, 1988 (19880226)  
JOURNAL: Section: P, Section No. 966, Vol. 13, No. 530, Pg. 90,  
November 27, 1989 (19891127)

DATA PROCESSING SYSTEM

INTL CLASS: G06F-011/22 ; G06F-009/44

ABSTRACT

... attain the display, etc., of a vicariously executing memory without affecting the execution of a **user program** and to secure immediacy by dividing an emulation memory into plural memory banks and controlling...

...CONSTITUTION: The emulsion memory, which **stores** the **user program**

to be **executed** by a target **processor** TMPU and **data** , is divided into two memory banks EMEM0 and EMEM1. The EMEM0 is allocated to an...

12/3,K/5 (Item 5 from file: 347)  
DIALOG(R)File 347:JAPIO  
(c) 2004 JPO & JAPIO. All rts. reserv.

02584859  
SHARE/EXCLUSION CONTROL SYSTEM FOR AREA SHARED FILE AMONG PLURAL HOST COMPUTERS

PUB. NO.: 63-201759 [JP 63201759 A]  
PUBLISHED: August 19, 1988 (19880819)  
INVENTOR(s): KATO MITSUGI  
APPLICANT(s): NEC CORP [000423] (A Japanese Company or Corporation), JP  
(Japan)  
APPL. NO.: 62-034225 [JP 8734225]  
FILED: February 17, 1987 (19870217)  
JOURNAL: Section: P, Section No. 804, Vol. 12, No. 492, Pg. 9,  
December 22, 1988 (19881222)

INTL CLASS: G06F-015/16

#### ABSTRACT

... deletion processing from plural hosts, and to raise the use efficiency of an area shared **file** , by **executing** an exclusion/share control by a member unit to the area shared file...

...CONSTITUTION: For instance, a **user program .beta. executes** a request for updating a member M utilized exclusively by a **user program .alpha.**, and when a member allocation control means is started thereby, since a fact that its member M has been utilized exclusively by the **user program .alpha.** is **stored** in a using member storage means, its request is rejected, and an exclusive utilization of the member M by the **user program .alpha.** is secured. On the contrary, in case when the **user program .alpha.** has requested the member M in a utilization form of a common utilization, the...

...M, when a request for updating, etc. its member M has been received from the **user program .beta.** In such a way, the member M can be utilized in common.

12/3,K/6 (Item 6 from file: 347)  
DIALOG(R)File 347:JAPIO  
(c) 2004 JPO & JAPIO. All rts. reserv.

01495355 \*\*Image available\*\*  
DEBUGGING METHOD

PUB. NO.: 59-206955 [JP 59206955 A]  
PUBLISHED: November 22, 1984 (19841122)  
INVENTOR(s): NISHIGUCHI OSAMU  
APPLICANT(s): OMRON TATEISI ELECTRONICS CO [000294] (A Japanese Company or Corporation), JP (Japan)  
APPL. NO.: 58-082337 [JP 8382337]  
FILED: May 10, 1983 (19830510)  
JOURNAL: Section: P, Section No. 346, Vol. 09, No. 76, Pg. 60, April

05, 1985 (19850405)

INTL CLASS: G06F-011/28 ; G06F-009/06

ABSTRACT

PURPOSE: To reduce the cost, by discriminating the accessing condition of a memory through **software process** to eliminate the necessity of dedicated hardware...

... and console controlling circuit 7. The keyboard of a console 2 is operated when a **user** application program is inputted, the content of a program is checked, or other data are inputted. When a **user program** is **executed**, the main control section 3 appoints an address of the memory 6 and, when the address is reached, stops the **user program** by performing an interruption. When the **user program** is stopped, the main control section 3 checks the content of a stack, in which an address storing the return instruction of the **user program** is **stored**, and discriminates whether the memory 6 is fetched as a program or data.

12/3,K/7 (Item 1 from file: 350)

DIALOG(R) File 350:Derwent WPIX

(c) 2004 Thomson Derwent. All rts. reserv.

014186829 \*\*Image available\*\*

WPI Acc No: 2002-007526/200201

XRPX Acc No: N02-006616

**Application software shared system for enterprise, acquires intrinsic information of user stored in intrinsic memory based on stored user information, for executing application software**

Patent Assignee: A & I SYSTEM KK (AISY-N)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 2001290641	A	20011019	JP 2000108601	A	20000410	200201 B

Priority Applications (No Type Date): JP 2000108601 A 20000410

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
JP 2001290641	A	12	G06F-009/06	

... **system for enterprise, acquires intrinsic information of user stored in intrinsic memory based on stored user information, for executing application software**

Abstract (Basic):

... peculiar to each user required when utilizing an application software, respectively. The information required for **executing** an application **software**, is acquired from the stored common and intrinsic information of the users by utilizing the...

... ensured when the same application software is shared.

Application software is lent cheaply, as several **users** share the same **application** software...

International Patent Class (Main): G06F-009/06

International Patent Class (Additional): G06F-015/00 ...

... G06F-015/16 ...

... G06F-015/167 ...

... G06F-015/177

12/3,K/8 (Item 2 from file: 350)  
DIALOG(R)File 350:Derwent WPIX  
(c) 2004 Thomson Derwent. All rts. reserv.

013166591 \*\*Image available\*\*  
WPI Acc No: 2000-338464/200029  
XRPX Acc No: N00-254075

**Data acquisition system for used in laboratory research, data logging,  
has iterators which are executed using information in buffer object to  
perform data transfer**

Patent Assignee: NAT INSTR CORP (NAIN-N)  
Inventor: EVANS K; SCHWAN B C  
Number of Countries: 001 Number of Patents: 001  
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 6052743	A	20000418	US 97968221	A	19971112	200029 B

Priority Applications (No Type Date): US 97968221 A 19971112

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 6052743	A	31	G06F-013/28	

Abstract (Basic):

... and from computer system. Foreground and background iterators  
stored in memory of computer system are **executed** based on DAQ **user  
application** and using information in buffer object to perform data  
transfer to and from the DAQ...

... A DAQ driver level software receives calls from DAQ **user  
application**. The buffer object **stores** information regarding data  
format, data size and number of scans of data to be transferred...

...is executed to transfer between client and buffer portion of memory. The  
background iterator is **executed** to transfer **data** between buffer  
portion of memory and on-board memory on DAQ device. An INDEPENDENT  
CLAIM...

...performing intelligent buffering function using buffer object in data  
acquisition system used in laboratory research, **process** monitoring  
and control, **data** logging, analytical chemistry, test and analysis of  
physical phenomena, control of mechanical or electrical machinery...

International Patent Class (Main): G06F-013/28

12/3,K/9 (Item 3 from file: 350)  
DIALOG(R)File 350:Derwent WPIX  
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012481059 \*\*Image available\*\*  
WPI Acc No: 1999-287167/199924  
XRPX Acc No: N99-214459

**User and application information manipulating method in distributed  
computer environment**

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC )  
Inventor: COLLINS M; TORRES R J

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5897635	A	19990427	US 95474396	A	19950607	199924 B

Priority Applications (No Type Date): US 95474396 A 19950607

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 5897635	A	19	G06F-017/30	

User and application information manipulating method in distributed computer environment

Abstract (Basic):

... A user and application information software with a central datafile, user interface and manipulating procedures is installed. User input is monitored and respective procedure is executed enabling user modification of datafile entries and/or information associated with applications in synchronization with directory and...

... An INDEPENDENT CLAIM is included for user and application information storing and manipulating apparatus...

...done without time consumption and repetitive process as a single centralized file contains information about user and application and the file is readily, easily and efficiently modified through customized user interface. System upgradation and efficient administration of user demand is enabled by providing centralized repository for information needed by all applications. User is enabled to centrally review, modify or update data file entries by pulling user information from each application and storing in datafile. Interaction with various applications is enabled by centrally storing standard profiles. Enables implementation...

...The figure depicts flow diagram for central database interface for storing and manipulating user and application information on distributed computer system...

International Patent Class (Main): G06F-017/30

12/3,K/10 (Item 4 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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012255160 \*\*Image available\*\*

WPI Acc No: 1999-061266/199906

XRPX Acc No: N00-191200

PLC has power failure compensation function and power failure compensation e.g. for manufacturing automation system in which the program data and execution step of the user 's program are stored into a storing unit

Patent Assignee: LG IND SYSTEMS CO LTD (GLDS )

Inventor: YANG S; YANG S W

Number of Countries: 003 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
CN 1193773	A	19980923	CN 97107262	A	19971217	199906 B
US 6038669	A	20000314	US 97988807	A	19971211	200023
KR 98048106	A	19980915	KR 9666647	A	19961217	199941
KR 186349	B1	19990415	KR 9666647	A	19961217	200051

Priority Applications (No Type Date): KR 9666647 A 19961217

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
CN 1193773	A			G06F-009/06	
US 6038669	A		8	G06F-001/00	
KR 98048106	A			G05B-019/05	
KR 186349	B1			G05B-019/05	

... function and power failure compensation e.g. for manufacturing automation system in which the program data and execution step of the user 's program lare stored into a storing unit

Abstract (Basic):

... Programmable Logic Controller (PLC) includes: a first storing unit for storing a program **execution data** and a **user 's program executed** by the CPU; a power failure detector for checking a state of an alternating current...

... execution result value of the program finally executed before the power failure into the third **storing** unit, reading the **user 's program stored** in the first storing unit and the data stored in the second and third storing unit, and **executing the user 's program** from the next step of the finally executed program step...

...International Patent Class (Main): **G06F-001/00** ...

... **G06F-009/06**

12/3,K/11 (Item 5 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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011680497 \*\*Image available\*\*

WPI Acc No: 1998-097406/199809

XRPX Acc No: N98-078306

**Bus switching circuit for microcomputer application system development - has switching unit which switches a bus connecting memory and CPU so that second memory can be accessed externally when first memory is accessed by CPU and vice versa**

Patent Assignee: TOSHIBA KK (TOKE )

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 9325935	A	19971216	JP 96142942	A	19960605	199809 B

Priority Applications (No Type Date): JP 96142942 A 19960605

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
JP 9325935	A		7	G06F-013/16	

...Abstract (Basic): that connects first and second memories (1A,1B) and a CPU (3A,3B). The CPU **executes a user program stored** in the first and second memories and assigns the same address to the first and ...

...by CPU. CPU which uses high speed clock can be used since structure is simple. **Data** can be **written** simultaneously...

International Patent Class (Main): **G06F-013/16**

12/3,K/12 (Item 6 from file: 350)  
DIALOG(R)File 350:Derwent WPIX  
(c) 2004 Thomson Derwent. All rts. reserv.

010527690 \*\*Image available\*\*  
WPI Acc No: 1996-024643/199603  
XRPX Acc No: N96-020780

**Automatic data operation backup device for microcomputer - stores user program data in corresponding register and backs up data after executing monitor program which is offered while executing user program**

Patent Assignee: KINSEI ELECTRON KK (KINS-N); LG SEMICONDUCTOR CO LTD (GLDS ); GOLDSTAR ELECTRON CO LTD (GLDS )

Inventor: CHO D

Number of Countries: 003 Number of Patents: 003

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 7295851	A	19951110	JP 9515606	A	19950202	199603 B
US 5878247	A	19990302	US 95382705	A	19950202	199916
			US 97899597	A	19970724	
KR 9609824	B1	19960724	KR 942015	A	19940203	199922

Priority Applications (No Type Date): KR 942015 A 19940203

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
JP 7295851	A		8	G06F-011/22	
US 5878247	A			G06F-011/22	Cont of application US 95382705
KR 9609824	B1			G06F-003/00	

... stores user program data in corresponding register and backs up data after executing monitor program which is offered while executing user program

...Abstract (Basic): The backup device has a CPU (104) which executes a user program from outside or a monitor program from inside. When the CPU is executing the user program, the monitor program is offered and CPU stores the user program in memory (103). The monitor program demand signal, register record signal and register interruptor signal...

...storage (106). After the monitor program is completed, the CPU makes the control return to user program.

International Patent Class (Main): G06F-003/00 ...

... G06F-011/22

International Patent Class (Additional): G06F-011/28

12/3,K/13 (Item 7 from file: 350)  
DIALOG(R)File 350:Derwent WPIX  
(c) 2004 Thomson Derwent. All rts. reserv.

010270844 \*\*Image available\*\*  
WPI Acc No: 1995-172099/199523  
XRPX Acc No: N95-134855

**Performance monitoring of programs on data processor - holding monitoring programme for user level or system programmes within**

# **kernel of operating system**

Patent Assignee: MICROSOFT CORP (MICR-N)

Inventor: BOLOSKY W J; RASHID R F

Number of Countries: 006 Number of Patents: 007

## **Patent Family:**

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 652518	A1	19950510	EP 94117366	A	19941103	199523 B
CA 2134060	A	19950505	CA 2134060	A	19941021	199531
JP 7182209	A	19950721	JP 94268813	A	19941101	199538
US 5485574	A	19960116	US 93147645	A	19931104	199609
EP 652518	B1	20000105	EP 94117366	A	19941103	200006
DE 69422476	E	20000210	DE 622476	A	19941103	200015
			EP 94117366	A	19941103	
CA 2134060	C	20010102	CA 2134060	A	19941021	200104

Priority Applications (No Type Date): US 93147645 A 19931104

## **Patent Details:**

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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EP 652518	A1	E	12	G06F-011/34	
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Designated States (Regional): DE FR GB

CA 2134060	A			G06F-009/44	
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JP 7182209	A		8	G06F-011/34	
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US 5485574	A		10	G06F-011/34	
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EP 652518	B1	E		G06F-011/34	
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Designated States (Regional): DE FR GB

DE 69422476	E			G06F-011/34	Based on patent EP 652518
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CA 2134060	C	E		G06F-009/44	
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**Performance monitoring of programs on data processor - ...**

... holding **monitoring programme** for **user level or system** programmes within **kernel of operating system**

...Abstract (Basic): monitoring involves providing a facility in a kernel of an operating system for counting instructions **executed** by the **processor**. A **user level monitoring programme** monitors the instructions counted by the facility. A section of **code** is **executed** on the processor. The facility is used to count a number of instructions executed when the section of **code** is **executed**.

...

...The count value is reported to the **user level monitoring program**. The section of **code** which is **executed** is part of the kernel of the operating system, or part of the **user level programme**.

...Abstract (Equivalent): In a **data processing** system having a processor that runs at a system level or a user level when...

...providing a facility in the kernel of the operating system for counting instructions **executed** by the **processor** and a **user level monitoring program** for monitoring the instructions counted by the facility...

... **executing** a section of **code** on the **processor** without recompiling the section of code to embellish the code for profiling...

...of the operating system to count the number of instructions executed when the section of **code** on the **processor**; and...



...reporting the count of the number of instructions **executed** to the  
**user level monitoring program** .

International Patent Class (Main): **G06F-009/44** ...

... **G06F-011/34**

International Patent Class (Additional): **G06F-009/06**

**12/3,K/14** (Item 8 from file: 350)  
DIALOG(R)File 350:Derwent WPIX  
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009270972 \*\*Image available\*\*  
WPI Acc No: 1992-398384/199248  
XRPX Acc No: N92-303959

**Computer system with file access security function - reads out operator  
profile information according to user ID and reads out environment  
profile and access protection information when program and access  
requested**

Patent Assignee: TOSHIBA KK (TOKE )

Inventor: ORITA Y

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5163147	A	19921110	US 90575439	A	19900830	199248 B

Priority Applications (No Type Date): JP 89225402 A 19890831

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 5163147	A	9	G06F-012/14	

...Abstract (Basic): a security function, environment profile information  
defining a file to be accessed and an executable **user program** are  
previously **stored** into a storage unit. The environment profile  
information is selected by operator profile information corresponding  
to ID information input via a work station by a user. A host computer  
**executes** the **user program** defined by the environment profile  
information...

...When a specified file access is requested after the execution of the  
**user program** , whether **execution** of the **file access** is permitted  
or not is determined according to access protection information. The  
access protection...

International Patent Class (Main): **G06F-012/14**

**12/3,K/15** (Item 9 from file: 350)  
DIALOG(R)File 350:Derwent WPIX  
(c) 2004 Thomson Derwent. All rts. reserv.

008490539 \*\*Image available\*\*  
WPI Acc No: 1990-377539/199051  
XRPX Acc No: N90-287735

**Portable electronic device with versatile program storage - has on-line  
mode with device connected to external appts. and off-line mode device is  
solely used**

Patent Assignee: TOSHIBA KK (TOKE )

Inventor: NARA S

Number of Countries: 003 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 402759	A	19901219	EP 90110739	A	19900606	199051 B
US 5168151	A	19921201	US 90533880	A	19900606	199251
EP 402759	B1	19941012	EP 90110739	A	19900606	199439
DE 69013233	E	19941117	DE 613233	A	19900606	199445
			EP 90110739	A	19900606	

Priority Applications (No Type Date): JP 89148994 A 19890612

Patent Details:

Patent No	Kind	Lang	Pg	Main IPC	Filing Notes
EP 402759	A				
					Designated States (Regional): DE FR
US 5168151	A		10	G06F-015/21	
EP 402759	B1	E	12	G07F-007/10	
					Designated States (Regional): DE FR
DE 69013233	E			G07F-007/10	Based on patent EP 402759

...Abstract (Basic): The device comprises a **data processing** unit which performs basic processing of the device and **user application processing**. A first nonrewritable memory stores a program for executing the basic processing. A second rewritable memory **stores a program for executing user application processing**.

...

...The **data processing** unit enables read/write access to the second memory in the on-line mode, and disables it in

...Abstract (Equivalent): and has an off-line mode in which the device is solely used, comprising: a **data processing** unit (28) having a function of basic processing for the device and a function of **user application processing**; nonrewritable first memory means (31c) for storing a program for executing the basic processing; and rewritable second memory means (31a,31b) for **storing a program for executing a user application processing**; characterised by control means (40) for permitting said **data processing** unit to enable read/write access to the second memory means in the on-line...

...control means (40) including: comparison means (51,52) for comparing a start address of the **user application processing program**, stored in said second memory means, and an address output from said **data processing** unit, and for, when a coincidence between the two addresses is detected, outputting a coincidence...

...output and said IC card is powered by a built-in battery (25), disabling said **data processing** unit from accessing said second memory means, and for, when the coincidence signal is not output or said IC card is powered by said external apparatus, enabling said **data processing** unit to access said second memory means...

...Abstract (Equivalent): memory area for storing a basic function, and a rewritable second memory area capable of **storing a user application program**. The IC card includes a circuit for switching between enabling and disabling accesses to the...

International Patent Class (Main): **G06F-015/21** ...

DIALOG(R)File 350:Derwent WPIX  
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008029050     \*\*Image available\*\*

WPI Acc No: 1989-294162/198941

XRPX Acc No: N89-224385

**Massively distributed high speed logic simulating engine - employs hierarchical logic architecture each level operating in parallel with exchange of data through switch units**

Patent Assignee: IBM CORP (IBMC )

Inventor: BEECE D K; DENNEAU M M; HOCHSCHILD P H; RAPPAPORT A; TREMPER C A

Number of Countries: 005 Number of Patents: 003

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 336243	A	19891011	EP 89105325	A	19890322	198941 B
US 4914612	A	19900403	US 88175879	A	19880331	199019
EP 336243	A3	19930120	EP 89105325	A	19890322	199346

Priority Applications (No Type Date): US 88175879 A 19880331

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes

EP 336243 A E 27

Designated States (Regional): DE FR GB IT

...Abstract (Basic): simulation engine comprises several levels of hierarchy. At the lowest level is a logic chip **storing** a sequentially **executed program of operators** and operand addresses, referring to an input memory of the chip. Next level comprises a...

...of logic circuits by large number of parallel, specialised processing units, transmitting large amounts of **data** between **processors** .

...Abstract (Equivalent): lowest level is a logic chip which has stored in an instruction memory a sequentially **executed program** of logical **operators** and operand addresses. The operand addresses refer to an input memory of the chip. The...

International Patent Class (Additional): G06F-009/02 ...

... G06F-013/20 ...

... G06F-015/60

12/3,K/17     (Item 11 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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007842885

WPI Acc No: 1989-107997/198915

XRPX Acc No: N89-082358

**Computer-based controller for multiple processors of machine - uses function chart data, macro instructions and instructions giving ladder programs**

Patent Assignee: ALLEN BRADLEY CO (ALLB )

Inventor: FLOOD M A; KALAN M D; PETERSON A L; PREIS P N

Number of Countries: 006 Number of Patents: 005

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 311007	A	19890412	EP 88116390	A	19881004	198915 B

US 4937777	A	19900626	US 87105815	A	19871007	199028
BR 8901253	A	19901016				199046 N
CA 1310387	C	19921117	CA 573157	A	19880727	199252
EP 311007	B1	19940309	EP 88116390	A	19881004	199410

Priority Applications (No Type Date): US 87105815 A 19871007

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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EP 311007	A	E	41		
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Designated States (Regional): DE FR IT

EP 311007	B1	E	42	G05B-019/04
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Designated States (Regional): FR

CA 1310387	C		G05B-019/04
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...Abstract (Equivalent): a plurality of processors (18) connected to the back plane, each processor capable of simultaneously **executing** a different **user** provided control **program** and having a memory (106) which **stores** **user** provided control **programs** for execution on said **processor** and **user** provided **program** **execution** sequence **data** (313) comprising a plurality of descriptors (430, 432, 434, 436) each descriptor identifying a **user** provided control **program**, a transition condition which occurs when the execution of the identified control program should terminate, and the next **user** provided control **program** to be **executed** and which processor is to **execute** the next **user** control **program**, each of said processors (18) having a means (94, 95, 96) to transmit program execution...

...Abstract (Equivalent): The programmable controller includes several program processors. Each of the program processors is operable to **execute** simultaneously a different **user** control **program** that directs the operation of the operation of the machine to perform specific functions. Each of the program processors includes a memory for **storing** the **user** control **programs** and function chart data. The function chart data comprises a series of descriptor files each of which contain an identification of a **user** control **program** to **execute**, a transition condition that indicates when the execution of that **user** control **program** is to terminate, and which descriptor **file** is to be **processed** next as well as the program processors to process it...

International Patent Class (Additional): G06F-015/02

12/3,K/18 (Item 12 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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007797454 \*\*Image available\*\*

WPI Acc No: 1989-062566/198909

XRPX Acc No: N89-047751

**Programmable controller for multiple machine processors - has processors operating separately but guided overall by controller assigning tasks**

Patent Assignee: ALLEN BRADLEY CO (ALLB )

Inventor: GALDUN D L; IMMORMINO F R; RISCHAR C M; STEWART D L

Number of Countries: 006 Number of Patents: 007

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 304880	A	19890301	EP 88113732	A	19880824	198909 B
US 4858101	A	19890815	US 8789587	A	19870826	198941
BR 8901348	A	19901023				199047 N

CA 1284370	C	19910521			199125
EP 304880	A3	19920923	EP 88113732	A	19880824 199339
EP 304880	B1	19951018	EP 88113732	A	19880824 199546
DE 3854594	G	19951123	DE 3854594	A	19880824 199601
			EP 88113732	A	19880824

Priority Applications (No Type Date): US 8789587 A 19870826

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
EP 304880	A	E	53		
Designated States (Regional): DE GB IT					
US 4858101	A		47		
EP 304880	B1	E	54	G05B-019/05	
Designated States (Regional): DE GB IT					
DE 3854594	G			G05B-019/05	Based on patent EP 304880

...Abstract (Equivalent): by a plurality of processors (18) connected to the backplane, each processor capable of simultaneously **executing** a different **user** provided control **program** and having a memory (106) which **stores** **user** provided control **programs** for execution on said **processor** and **user** provided **program** **execution** sequence **data** (313) comprising a plurality of descriptors (430, 432, 434, 436), each descriptor identifying a **user** provided control **program**, a transition condition which occurs when the execution of the identified control program should terminate, and the next **user** provided control **program** to be **executed** and which processor is to **execute** the next **user** control **program**, each of said processors (18) having a means (94, 95, 96) to transmit program execution...

...Abstract (Equivalent): functions includes a number of program processors. Each of the program processors are operable to **execute** simultaneously a different **user** control **program** that directs the operation of the machine to perform specific functions. Each of the processor is contained within a separate module which also includes a memory for **storing** the **user** control **programs** that are to be executed by that processor means. A mechanism is also provided to control the sequence in which the **user** control **programs** are **executed** and which of the processor means executes a given control program...

...circuit controls the gathering of data from various external sensors and in response to output **data** received from the **processor** means, controls the operation of actuator devices on the machine. The input/output data regarding...

...International Patent Class (Additional): G06F-009/46

12/3,K/19 (Item 13 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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007542993 \*\*Image available\*\*

WPI Acc No: 1988-176925/198826

XRPX Acc No: N88-135203

**Controlled dynamic load balancing for multiprocessor system - inserting allocation parameters into program object code file to adjust assignment function**

Patent Assignee: AMERICAN TELEPHONE & TELEGRAPH CO (AMTT ); AT & T CO (AMTT ); AT & T BELL LAB (AMTT )

Inventor: BISHOP T P; DAVIS M H; PETERSON J S; SURRATT G T; PETERSON S J  
Number of Countries: 011 Number of Patents: 007

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 272836	A	19880629	EP 87310797	A	19871209	198826 B
BR 8706965	A	19880726				198835
CA 1296432	C	19920225				199214
US 5115505	A	19920519	US 86941701	A	19861222	199223
			US 90545679	A	19900628	
EP 272836	B1	19940302	EP 87310797	A	19871209	199409
DE 3789215	G	19940407	DE 3789215	A	19871209	199415
			EP 87310797	A	19871209	
KR 9708524	B1	19970524	KR 8714588	A	19871221	199943

Priority Applications (No Type Date): US 86941701 A 19861222; US 90545679 A 19900628

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
EP 272836	A	E	20		
				Designated States (Regional):	BE DE FR GB IT NL SE
US 5115505	A		18	G06F-009/46	Cont of application US 86941701
EP 272836	B1	E	20	G06F-009/46	
				Designated States (Regional):	BE DE FR GB IT NL SE
DE 3789215	G			G06F-009/46	Based on patent EP 272836
KR 9708524	B1			G06F-015/16	

...Abstract (Basic): inserts a set of processor allocation parameters into the header portion of the stored object **code file** which is **read**. One of the processors is allocated in response to the parameters in the header portion of the **file** to **execute** the **code** portion of the **file**. The **processor** allocation parameters are written into the header portion in response to a **user** command. System **processor** allocation flags are **stored**. The system flags are checked to determine if a specific processor arrangement is allowed...

...Abstract (Equivalent): of processor resources in a multiprocessor system having a plurality of processors one of said **processors** storing object **code file** for a new program and said object code file having a header and code portions...

...user of said multiprocessor system after compilation of said object code program, means (206) for **reading** said stored object **code file**; and means (207, 208, 209) for allocating one of said processors in response to said processor allocation parameters in said header portion of the **read object code** to **execute** said **code** portion of said **read object code**.

...Abstract (Equivalent): The system administrator controls the assignment function by defining certain system variables and flags. The **application programmer** can adjust the assignment function by causing allocation parameters to be passed in a system call before execution of the assignment function. To adjust the assignment function, the **program user executes** a system command that inserts similar allocation parameters into the program object code file stored...

...allocation parameters and performs the assignment function as it has been adjusted on a system, **program** or **user** level basis. USE - A method for allowing a system administrator, **application programmer**, and/or **program user** to adjust the **processor** assignment function

in a multiprocessor system.  
International Patent Class (Main): G06F-009/46 ...

... G06F-015/16  
International Patent Class (Additional): G06F-015/16

12/3,K/20 (Item 14 from file: 350)  
DIALOG(R)File 350:Derwent WPIX  
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007043031  
WPI Acc No: 1987-043028/198706  
XRPX Acc No: N87-032801

**Programmable controller with programmable real time interrupt interval -  
has intervals between interrupts determined by instructions within user  
control program for dynamic alteration**

Patent Assignee: ALLEN BRADLEY CO (ALLB )  
Inventor: JOHNSTON D A; SCHMIDT O; SCHULTZ R E  
Number of Countries: 001 Number of Patents: 001  
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 4638452	A	19870120	US 84584128	A	19840227	198706 B

Priority Applications (No Type Date): US 84584128 A 19840227

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 4638452	A	17		

... **has intervals between interrupts determined by instructions within  
user control program for dynamic alteration**

...Abstract (Basic): The programmable controller comprises: a memory  
**storing a user control program** with a set of control program  
instructions and storing a user interrupt routine with a...

...a real time interrupt interval number. A main processor coupled to the  
memory and the **data** table repeatedly **executes the user control  
program** to operate a machine connected to the programmable controller.  
The processor writes a real time interrupt interval number of the **data**  
table when the **processor** executes selected ones of the control  
program instructions...

...A real time clock is coupled to the main **processor** and the **data**  
table to interrupt the execution of the **user control program** at  
time intervals determined by the value of the stored real time  
interrupt interval number. The main **processor** **executes the user**  
interrupt routine each time the **user control program** is  
interrupted. The real time clock includes an RTI in progress device for  
establishing the length of time required by the main **processor** to  
**execute the user** interrupt routine after it is interrupted by the  
real time clock.

International Patent Class (Additional): G06F-015/18

12/3,K/21 (Item 15 from file: 350)  
DIALOG(R)File 350:Derwent WPIX  
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004180478

WPI Acc No: 1985-007358/198502

XRPX Acc No: N85-005094

**Programmable control system - has special bus for fetching next word during execution of present instruction**

Patent Assignee: SIEMENS AG (SIEI )

Inventor: WOLLSCHIED D

Number of Countries: 012 Number of Patents: 007

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
DE 3323824	A	19850103	DE 3323824	A	19830701	198502 B
EP 130269	A	19850109	EP 84100401	A	19840116	198503
ES 8500473	A	19850101				198510
NO 8304728	A	19850128				198511
US 4592010	A	19860527	US 84568107	A	19840104	198624
EP 130269	B	19880525				198821
DE 3471534	G	19880630				198827

Priority Applications (No Type Date): DE 3323824 A 19830701

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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DE 3323824	A		12		
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EP 130269	A	G			
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Designated States (Regional): AT BE CH DE FR GB IT LI SE

EP 130269	B	G			
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Designated States (Regional): AT BE CH DE FR GB IT LI SE

...Abstract (Equivalent): A store-programmable control unit with a cyclically **executed user programme**, comprising at least: a) a word processor with word processor bus for the processing of...

...word commands, b) a bit processor for the processing of binary logic-linking commands, c) **user programme store**, operating system **store** and **process** bit map **data** store, characterised in that d) simultaneously to the processing of an existing binary logic-linking...

...bus (32) via another separate bus (31) to bit processor (3) continuously acquires from the **user programme store** (5) the words assigned to the next command of the **user programme** and assembles the next command which it makes available immediately following the execution of the...

...Abstract (Equivalent): The command-fetching device (11) continuously loads **user program** memory words according to control commands (15) from a **user program** memory (5) via a data switch (8) and bus (31) into the command memory and...

...data on which the command operates, is accessed via a second bus (32) and a **data processor** (14...

International Patent Class (Additional): G06F-009/06 ...

... G06F-013/06 ...

... G06F-015/46

12/3,K/22 (Item 16 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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003758183

WPI Acc No: 1983-754394/198336

XRPX Acc No: N83-156243

**Distributed data processor with shaped common data files - has user station CPU, under application program control, connected to data centre CPU, under access program control**

Patent Assignee: BILLINGS COMPUTER CORP (BILL-N); BILLINGS R E (BILL-I)

Inventor: BILLINGS R E

Number of Countries: 015 Number of Patents: 006

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 87143	A	19830831				198336 B
AU 8311679	A	19830825				198341
BR 8300828	A	19831116				198402
ZA 8301146	A	19840604				198439
CA 1198826	A	19851231				198606
US 4714989	A	19871222	US 86921219	A	19861020	198801

Priority Applications (No Type Date): US 82350159 A 19820219; US 86826721 A 19860206

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
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EP 87143	A	E 30		
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Designated States (Regional): AT BE CH DE FR GB IT LI NL SE

**Distributed data processor with shaped common data files...**

**...has user station CPU, under application program control, connected to data centre CPU, under access program control**

**...Abstract (Basic): CPU and a mass storage system. An access control program operates the second CPU. Each user station has an application program to run its CPU. The program allows the user station to receive data from a user and supply him with data. The program also...**

**...Abstract (Equivalent): The functionally structured distributed data processing system includes a number of independently operating user station process -ors for servicing users , a data centre for storing data to be processed by the user stations, and a communication network for coupling each user station to one or more data...**

**...Each user station executes application programmes to which is linked a data base simulator which formulates requests or data operations to...**

International Patent Class (Additional): G06F-003/04 ...

... G06F-013/00 ...

... G06F-015/16